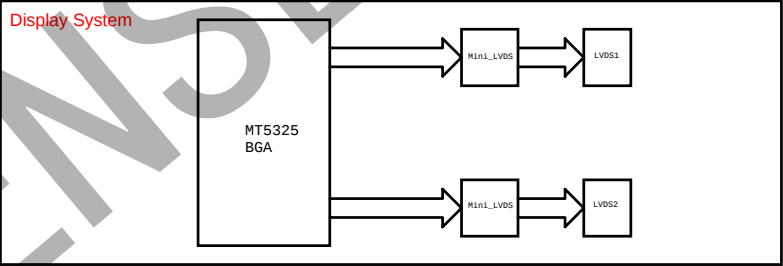
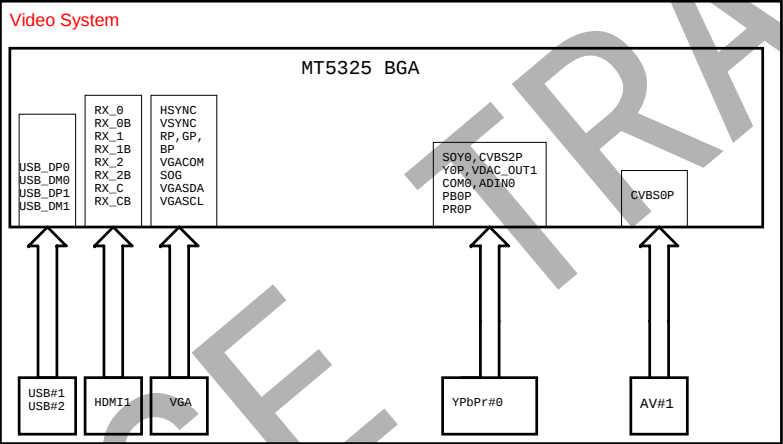
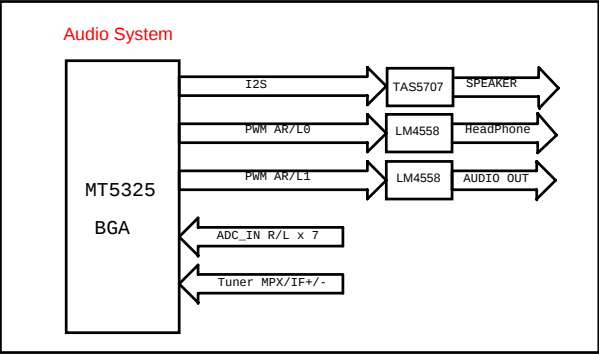
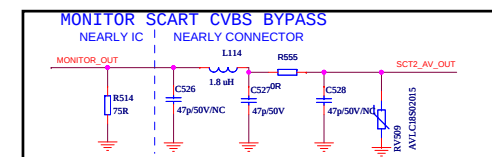
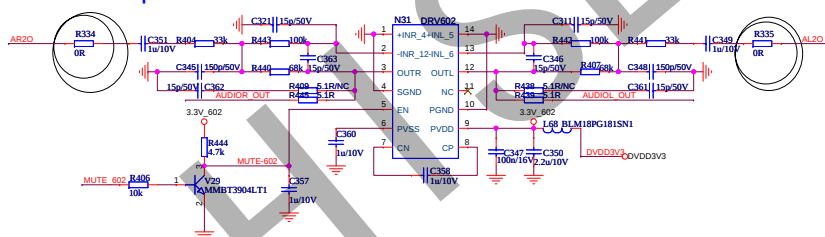
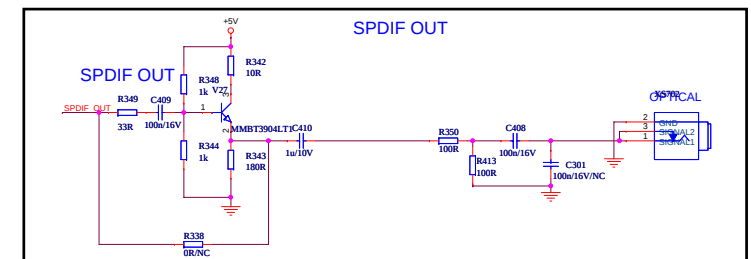
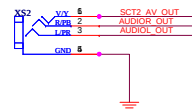
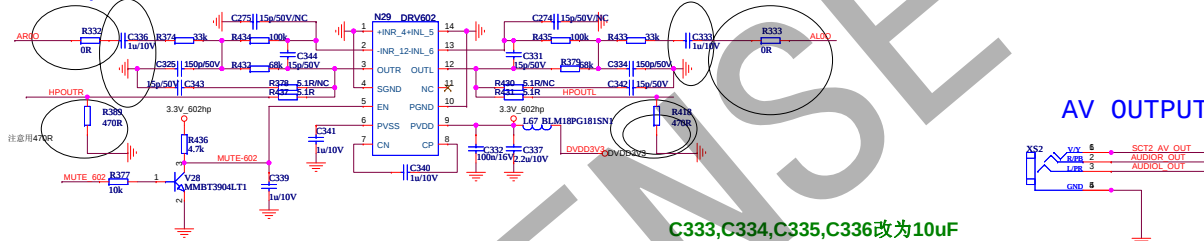
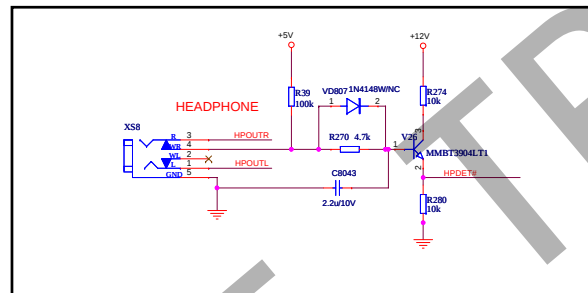
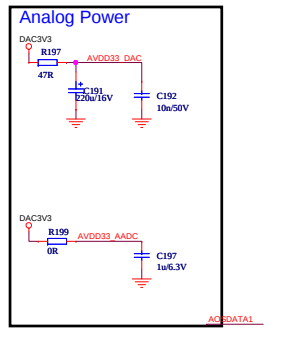
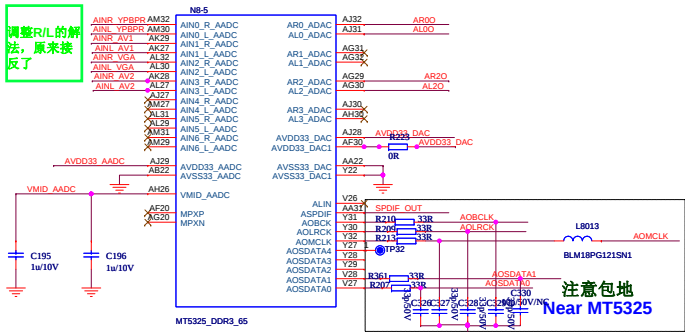


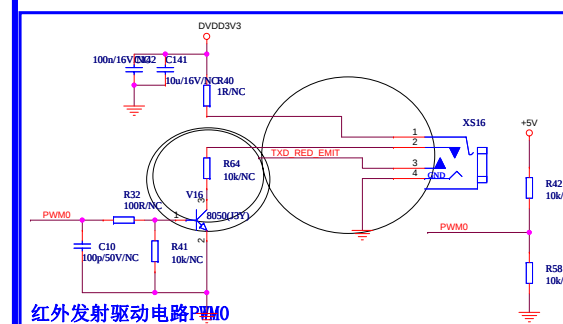
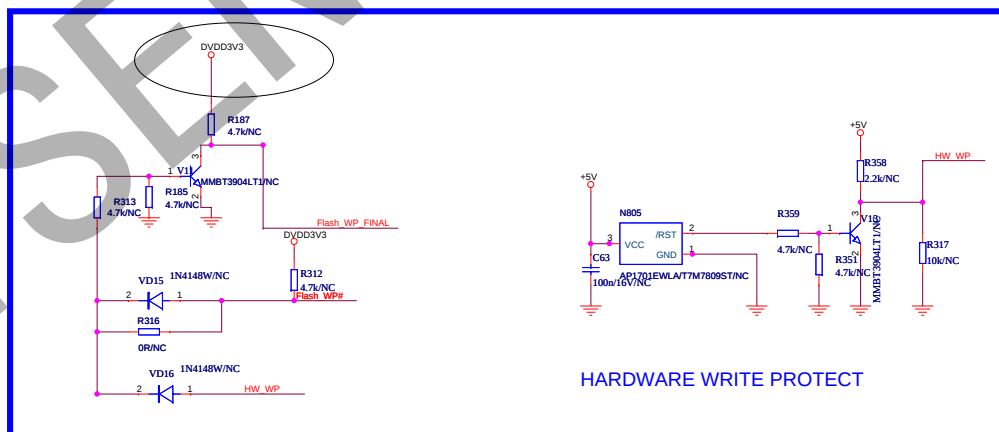
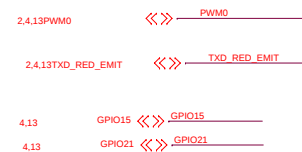
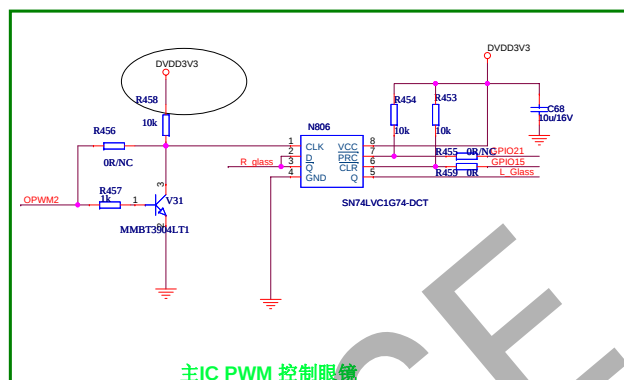
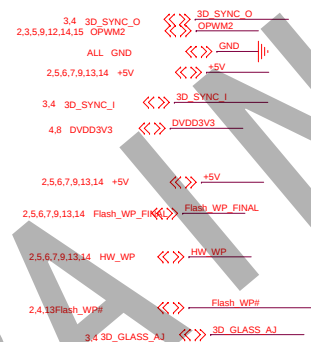
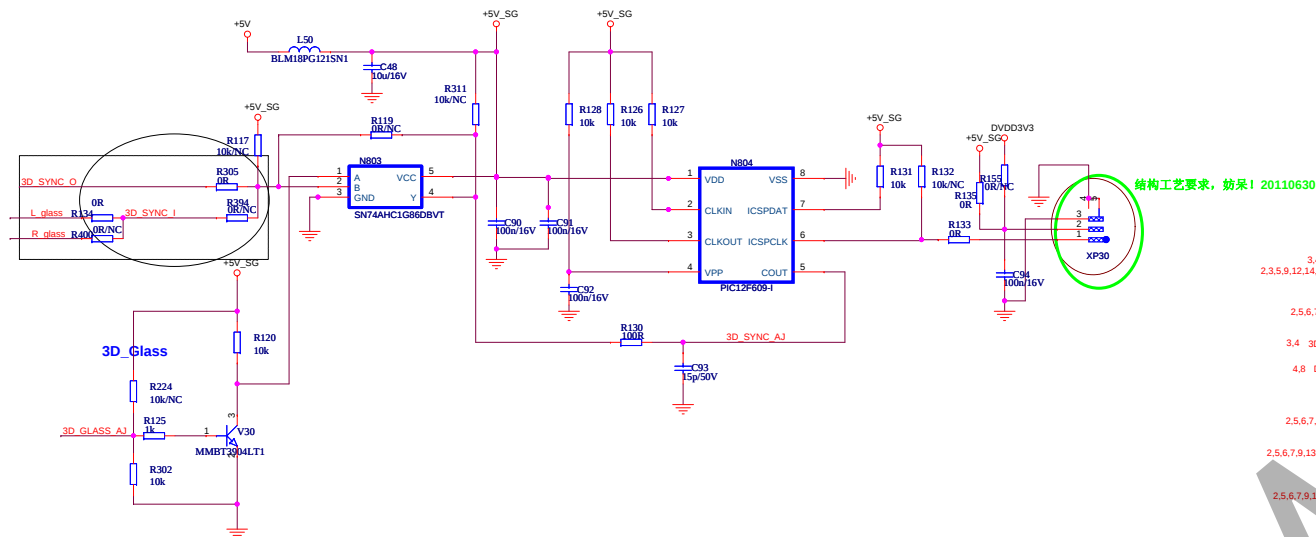
MT5325

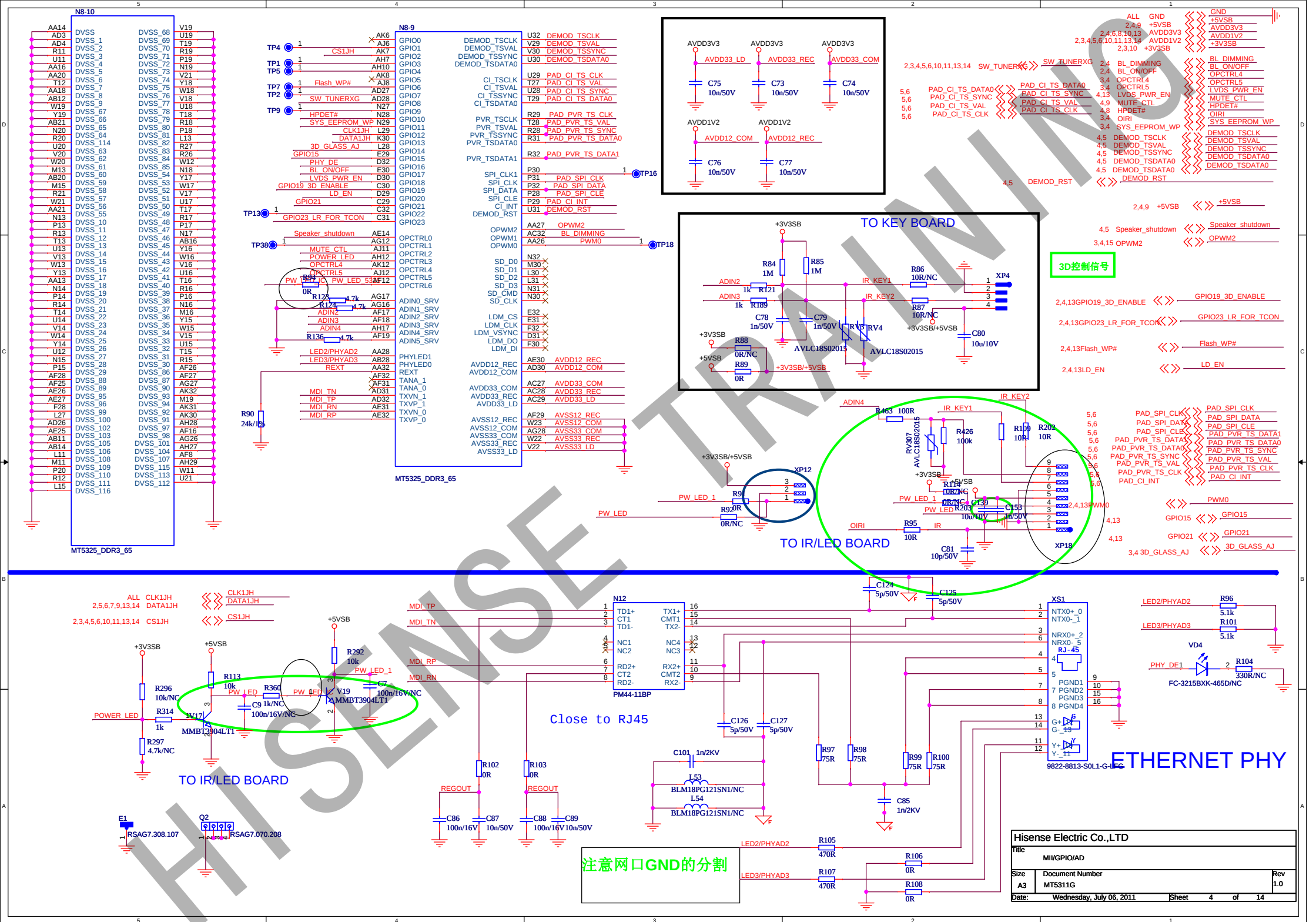
5325 GPIO Definition

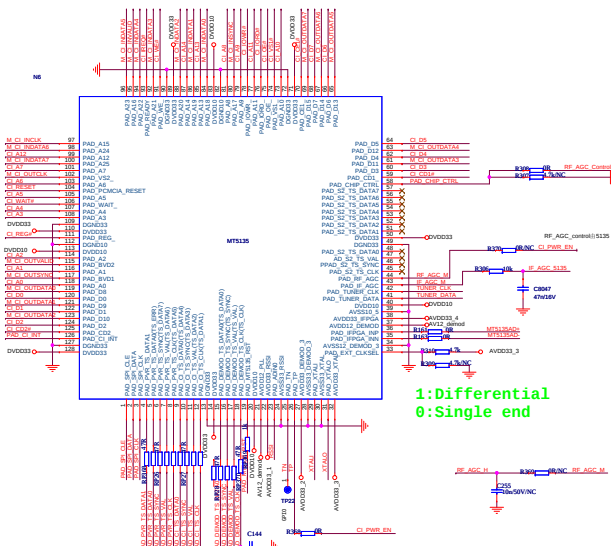
PIN NAME	NET NAME	Function define
GPIO_0		USB_PWR_EN0
GPIO_1		
GPIO_2		USB_PWR_EN1
GPIO_3		
GPIO_4		
GPIO_5		
GPIO_6		
GPIO_7		
GPIO_8		
GPIO_9		
GPIO_10		HPDET#
GPIO_11		SYS_EEPROM_WP
GPIO_12		
GPIO_13		
GPIO_14		
GPIO_15		
GPIO_16		PHY_DE
GPIO_17		BL_ON/OFF
GPIO_18		LVDS_PWR_EN
GPIO_19		
GPIO_20		
GPIO_21		
GPIO_22		
GPIO_23		
ADIN0		
ADIN1		
ADIN2		
ADIN3		Key PAD 1
ADIN4		Light-Sensor
ADIN5		USB_OC_COM
OPCTRL0		
OPCTRL1		
OPCTRL2		MUTE_CTL
OPCTRL3		POWER_LED
OPCTRL4		strap[0]
OPCTRL5		strap[1]
OPCTRL6		
OPWM0		
OPWM1		BL_DIMMING
OPWM2		
ASPDIF		strap[2]



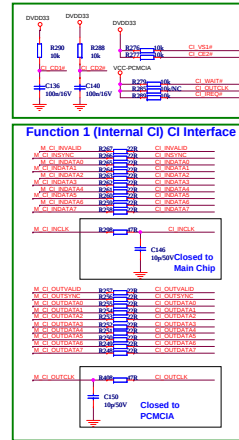




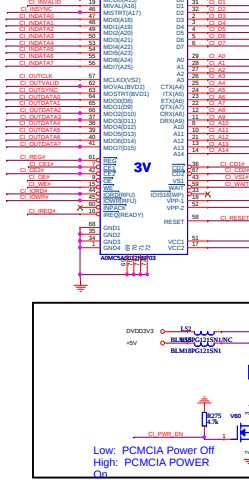




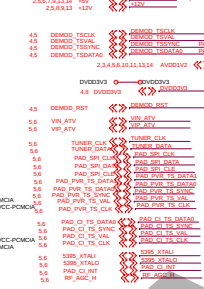
CI interface



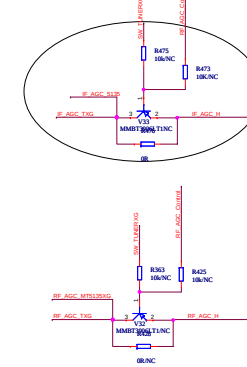
3V3



23A5A10111314

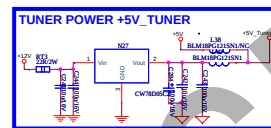
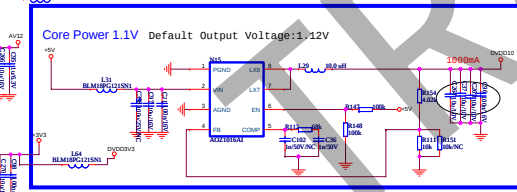
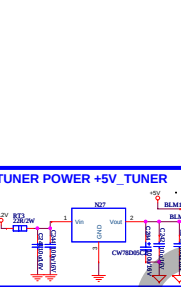


Analog test circuit



Reserved power circuit

ANALOG POWER AVDD1V2

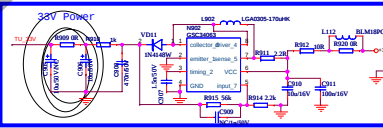


MT5311/MT5325
R4, R5= 60ohm,
R9, R27=50ohm,
Y1=NC

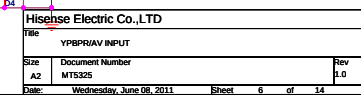
Tuner 请以分地

Nearly IC

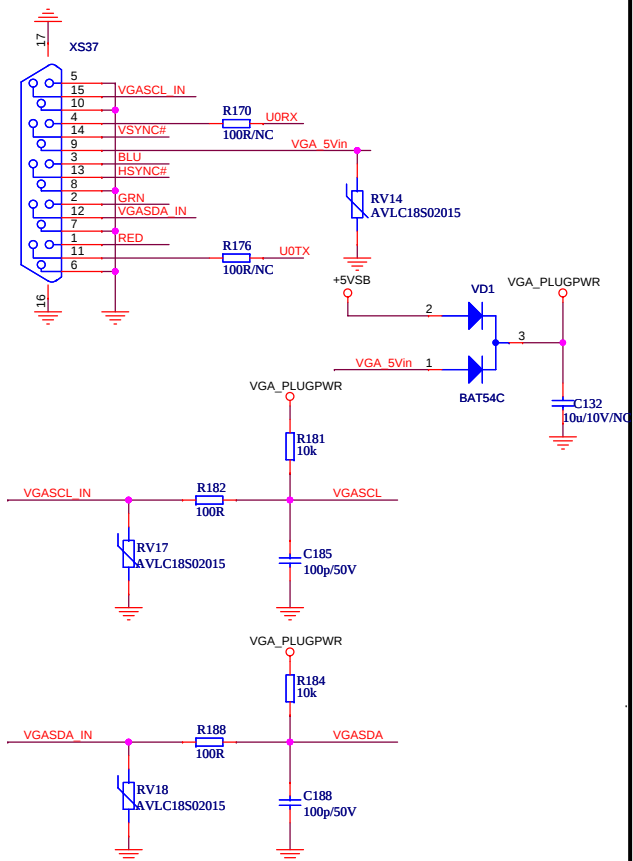
NOTE: custom BPF circuit design by different tuner



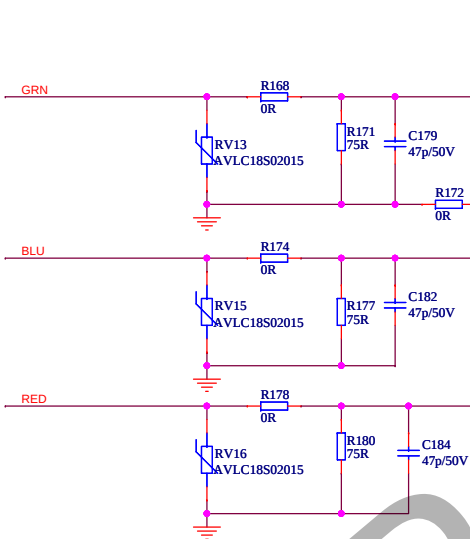
layout 注意
Tuner 割地



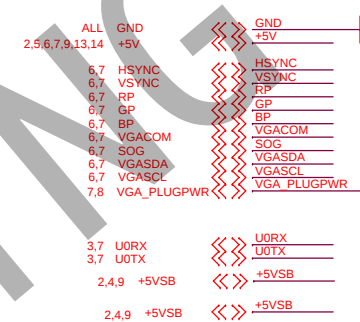
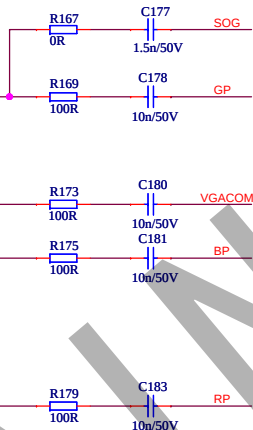
VGA INPUT



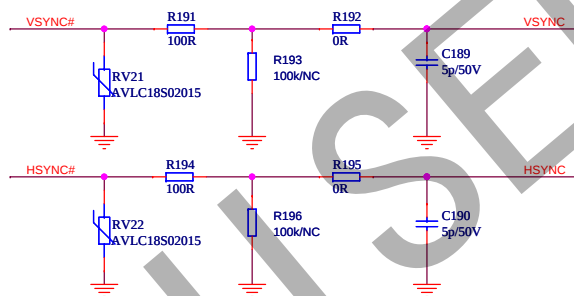
Close to VGA CONN.



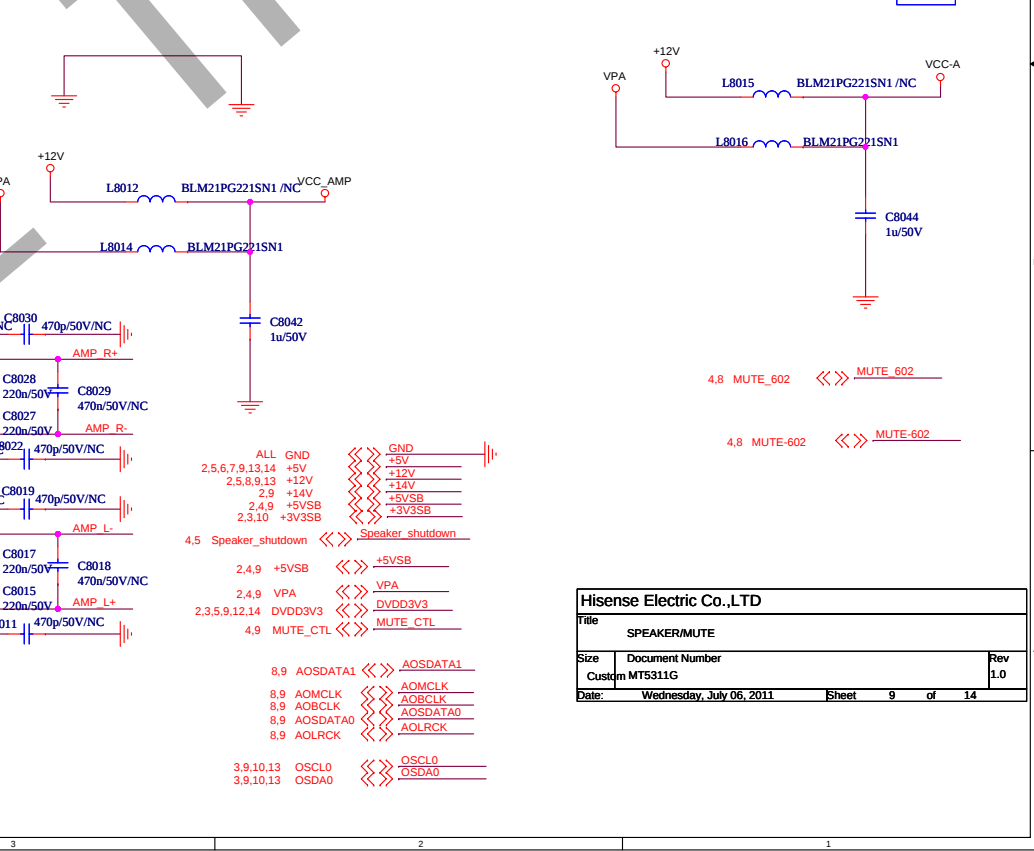
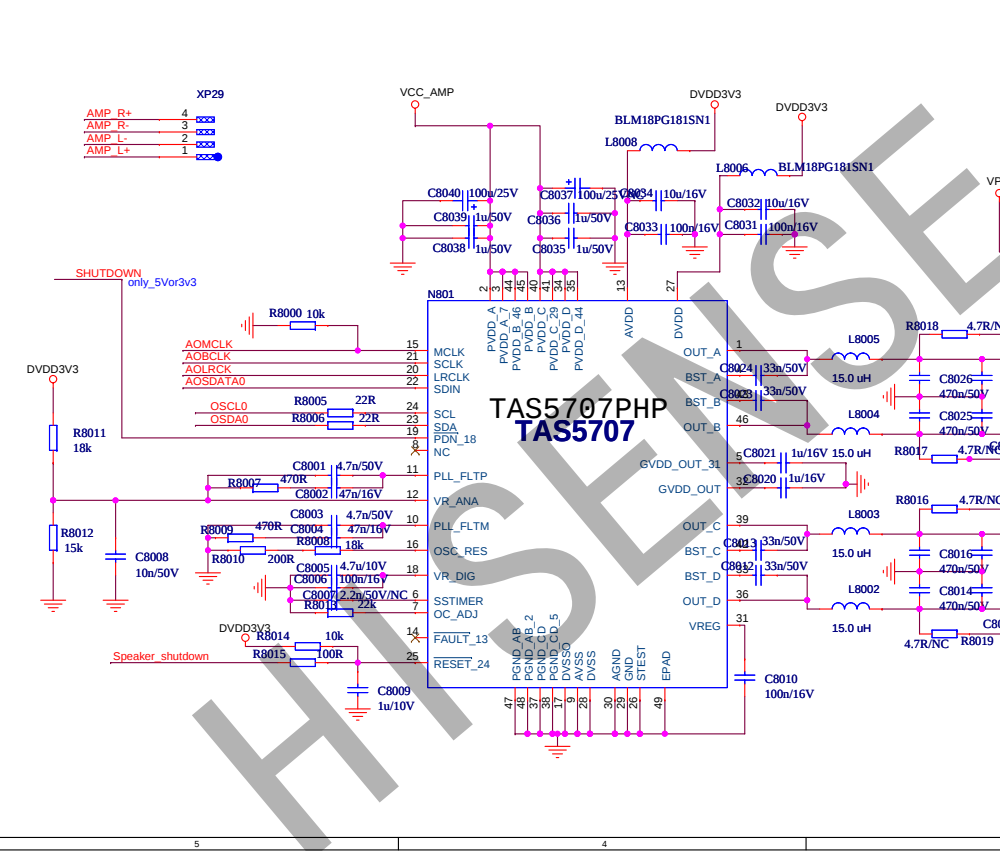
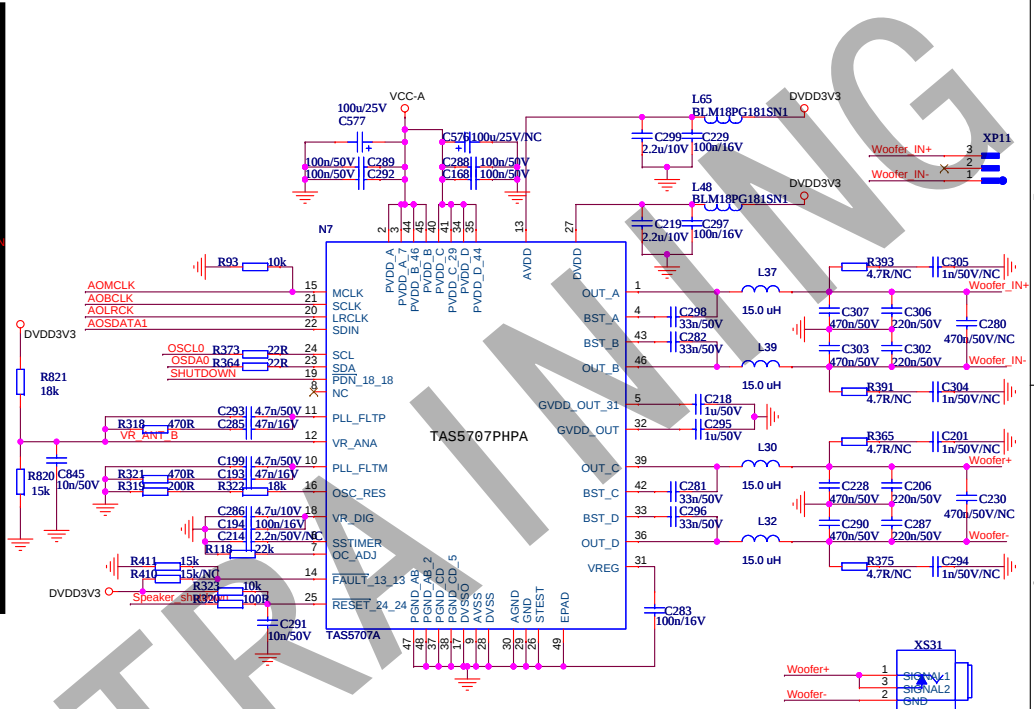
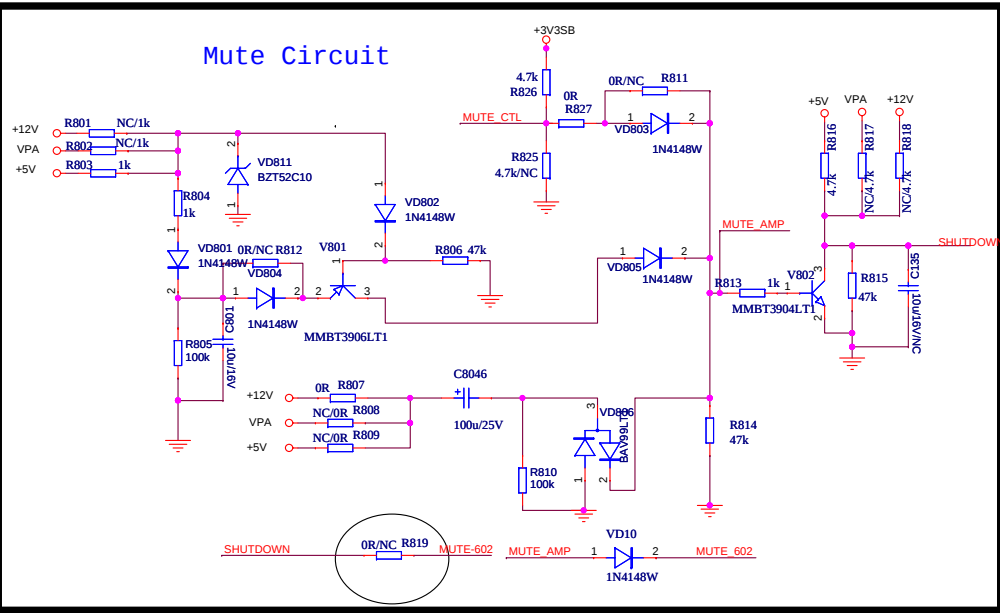
Close to MT5395



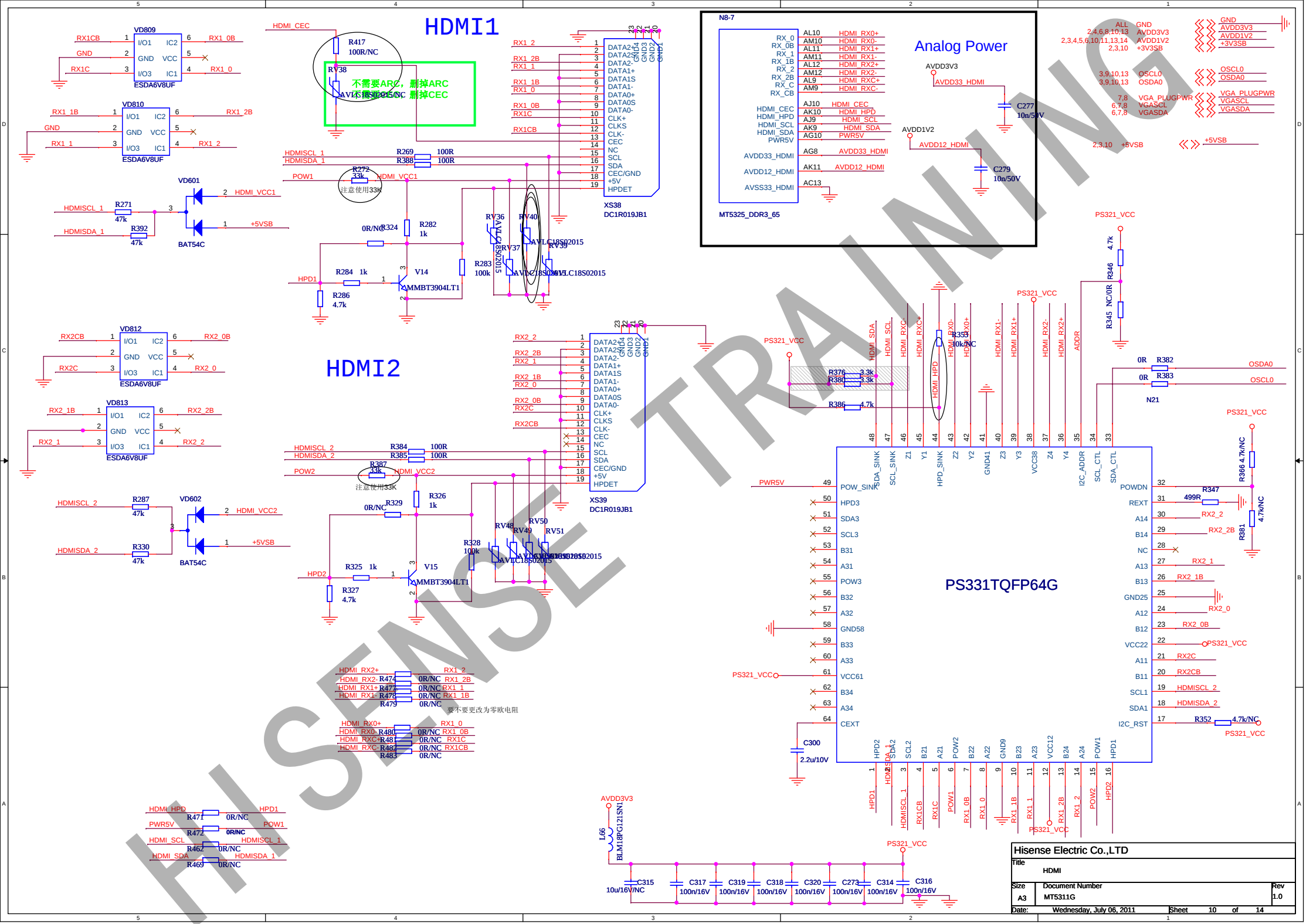
VGA SYNC SLICER

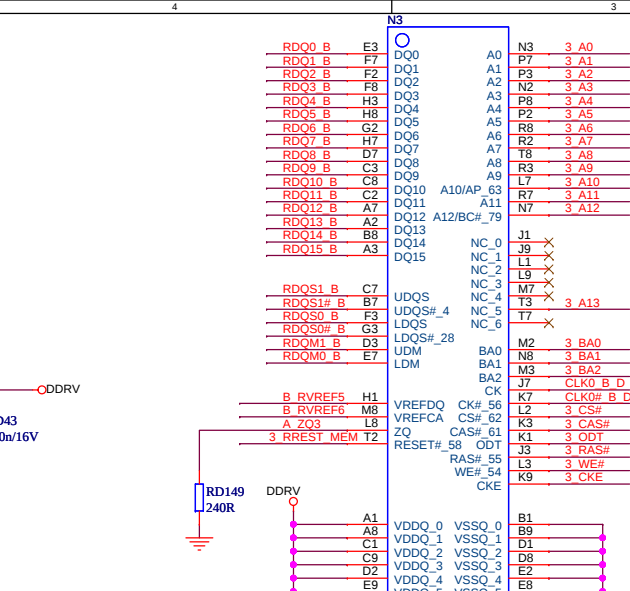


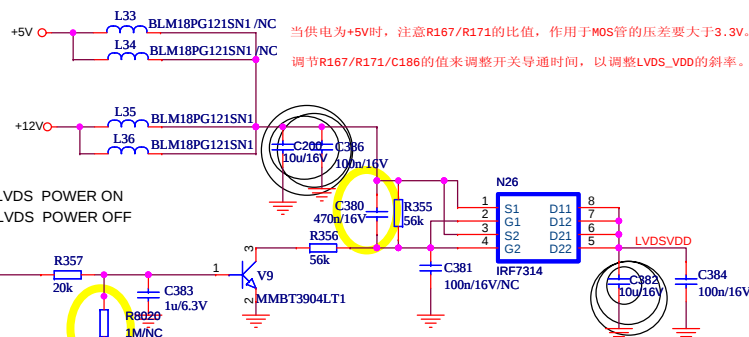
Mute Circuit



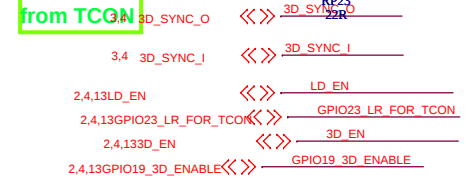
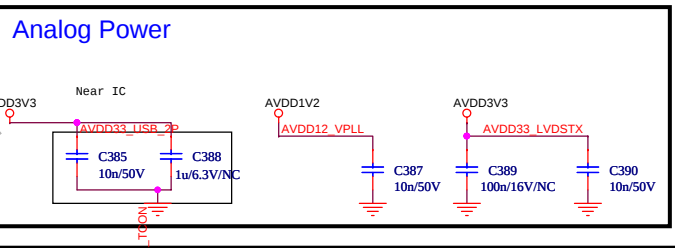
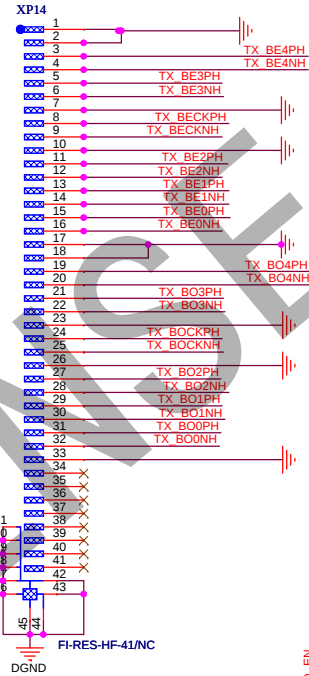
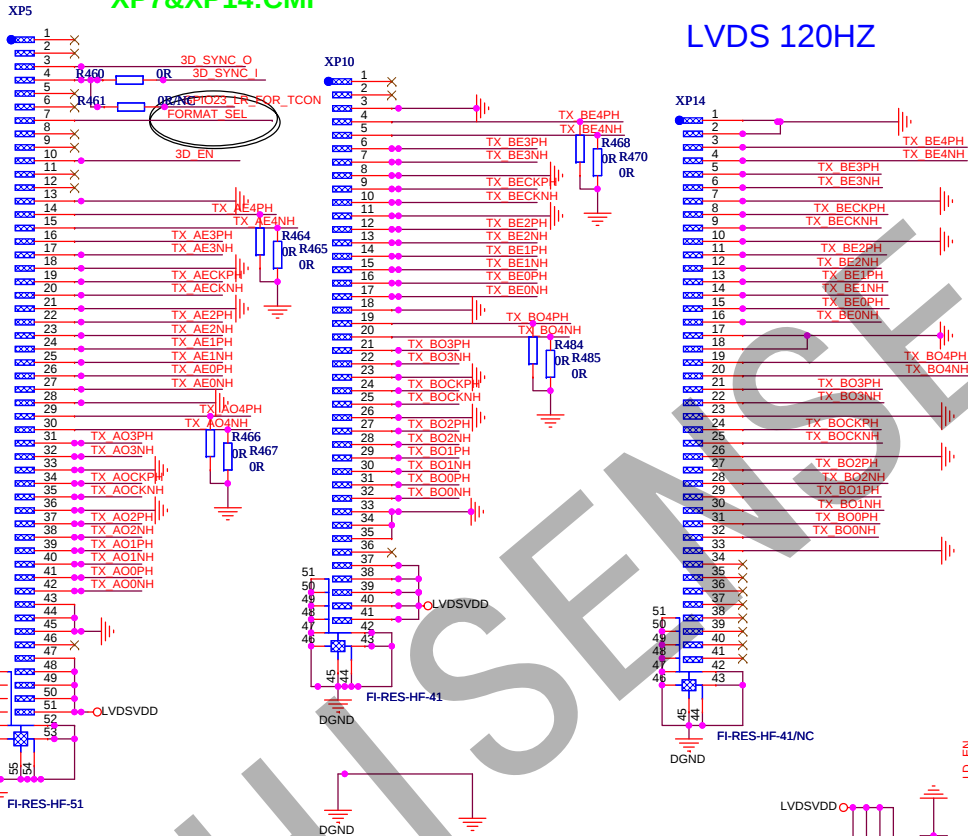
Hisense Electric Co.,LTD		
Title	SPEAKER/MUTE	
Size	Document Number	Rev
	Custom MT5311G	1.0
Date:	Wednesday, July 06, 2011	Sheet 9 of 14



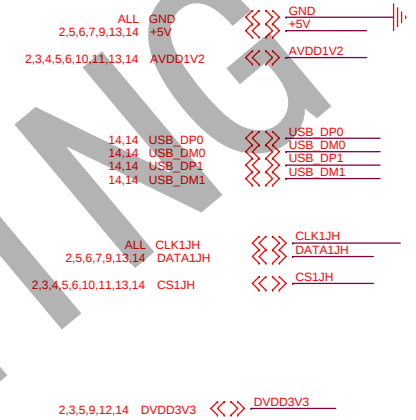
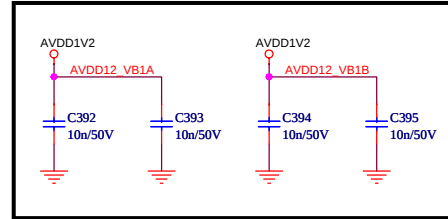
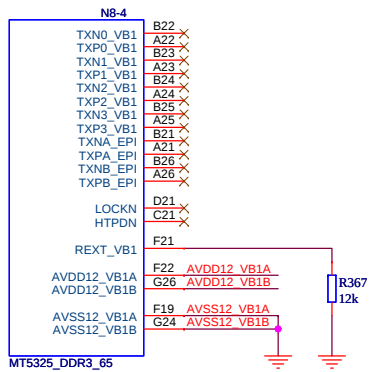




如果LVDS 是5V的供电，
注意调整R355/R356的
阻值

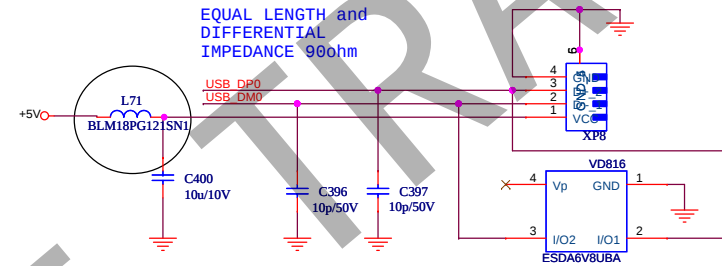


Hisense Electric Co.,LTD			
Title POWER AND INVERTOR			
Size A3	Document Number MT5311G	Rev 1.0	
Date:	Wednesday, July 06, 2011	Sheet	13 of 14



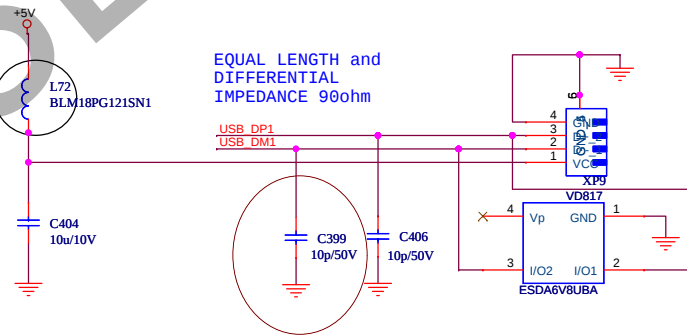
2 Ports of USB

USB PORT 1
Near Connector

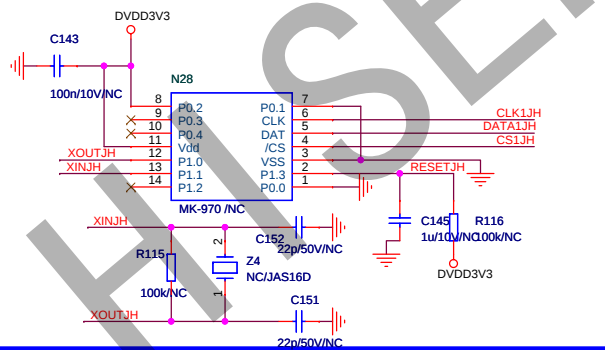


USB PORT 2

Near Connector



渐华游戏加密芯片MK-970



Hisense Electric Co.,LTD		
File	USB	
Size	Document Number	Rev
A3	MT5311G	1.0
Date:	Friday, June 03, 2011	Sheet 14 of 14