



# MIC5301/18 Evaluation Board

## Single $\mu$ Cap ULDO™

### General Description

The demonstration board can be used to evaluate both the MIC5301 and MIC5318 low dropout linear regulators. The MIC5301 and MIC5318 are offered in fixed and adjustable outputs. Fixed output versions add bypass pin functionality, allowing reduced output noise. The adjustable version allows for the use of a resistor divider network to adjust for the desired output voltage. The evaluation board accommodates the fixed version.

### Requirements

The MIC5301/18 requires a power supply that is able to deliver 300mA with an input voltage range of 2.3V to 6V. The output load can be either an active or passive load.

### Circuit Description

The MIC5301/18 single Ultra Low Dropout (ULDO™) linear regulator is easy to use. A minimum output capacitance of 1 $\mu$ F is required.

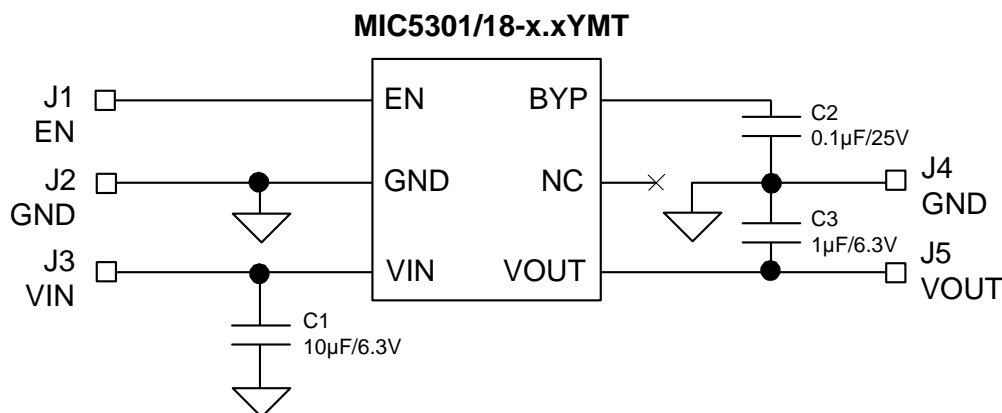
An input capacitor is required when the power supply is more than 4" away from the device. All evaluation boards include an input capacitor within 1" of the device. This is the minimum recommended distance from the device that the capacitor should be placed.

The evaluation board has a 10 $\mu$ F ceramic on the  $V_{IN}$  pin. Although this is much larger than required for the device, this was designed to allow for the long inductive test leads that will be attached to the evaluation board

Data sheets and support documentation can be found on Micrel's web site at: [www.micrel.com](http://www.micrel.com).

### Getting Started

1. **Connect an external supply to  $V_{IN}$  terminal.** Apply  $V_{IN}$  (J3) and ground (J2 and J4) terminals of the evaluation board, paying careful attention to polarity and supply voltage (2.3V to 6V). Be sure to monitor the supply voltage at the  $V_{IN}$  terminal.
2. **Enable the MIC5301/18.** Force the enable pin (J1) high to enable the output. Do not leave the enable pin floating. A floating enable pin may cause an indeterminate state on the output.
3. **Connect the load to  $V_{OUT}$  terminal (J5) and ground terminals (J4 or J2).** The load can be either passive (resistor) or active (electronic load). Be sure to monitor the output voltage at the  $V_{OUT}$  terminal.



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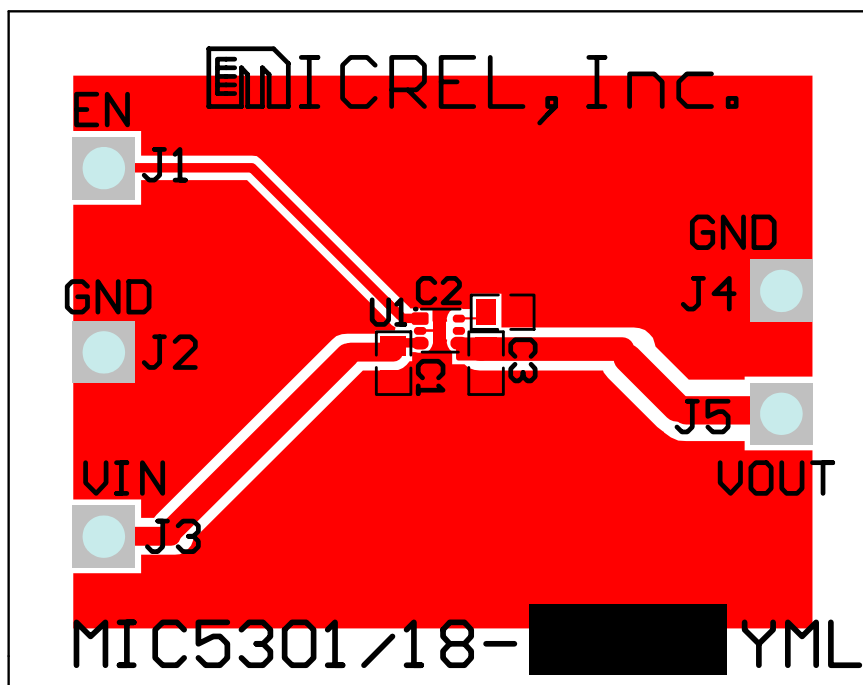
## Bill of Materials

Item	Part Number	Manufacturer	Description	Qty.
C1	C1608X5R0J106M	TDK <sup>(1)</sup>	10μF, ceramic capacitor, 0603, 6.3V	1
	GRM188R60J106K	muRata <sup>(2)</sup>		
	VJ0805G106KXYCW1BC	Vishay <sup>(3)</sup>		
	JMK107BJ106MA	Taiyo Yuden <sup>(4)</sup>		
	06036D106MAT2A	AVX <sup>(5)</sup>		
C2	C1608X5R1E104M	TDK <sup>(1)</sup>	0.1μF, ceramic capacitor, 0603, 6.3V	1
	GRM188R61E104KA01D	muRata <sup>(2)</sup>	0.1μF, ceramic capacitor, 0603, 25V	
	VJ0603Y104KXX	Vishay <sup>(3)</sup>		
C3	C1608X5R0J105M	TDK <sup>(1)</sup>	1μF, ceramic capacitor, 0603, 6.3V	1
	GRM188R60J105KE19D	muRata <sup>(2)</sup>		
	VJ0603G105KXYCW1BC	Vishay <sup>(3)</sup>		
U1	MIC5301YMT or MIC5318YMT	Micrel, Inc. <sup>(6)</sup>	ULDO with Single Output	1

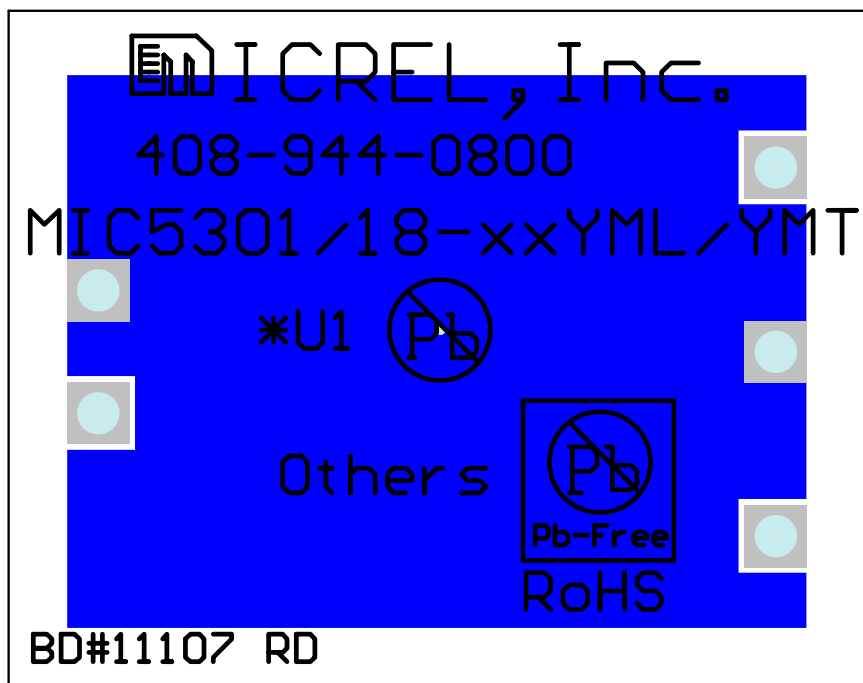
### Notes:

1. TDK: [www.tdk.com](http://www.tdk.com)
2. muRata: [www.murata.com](http://www.murata.com)
3. Vishay: [www.vishay.com](http://www.vishay.com)
4. Taiyo Yuden: [www.t-yuden.com](http://www.t-yuden.com)
5. AVX: : [www.avxcorp.com](http://www.avxcorp.com)
6. Micrel, Inc.: [www.micrel.com](http://www.micrel.com)

## PCB Layout



Top Layer



Bottom Layer

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