

256K x 16, 512K x 8 CMOS Mask Mask Programmable ROM

FEATURES

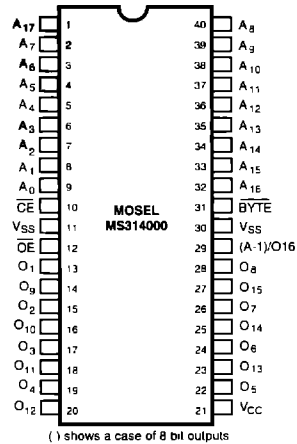
- Two organizations selectable by $\overline{\text{BYTE}}$ pin
- High Speed: 70ns (max)
Slow Speed: 200ns (max)
- Low Power operation:
 - Active 20mW @ 1 MHz
200mW @ 10 MHz
 - Standby 55μW (max) CMOS input level
- 3-volt operation - portable battery applications
- Short production turn-around time
- Fully static operation
- Automatic power-down ($\overline{\text{CE}}$)
- Address transition detection to eliminate DC power
- High noise immunity
- Complete TTL compatibility
- Built-in testability with User I.D. ROM codes
- 3-state outputs for wired-OR expansion
- Standard JEDEC 40-pin DIP EPROM or 64-pin plastic QFP pinouts
- EPROMs accepted as program data input

DESCRIPTION

The MS314000 is a 4 Mb CMOS Sigate mask-programmable static read only memory organized as 262,144 words by 16 bits or 524,288 words by 8 bits, selectable by $\overline{\text{BYTE}}$ pin.

The MS314000 is designed to be compatible to all microprocessors and similar applications where high-speed, low power, low cost, mass storage and simple interfacing are important design considerations.

PIN CONFIGURATIONS



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BLOCK DIAGRAM

