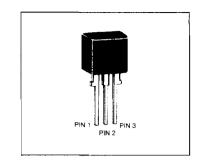
# Balanced Three Chip TO-220 "AC" Series

The three chip TO-220 "AC" series SIDACtor is a 500A rated solid state protection device designed for telecommunications systems that reference Tip and Ring to earth ground. Applications include any piece of transmission equipment that requires balanced protection and Bellcore 1089 compliance without the use of additional series resistance.

The "AC" series SIDACtor is used to help equipment meet various regulatory requirements including: Bellcore 1089, ITU K.20 & K.21, IEC 950, UL 1459 & 1950 and FCC Part 68.



#### **Electrical Parameters**

Part Number	V <sub>DRM</sub> Volts pins 1-2, 3-2	Vs Volts pins 1-2, 3-2	V <sub>DRM</sub> Volts pins 1-3	V <sub>S</sub> Volts pins 1-3	V <sub>T</sub> Volts	I <sub>DRM</sub> µAmps	ls mAmps	I <sub>T</sub> Amps	l <sub>H</sub> mAmps	C <sub>O</sub> pF
P1553AC	130	180	130	180	10	5	800	1	150	80
P1803AC	150	210	150	210	10	5	800	1	150	80
P2103AC	170	250	170	250	10	5	800	1	150	80
P2353AC	200	270	200	270	10	5	800	1	150	80
P2703AC	230	300	230	300	10	5	800	1	150	60
P3203AC	270	350	270	350	10	5	800	1	150	60
P3403AC	300	400	300	400	10	5	800	1	150	60

#### Notes:

- · All measurements are made at an ambient temperature of 25°C.
- · Listed SIDACtors are bi-directional. All electrical parameters & surge ratings apply to forward and reverse polarities.
- V<sub>DRM</sub> is measured at I<sub>DRM</sub>.
- V<sub>S</sub> is measured at 100V/µs.
- Special voltage (V<sub>S</sub> & V<sub>DRM</sub>) and holding current (I<sub>H</sub>) requirements are available upon request.
- Off-state capacitance is measured at 1MHz with a 2 volt bias and is a typical value.

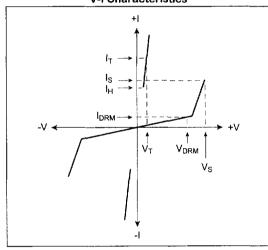
#### **Surge Ratings**

Series	l <sub>PP</sub> 2x10µs Amps	l <sub>PP</sub> 10x160µs Amps	l <sub>PP</sub> 10x1000µs Amps	itsm 60Hz Amps	di/dt Amps/us
AC	500	200	100	60	500

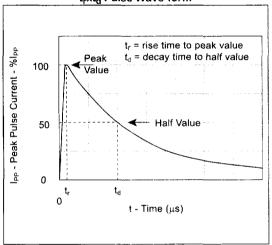
#### **Thermal Considerations**

Series	Symbol	Parameter	Value	Unit
	Tj	Junction Temperature Range	-40 to +150	°C
	T <sub>s</sub>	Storage Temperature Range	-65 to +150	°C
AC	T <sub>c</sub>	Maximum Case Temperature	+115	°C
	R <sub>ejc</sub>	Thermal Resistance: junction to case	+12	°C/W
	R <sub>eja</sub>	Thermal Resistance: junction to ambient	+50	°C/W

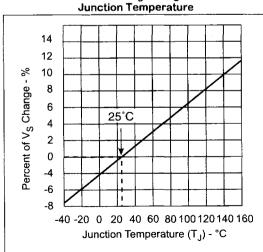




#### trxtd Pulse Wave-form



### Normalized V<sub>S</sub> Change vs.



## Normalized DC Holding Current vs. Case Temperature

