

14 Pin DIP TTL Square-Wave Generator With Complimentary Output

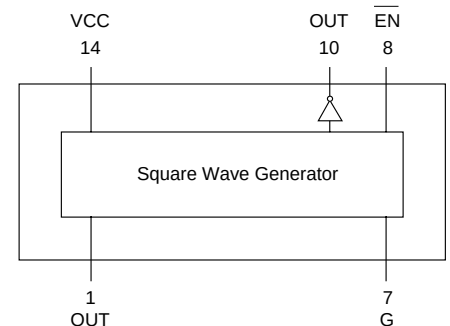
Features

- Output frequencies from 2 to 100 MHz $\pm 5\%$
- Low Profile 14 pin dual-in-line package
- Operating temperature range 0 to 70 °C
- Output synchronized using enable line
- Inherent Delay 4nS Typ.
- 50% duty cycle
- Schottky TTL

PART NUMBER	MHz $\pm 5\%$	PART NUMBER	MHz $\pm 5\%$
EPA209-2	2	EPA209-35	35
EPA209-3	3	EPA209-40	40
EPA209-4	4	EPA209-45	45
EPA209-5	5	EPA209-50	50
EPA209-10	10	EPA209-60	60
EPA209-15	15	EPA209-70	70
EPA209-20	20	EPA209-80	80
EPA209-25	25	EPA209-90	90
EPA209-30	30	EPA209-100	100

DC Electrical Characteristics					
Parameter	Test Conditions	Min	Max	Unit	
V_{OH}	High-Level Output Voltage	$V_{CC} = \min.$	$V_{IL} = \max.$	$I_{OH} = \max$	2.7
V_{OL}	Low-Level Output Voltage	$V_{CC} = \min.$	$V_{IH} = \min.$	$I_{OL} = \max$	0.5
V_{IK}	Input Clamp Voltage	$V_{CC} = \min.$	$I_I = I_{IK}$		-1.2V
I_{IH}	High-Level Input Current	$V_{CC} = \max.$	$V_{IN} = 2.7V$		50 μA
I_{IL}	Low-Level Input Current	$V_{CC} = \max.$	$V_{IN} = 5.25V$		1.0 mA
I_{OS}	Short Circuit Output Current	$V_{CC} = \max.$	$V_{IN} = 0.5V$		-2 mA
I_{CCH}	High-Level Supply Current	$V_{CC} = \max.$	$V_{OUT} = 0.$		-40 -100 mA
I_{CCL}	Low-Level Supply Current	$V_{CC} = \max.$	$V_{IN} = OPEN$		75 mA
T_{RO}	Output Rise Time	$V_{CC} = \max.$	$V_{IN} = 0$		75 mA
N_H	Fanout High-Level Output	$V_{CC} = 5.0V$			4 nS
N_L	Fanout Low-Level Output	$V_{CC} = \max.$	$V_{OH} = 2.7V$		20 TTL LOAD
		$V_{CC} = \max.$	$V_{OL} = 0.5V$		10 TTL LOAD

Schematic



Recommended Operating Conditions		Min	Max	Unit
V_{CC}	Supply Voltage	4.75	5.25	V
V_{IH}	High-Level Input Voltage	2.0		V
V_{IL}	Low-Level Input Voltage		0.8	V
I_{IK}	Input Clamp Current		-18	mA
I_{OH}	High-Level Output Current		-1.0	mA
I_{OL}	Low-Level Output Current		20	mA
d	Duty Cycle	45	55	%
T_A	Operating Free-Air Temperature	0	+70	°C

Package

