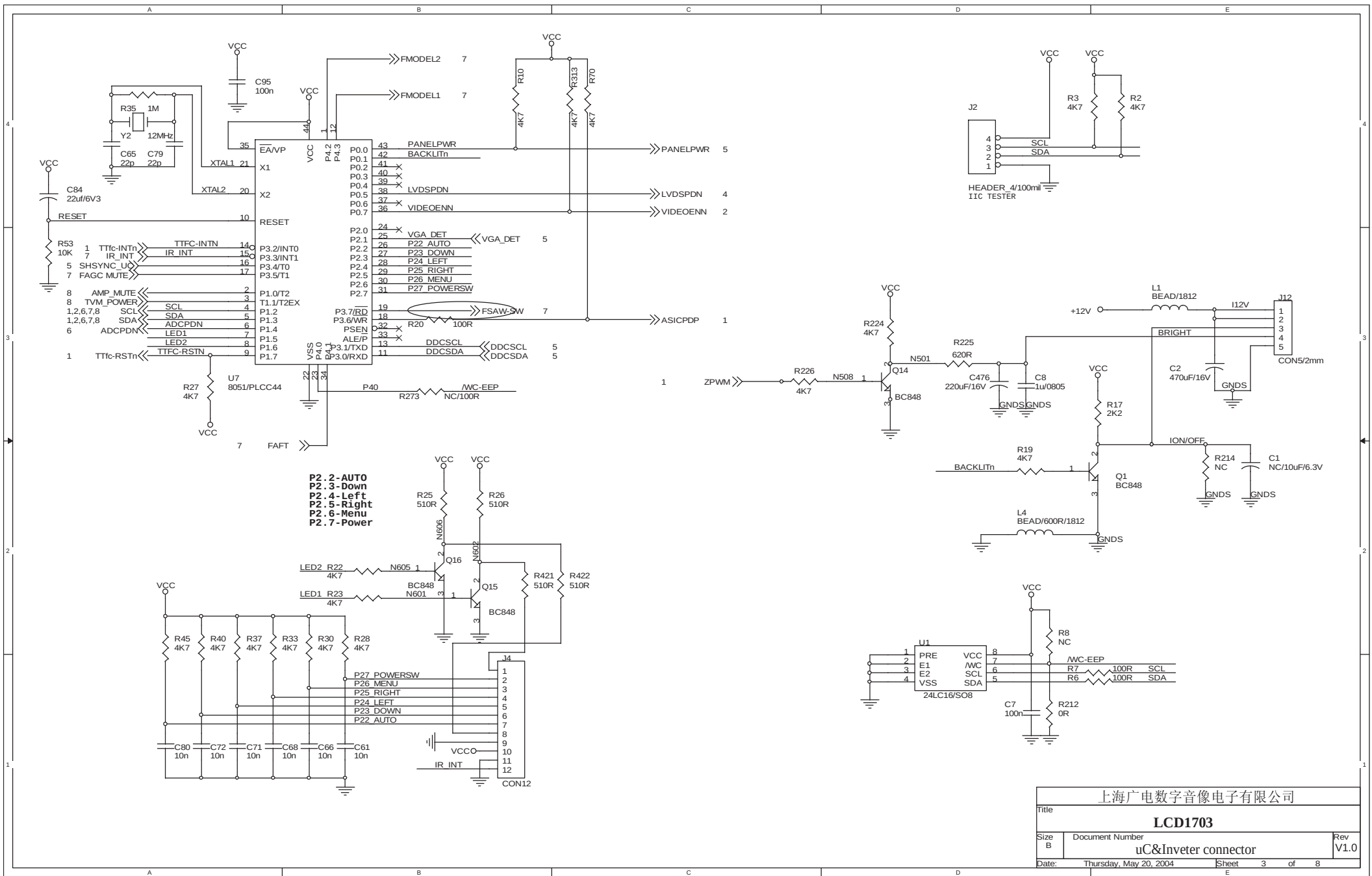
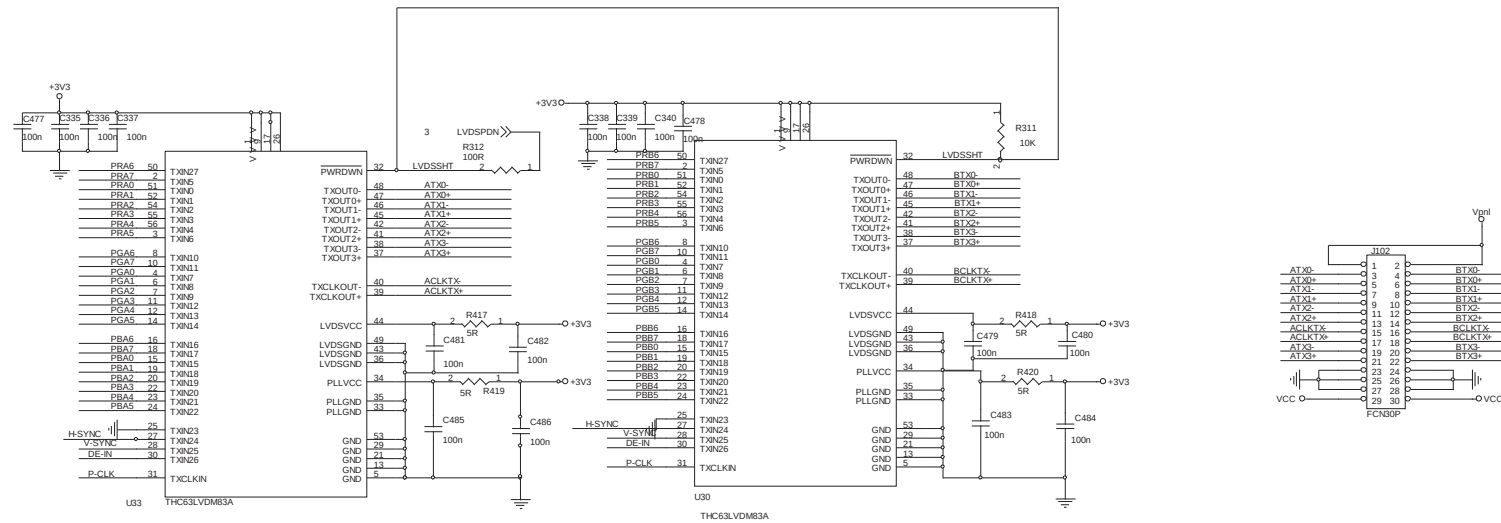
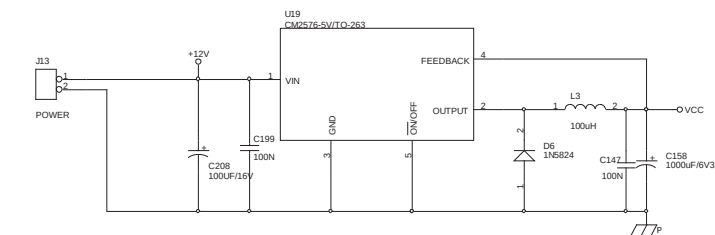
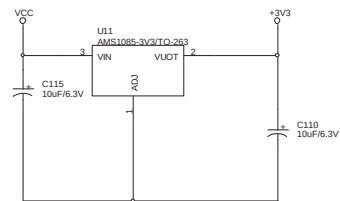




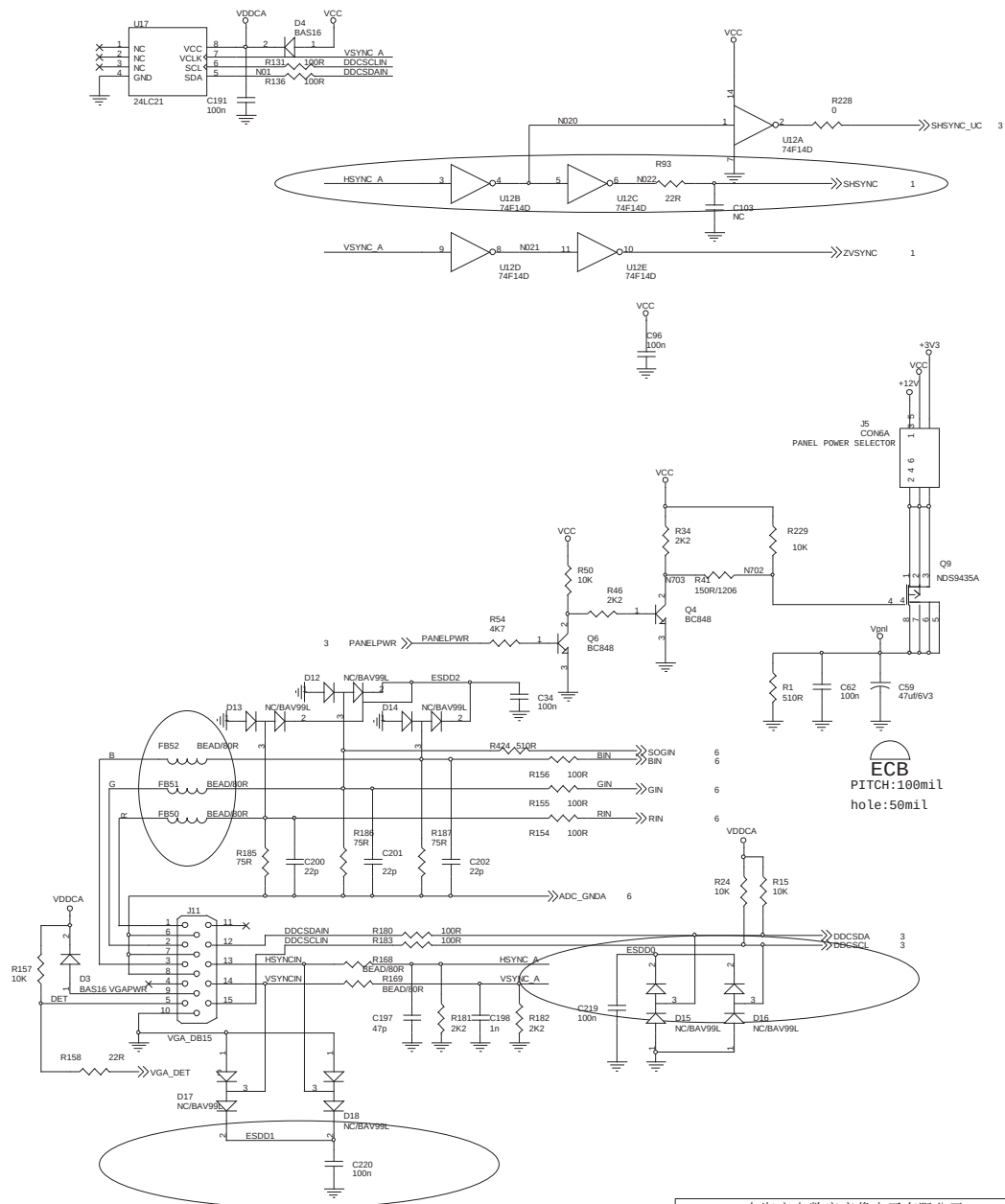




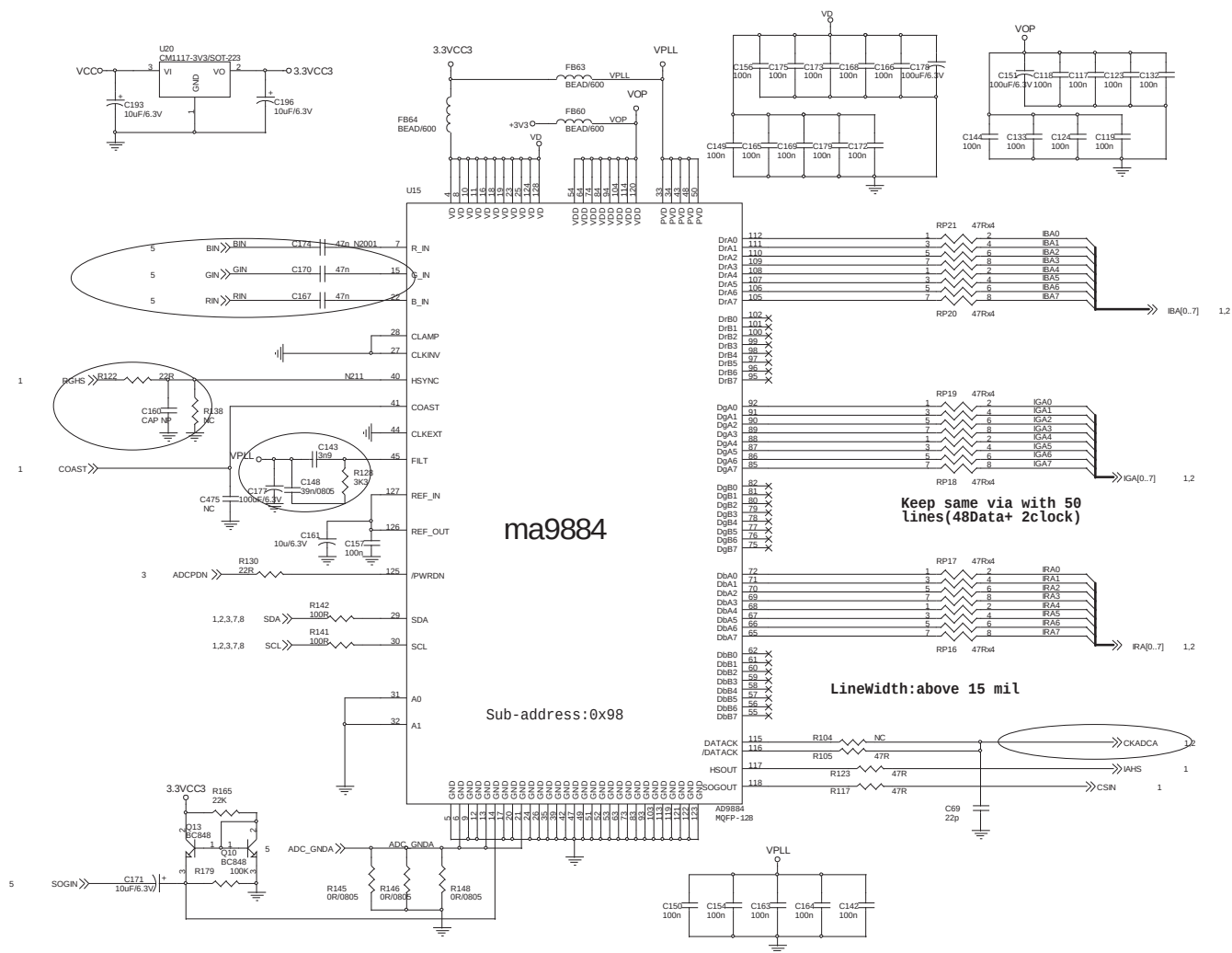
POWER BOARD 12V TO 5V

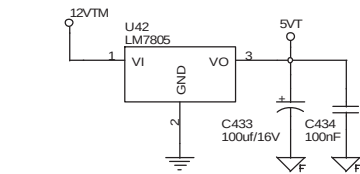
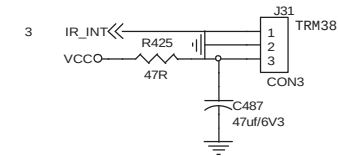
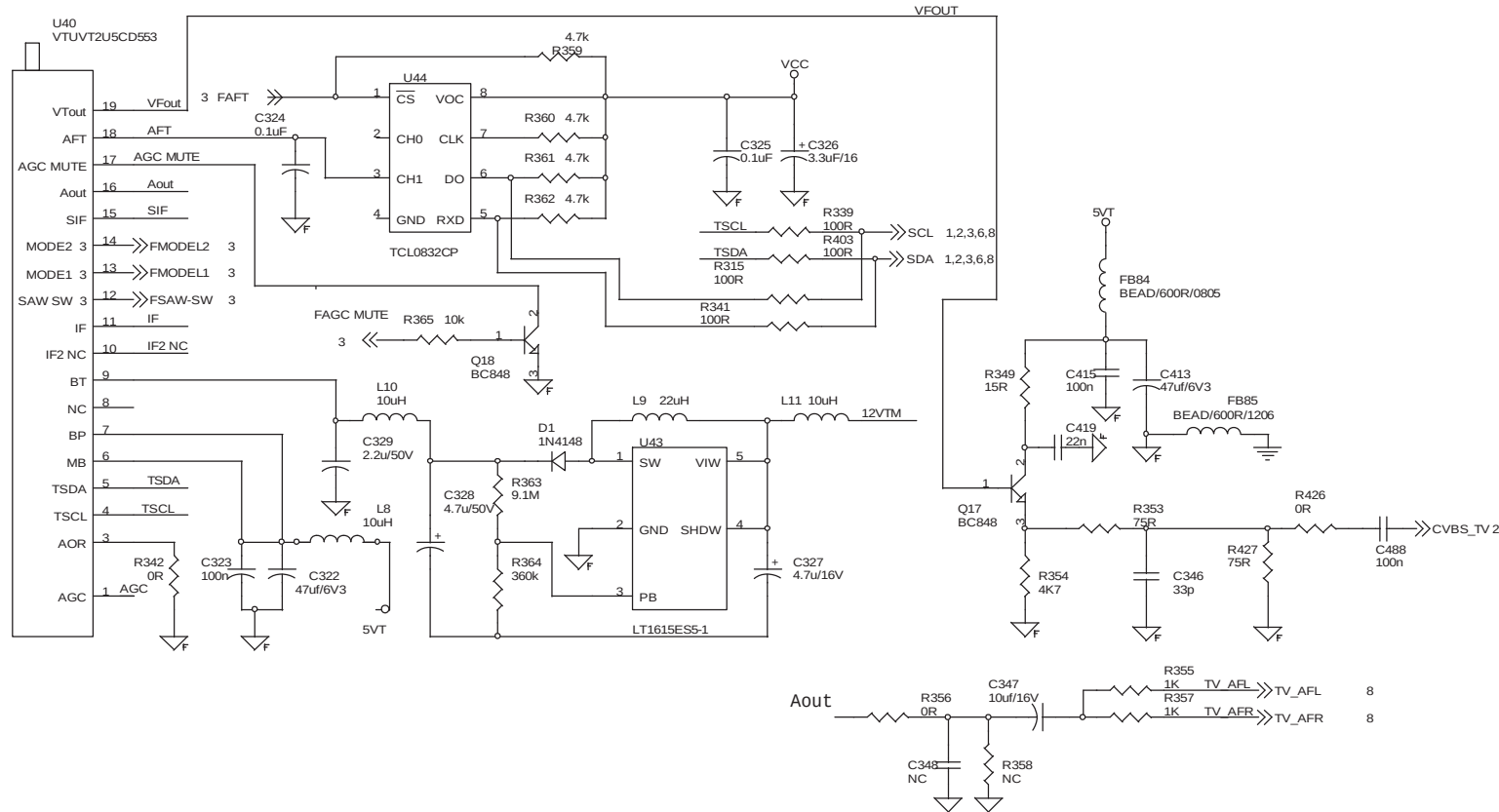


U11 is 5V to 3V LDO/3A



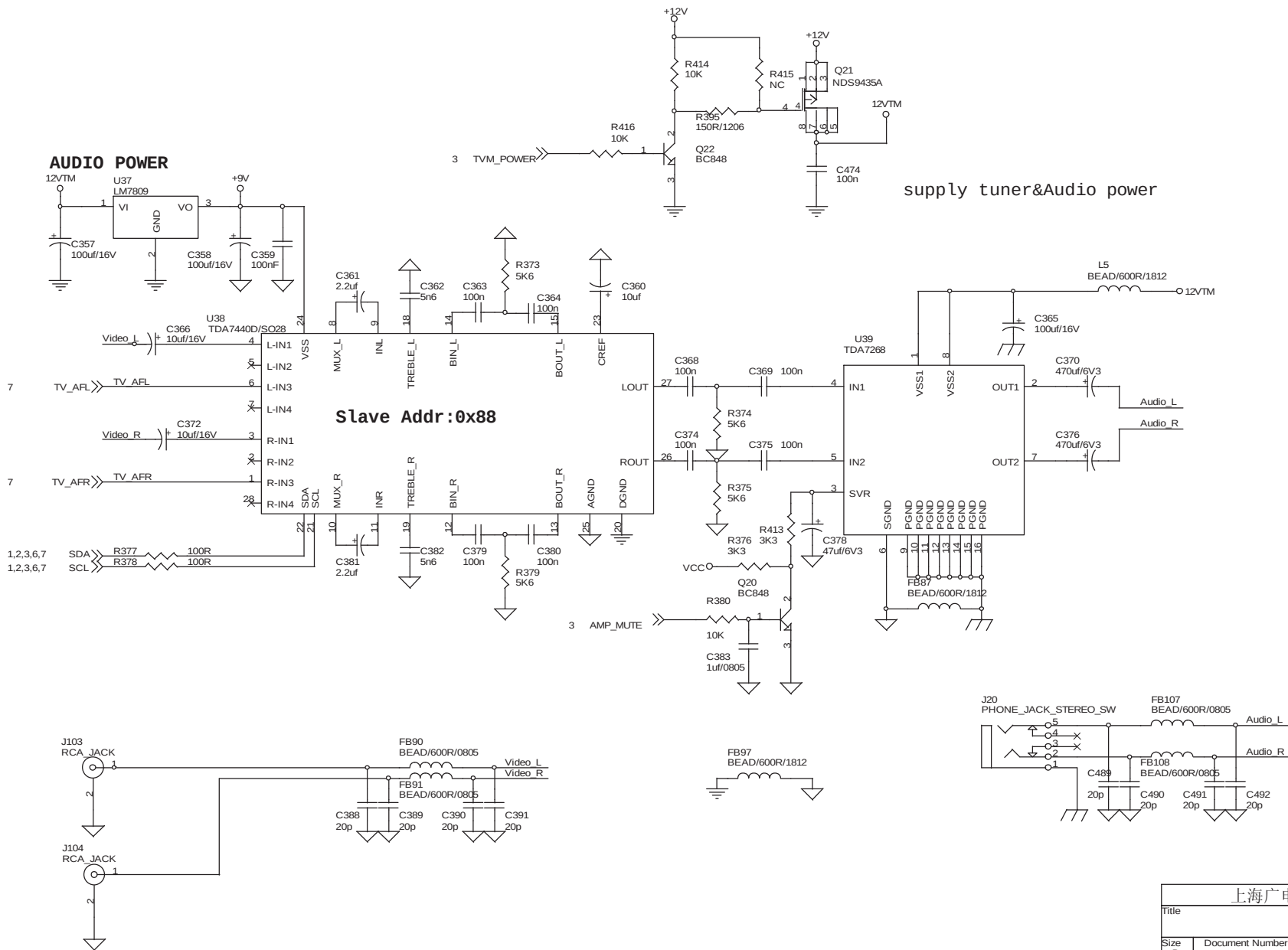
上海广电数字音像电子有限公司			
Title			
LCD1703			
Size	Document Number	Rev	
Custm		Power input&VGA input	V1.0
Date	Thursday, May 20, 2004	Sheet	5 of 8



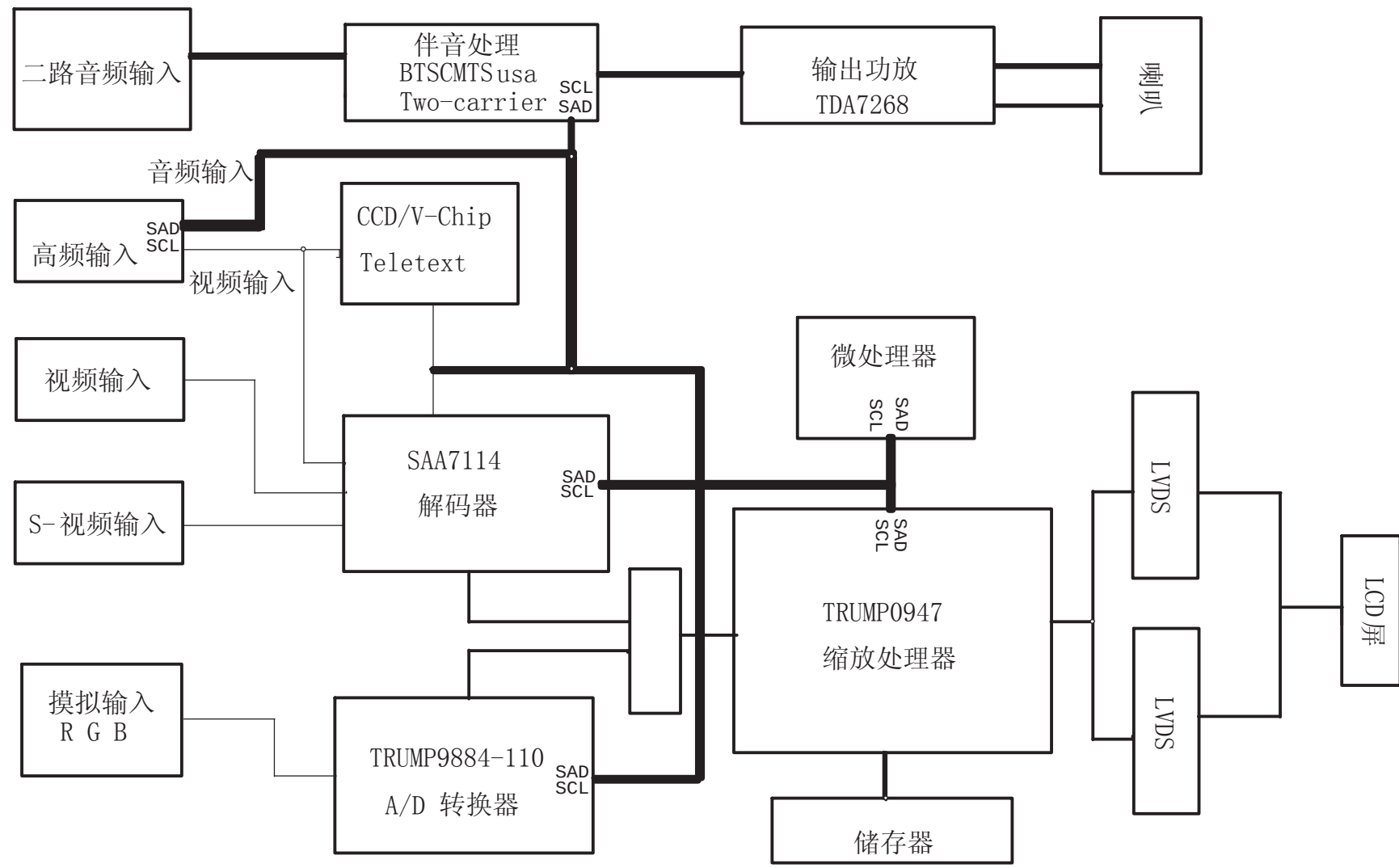


Schematic diagram of the output stage of the TVM1000. The input Aout is connected to a resistor R356 (0R). The output of R356 is connected to a node that branches to a capacitor C348 (NC) to ground and a capacitor C347 (10uF/16V) to ground. The output of C347 is connected to a node that branches to a resistor R355 (1K) to TV_AFL and a resistor R357 (1K) to TV_AFR.

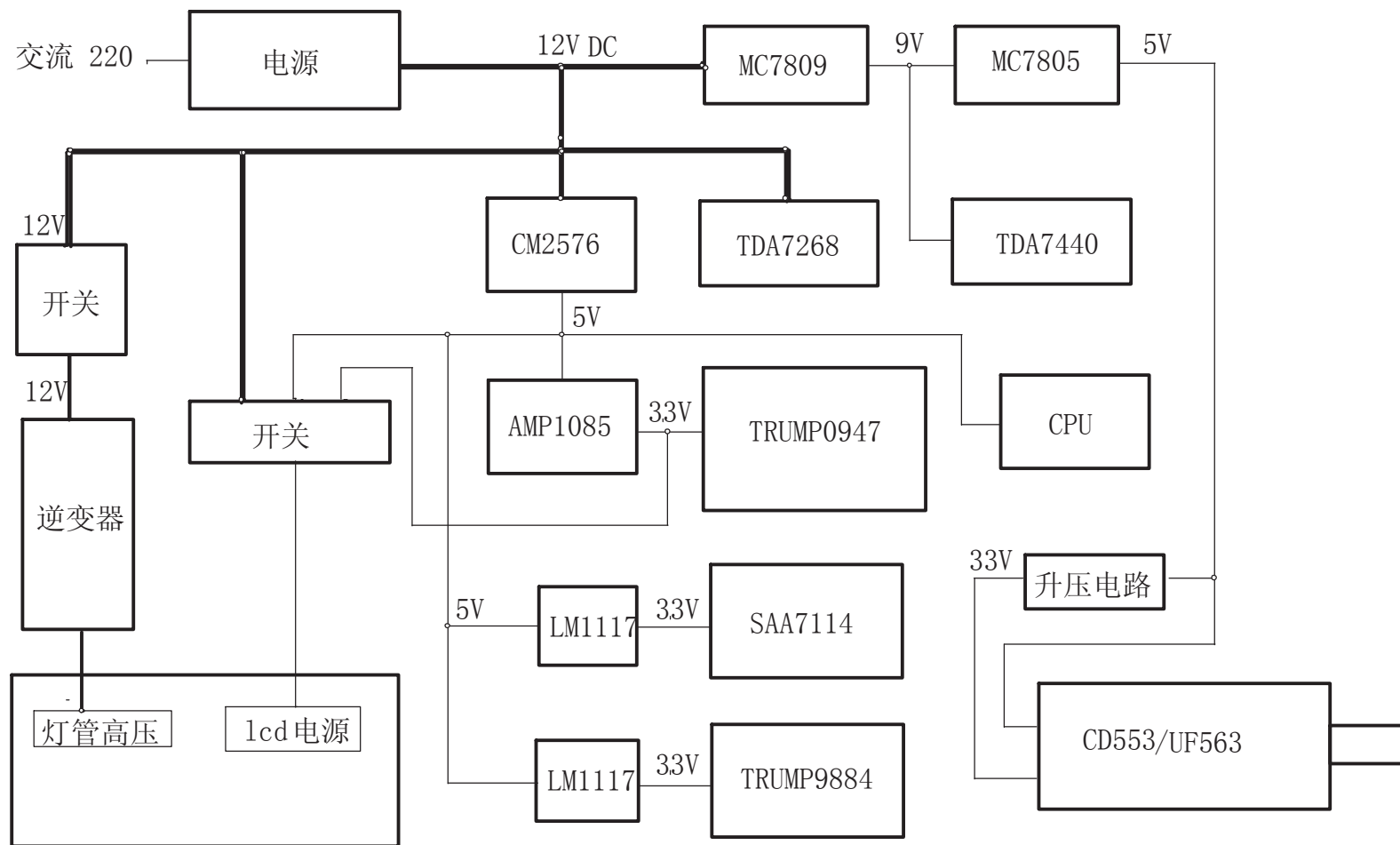
上海广电数字音像电子有限公司			
Title LCD1703			
Size B	Document Number 7Tuner&CC&MTS		Rev V1.0
Date:	Thursday, May 20, 2004	Sheet	7 of 8



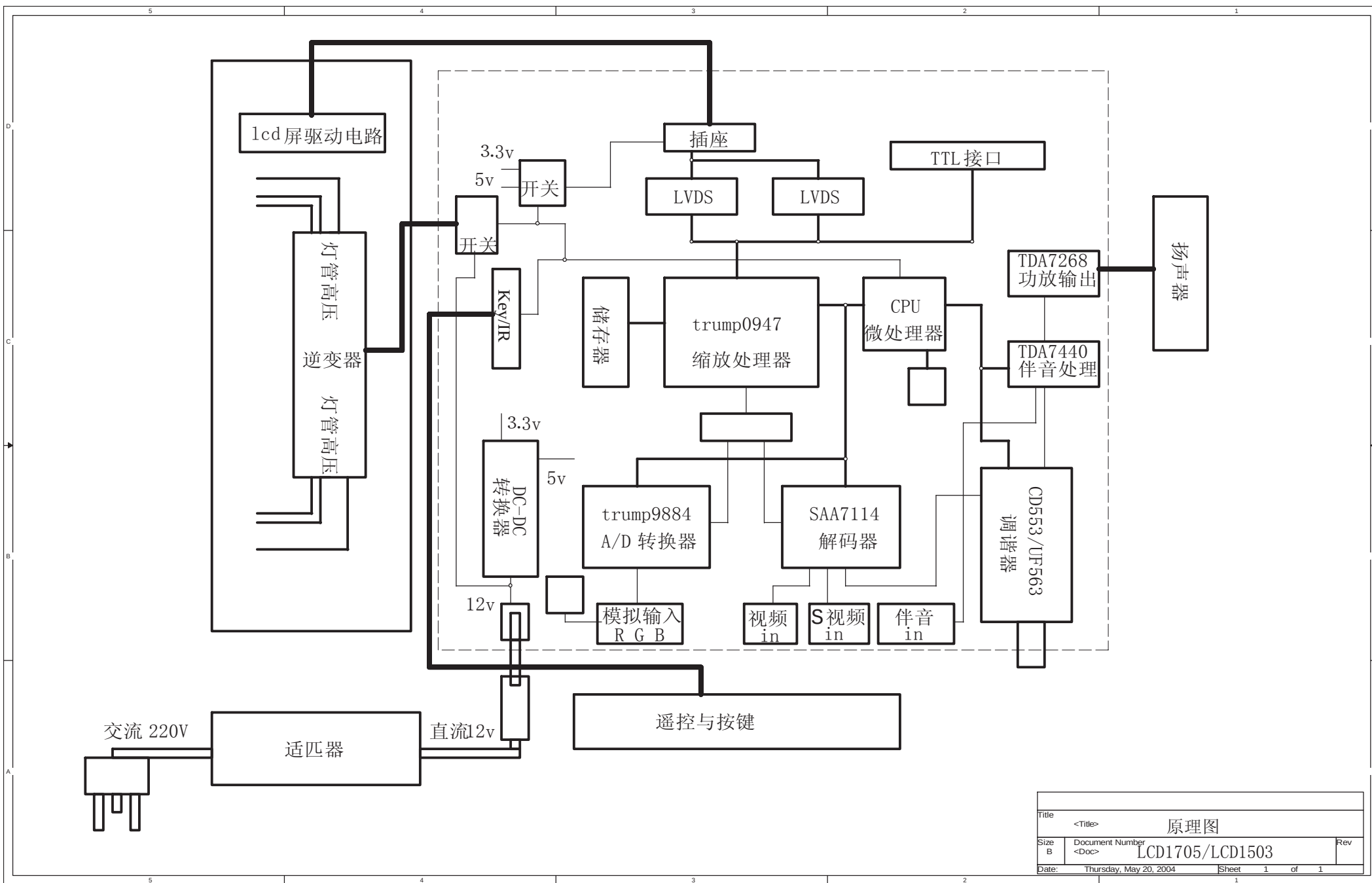
上海广电数字音像电子有限公司.			
Title		LCD1703	
Size	Document Number	Audio processor&Amplifier	
B		Rev	
		V1.0	
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Title			
<Title> 方框图			
Size	Document Number	Rev	
B	<Doc>	<RevCode>	
Date:		Thursday, May 20, 2004	Sheet 1 of 1



Title			
<Title> 电源分配			
Size	Document Number	Rev	
B	<Doc> LCD1705/LCD1503	<RevCode>	
Date:	Thursday, May 20, 2004	Sheet	1 of 1



Title		
<Title> 原理图		
Size	Document Number	Rev
B	<Doc> LCD1705/LCD1503	
Date:	Thursday, May 20, 2004	Sheet 1 of 1