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PN200A / MMBT200

PNP General-Purpose Amplifier

Description

This device is designed for general-purpose amplifier applications at collector currents to 300 mA. Sourced from Process 68.

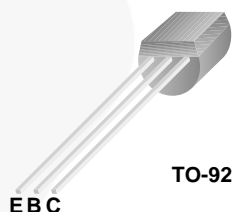


Figure 1. PN200A Device Package

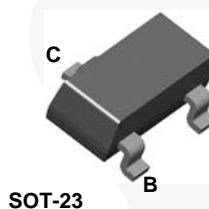


Figure 2. MMBT200 Device Package

Ordering Information

Part Number	Marking	Package	Packing Method
PN200A	PN200A	TO-92 3L	Bulk
MMBT200	N2	SOT-23 3L	Tape and Reel

Absolute Maximum Ratings^{(1),(2)}

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V_{CEO}	Collector-Emitter Voltage	-45	V
V_{CBO}	Collector-Base Voltage	-60	V
V_{EBO}	Emitter-Base Voltage	-6	V
I_C	Collector Current - Continuous	-500	mA
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	$^\circ\text{C}$

Notes:

- These ratings are based on a maximum junction temperature of 150°C .
- These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty cycle operations.

Thermal Characteristics

Values are at $T_A = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter	Max.		Unit
		PN200A ⁽³⁾	MMBT200 ⁽⁴⁾	
P_D	Total Device Dissipation	625	350	mW
	Derate Above 25°C	5.0	2.8	mW/ $^\circ\text{C}$
$R_{\theta JC}$	Thermal Resistance, Junction to Case	83.3		$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	357	$^\circ\text{C/W}$

Notes:

3. PCB size: FR-4 76 x 114 x 1.57 mm³ (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

4. Device mounted on FR-4 PCB 1.6 inch X 1.6 inch X 0.06 inch.

Electrical Characteristics

Values are at $T_A = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit	
Off Characteristics						
BV _{CBO}	Collector-Base Breakdown Voltage	I _C = -10 μA, I _B = 0	-60		V	
BV _{CEO}	Collector-Emitter Breakdown Voltage ⁽⁵⁾	I _C = -1.0 mA, I _E = 0	-45		V	
BV _{EBO}	Emitter-Base Breakdown Voltage	I _E = -10 μA, I _C = 0	-6.0		V	
I _{CBO}	Collector Cut-Off Current	V _{CB} = -50 V, I _E = 0		-50	nA	
I _{CES}	Collector Cut-Off Current	V _{CE} = -40 V, I _E = 0		-50	nA	
I _{EBO}	Emitter Cut-Off Current	V _{EB} = -4.0 V, I _C = 0		-50	nA	
On Characteristics						
h _{FE}	DC Current Gain	I _C = -100 μA, V _{CE} = -1.0 V	MMBT200	80		
			PN200A	240		
		I _C = -10 mA, V _{CE} = -1.0 V	MMBT200	100		450
			PN200A	300		600
		I _C = -100 mA, V _{CE} = -1.0 V ⁽⁵⁾	PN200A	100		
			I _C = -150 mA, V _{CE} = -5.0 V ⁽⁵⁾	MMBT200		100
PN200A	100					
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -10 mA, I _B = -1.0 mA		-0.2	V	
		I _C = -200 mA, I _B = -20 mA ⁽⁵⁾		-0.4		
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -10 mA, I _B = -1.0 mA		-0.85	V	
		I _C = -200 mA, I _B = -20 mA ⁽⁵⁾		-1.00		
Small Signal Characteristics						
f _T	Current Gain - Bandwidth Product	V _{CE} = -20 V, I _C = -20 mA,	250		MHz	
C _{ob}	Output Capacitance	V _{CB} = -10 V, f = -1.0 MHz		6.0	pF	
NF	Noise Figure	I _C = -100 μA, V _{CE} = -5.0 V, R _G = 2.0 kΩ, f = 1.0 kHz		4.0	dB	

Note:

5. Pulse test: pulse width $\leq 300\ \mu\text{s}$, duty cycle $\leq 2.0\%$.

Typical Performance Characteristics

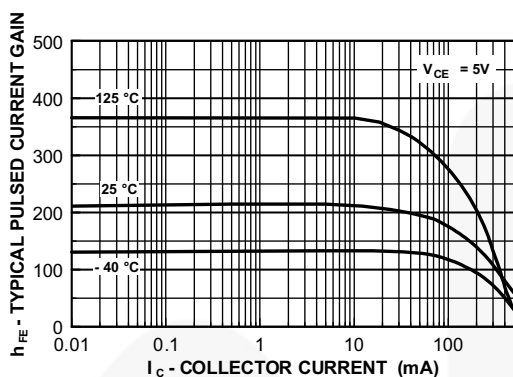


Figure 3. Typical Pulsed Current Gain vs. Collector Current

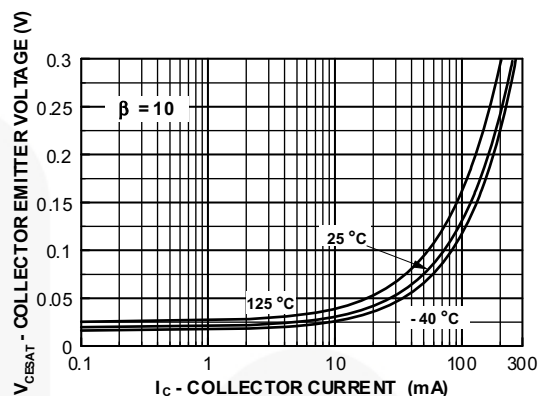


Figure 4. Collector-Emitter Saturation Voltage vs. Collector Current

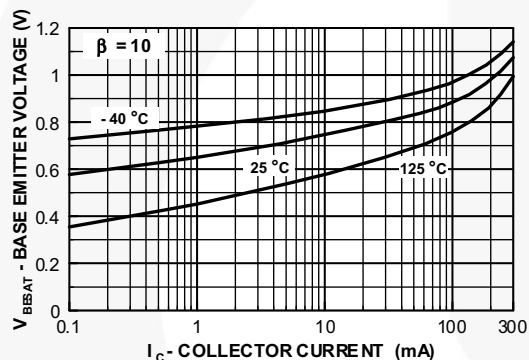


Figure 5. Base-Emitter Saturation Voltage vs. Collector Current

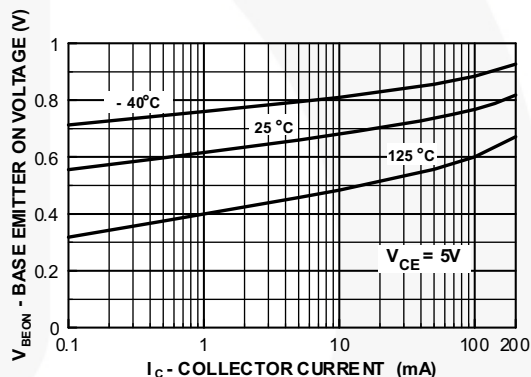


Figure 6. Base-Emitter On Voltage vs. Collector Current

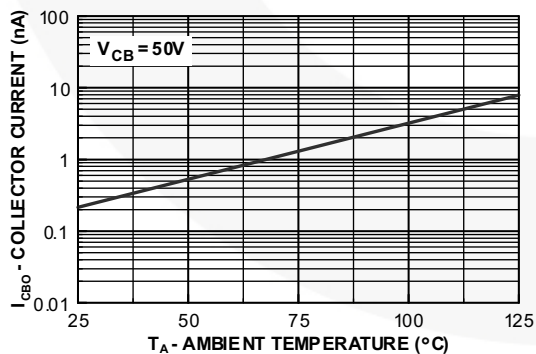


Figure 7. Collector Cut-Off Current vs. Ambient Temperature

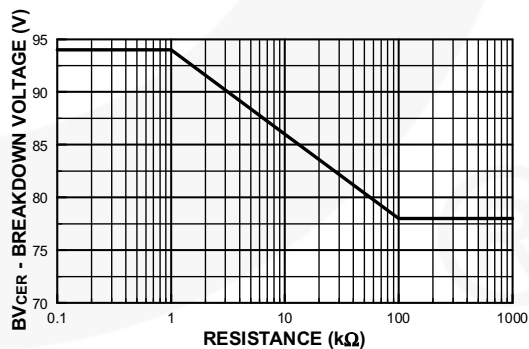


Figure 8. Collector-Emitter Breakdown Voltage with Resistance Between Emitter-Base

Typical Performance Characteristics (Continued)

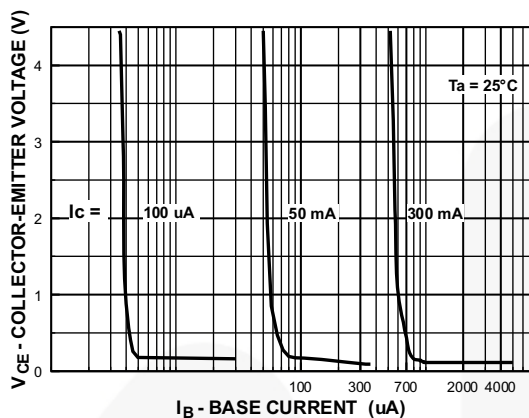


Figure 9. Collector Saturation Region

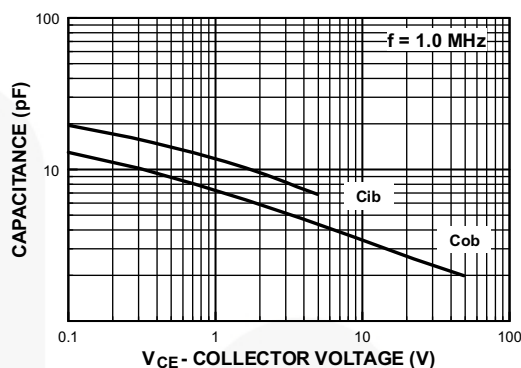


Figure 10. Input and Output Capacitance vs. Reverse Voltage

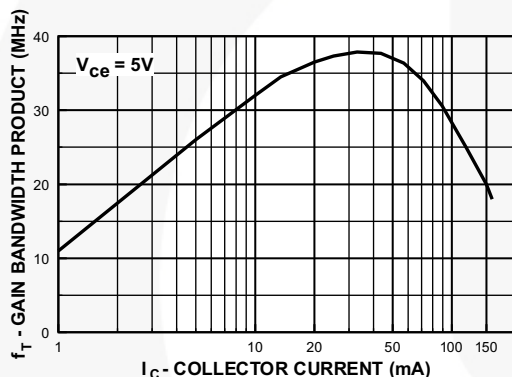


Figure 11. Gain Bandwidth Product vs. Collector Current

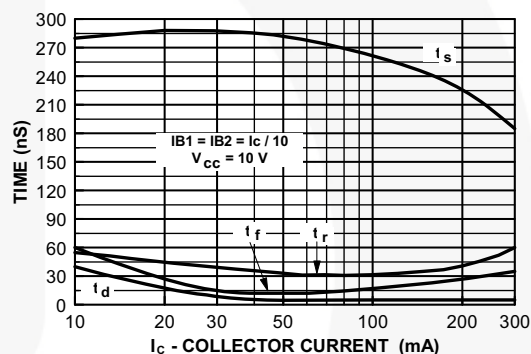


Figure 12. Switching Times vs. Collector Current

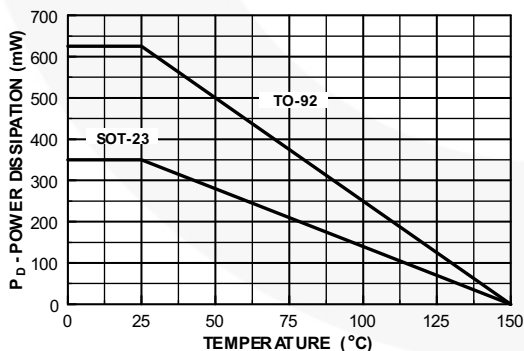
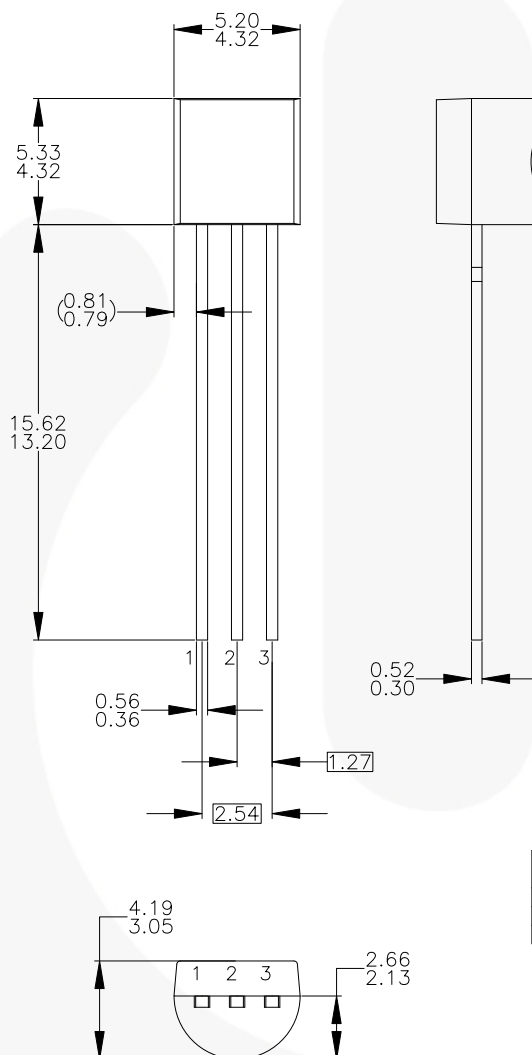


Figure 13. Power Dissipation vs. Ambient Temperature

Physical Dimensions

TO-92



NOTES: UNLESS OTHERWISE SPECIFIED

- A) DRAWING WITH REFERENCE TO JEDEC TO-92 RECOMMENDATIONS.
 B) ALL DIMENSIONS ARE IN MILLIMETERS.
 C) DRAWING CONFORMS TO ASME Y14.5M-1994.
 D) TO-92 (92,94,96,97,98) PIN CONFIGURATION:

PIN	92			94			96			97			98		
	P	F	M	P	F	M	B	F	M	P	F	M	P	F	M
1	E	S	S	E	S	S	B	D	G	C	G	D	C	G	D
2	B	D	G	C	G	D	E	S	S	B	D	G	E	S	S
3	C	G	D	B	D	G	C	G	D	E	S	S	B	D	G

LEGEND:

P — BIPOLAR E — EMITTER D — DRAIN
 F — JFET B — BASE S — SOURCE
 M — DMOS C — COLLECTOR G — GATE

- E) FOR PACKAGE 92, 94, 96, 97 AND 98:
 PIN CONFIGURATION DRAIN "D" AND SOURCE "S"
 ARE INTERCHANGEABLE AT JFET "F" OPTION.
 F) DRAWING FILENAME: MKT-ZA03DREV3.

Figure 14. 3-LEAD, TO-92, MOLDED, STD STRAGHIT LEAD (NO EOL CODE) (ACTIVE)

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http://www.fairchildsemi.com/packaging_dwg/PKG-ZA03D_BK.pdf

Physical Dimensions (Continued)

SOT-23

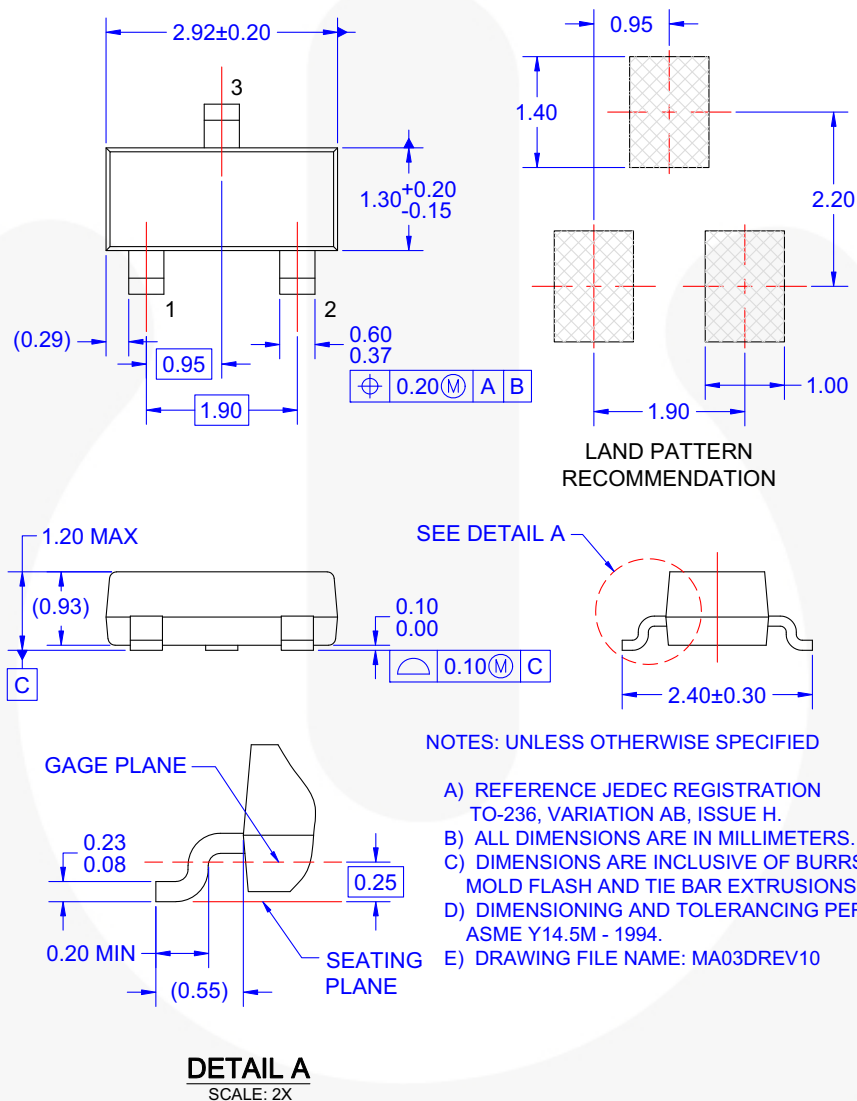


Figure 15. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE (ACTIVE)

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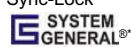



<http://www.fairchildsemi.com/dwg/MA/MA03D.pdf>

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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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