



NEC's HIGH NOISE REDUCTION HIGH SPEED DIGITAL OUTPUT TYPE 8 PIN DIP OPTOCOUPLER

PS9614
PS9614L

FEATURES

- **HIGH COMMON MODE TRANSIENT IMMUNITY:**
CMR, CML: $\pm 20 \text{ kV}/\mu\text{s}$ TYP
- **HIGH SPEED RESPONSE:**
10 Mbps
- **HIGH ISOLATION VOLTAGE:**
BV: 3750 V_{r.m.s.} MIN
- **OPEN COLLECTOR OUTPUT TYPE**
- **TAPE AND REEL AVAILABLE:**
PS9614L-E3, E4: 1000 Pcs/Reel

DESCRIPTION

NEC's PS9614 and PS9614L are optically coupled isolators containing a GaAlAs LED on the light emitting diode (input) side and a photodiode and a signal processing circuit on the detector (output) side on one chip. The PS9614 is in a plastic DIP (Dual In-line Package) and the PS9614L is a lead-bending type (Gull-wing) for surface mount.

APPLICATIONS

- MEASUREMENT EQUIPMENT
- PDP
- FACTORY AUTOMATION NETWORK

ELECTRICAL CHARACTERISTICS (T_A = -40~+85°C unless otherwise specified)

PART NUMBER				PS9614, PS9614L		
SYMBOLS		PARAMETERS	UNITS	MIN	TYP	MAX
Diode	V _F	Forward Voltage, I _F = 10 mA, T _A = 25°C	V	1.4	1.65	1.9
	I _R	Reverse Current, V _R = 5 V, T _A = 25°C	μA		10	
	C _t	Terminal Capacitance, V = 0V, f = 1 MHz, T _A = 25°C	pF		30	
Detector	I _{OH}	High Level Output Current, V _{CC} = V _O = 5.5 V, V _F = 0.8 V	μA		0.03	250
	V _{OL}	Low Level Output Voltage, V _{CC} = 5.5 V, I _F = 5 mA, I _O = 13 mA	V		0.2	0.6
	I _{CCH}	High Level Supply Current, V _{CC} = 5.5 V, I _F = 0 mA	mA		2.6	8
	I _{CCL}	Low Level Supply Current, V _{CC} = 5.5 V, I _F = 10 mA	mA		7	11
	I _{FHL}	Threshold Input Current, V _{CC} = 5 V, V _O = 0, 0.8 V, R _L = 350 Ω	%		2.3	5
Coupled	R _{I-O}	Isolation Resistance, V _{in-out} = 1k V _{DC} , RH = 40 to 60%, T _A = 25°C	Ω	10 ¹¹		
	C _{I-O}	Isolation Capacitance, V = 0, f = 1 MHz, T _A = 25°C	pF		0.9	
	t _{PHL}	Propagation Delay Time, High → Low ¹ , V _{CC} = 5 V, R _L = 350 Ω, I _F = 7.5 mA	ns		61	75
		T _A = 25°C				100
	t _{PLH}	Propagation Delay Time, Low → High ¹ , V _{CC} = 5 V, R _L = 350 Ω, I _F = 7.5 mA	ns		51	75
		T _A = 25°C				100
	t _r	Rise Time, V _{CC} = 5 V, R _L = 350 Ω, I _F = 7.5 mA	ns		20	
	t _f	Fall Time, V _{CC} = 5 V, R _L = 350 Ω, I _F = 7.5 mA	ns		8	
	PWD	Pulse Width Distortion, V _{CC} = 5 V, R _L = 350 Ω, I _F = 7.5 mA	ns		10	50
	t _{PSK}	Propagation Skew, V _{CC} = 5 V, R _L = 350 Ω, I _F = 7.5 mA	ns			60
	CMH	Common Mode Transient Immunity at High Level Output ² V _{CC} = 5 V, V _{CM} = 1 kV, T _A = 25°C, I _F = 0 mA, V _O (MIN) = 2 V, R _L = 350 Ω	kV/μs	10	20	
	CML	Common Mode Transient Immunity at Low Level Output ² V _{CC} = 5 V, V _{CM} = 1 kV, T _A = 25°C, I _F = 0 mA, V _O (MIN) = 0.8 V, R _L = 350 Ω	kV/μs	10	20	

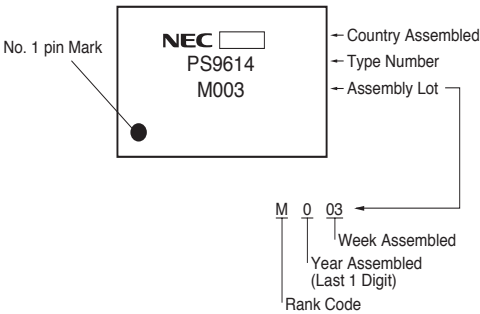
Please see notes on the next page.

ABSOLUTE MAXIMUM RATINGS¹ (T_A = 25°C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
Diode			
I _F	Forward Current	mA	30
V _R	Reverse Voltage	V	3
Detector			
V _{CC}	Supply Voltage	V	7
V _O	Output Voltage	V	7
I _O	Output Current	mA	25
P _C	Power Dissipation	mW	40
BV	Isolation Voltage ²	V _{r.m.s.}	3750
T _A	Operating Ambient Temp.	°C	-40 to +85
T _{STG}	Storage Temperature	°C	-55 to +125

- Notes:
- Operation in excess of any one of these parameters may result in permanent damage.
 - AC voltage for 1 minute at T_A = 25 °C, RH = 60 % between input and output.

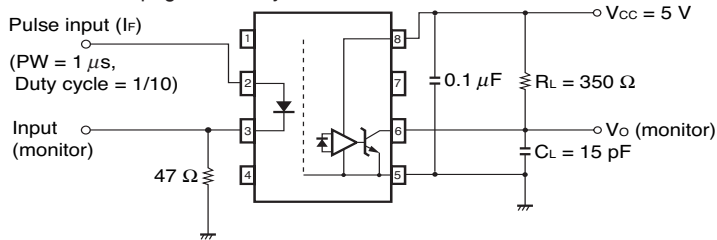
MARKING



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Note:

1. Test Circuit for Propagation Delay Time:



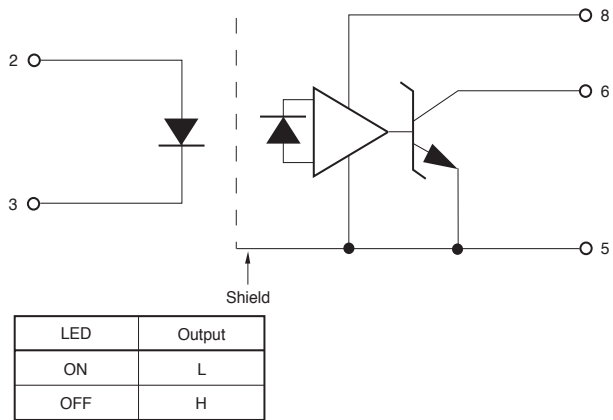
*C_L is approximately 15 pF which includes probe and stray wiring capacitance.

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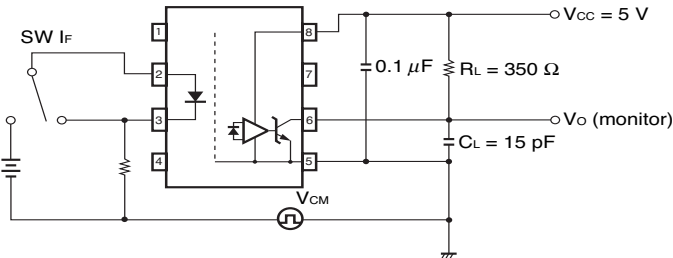
RECOMMENDED OPERATING CONDITIONS

SYMBOLS	PARAMETERS	UNITS	MIN	TYP	MAX
V _{FL}	Low Level Input Voltage	V	0		0.8
I _{FH}	High Level Input Current	mA	6.3	10	12.5
V _{CC}	Supply Voltage	V	4.5	5	5.5
N	TTL (loads) (R _L = 1kΩ)				5
R _L	Pull-up Resistance	Ω	330		4k
T _A	Operating Ambient Temperature		-40	+85	

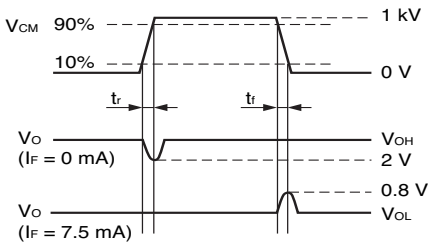
FUNCTIONAL DIAGRAM



2. Test Circuit for Common Mode Transient Immunity:



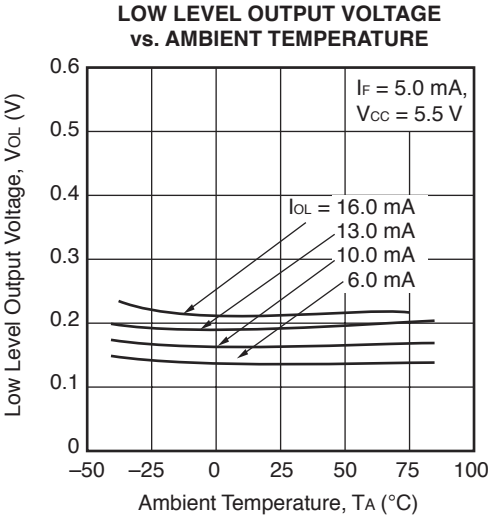
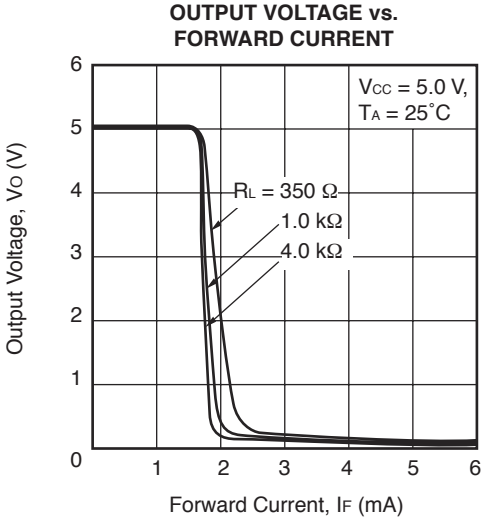
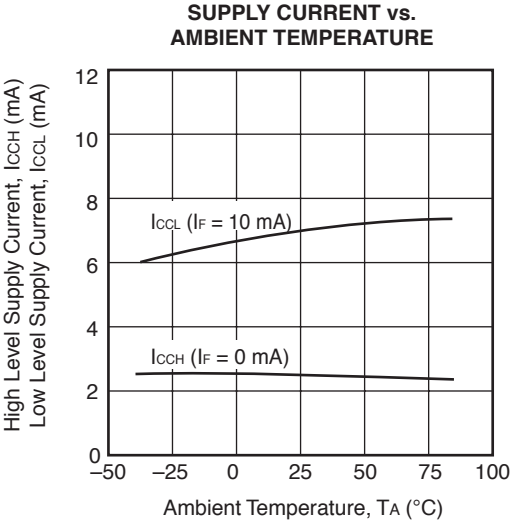
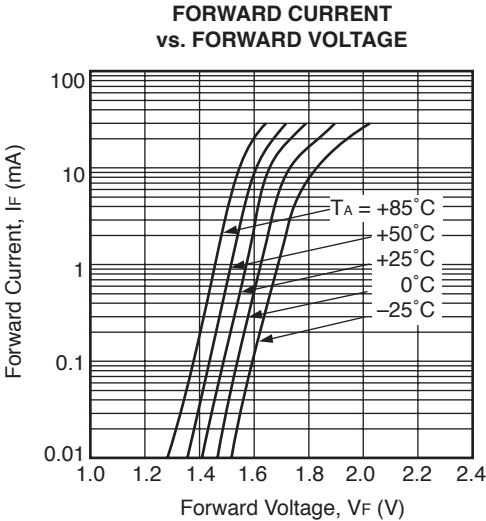
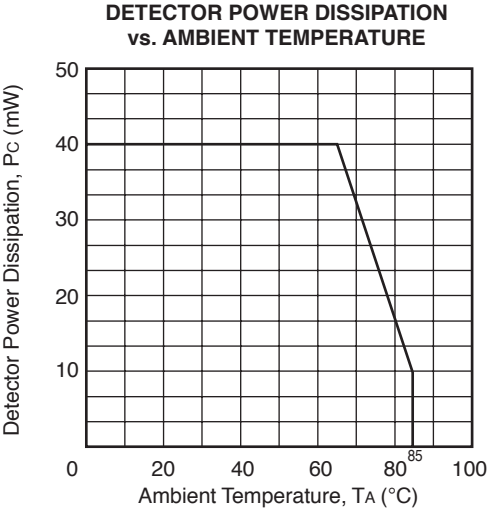
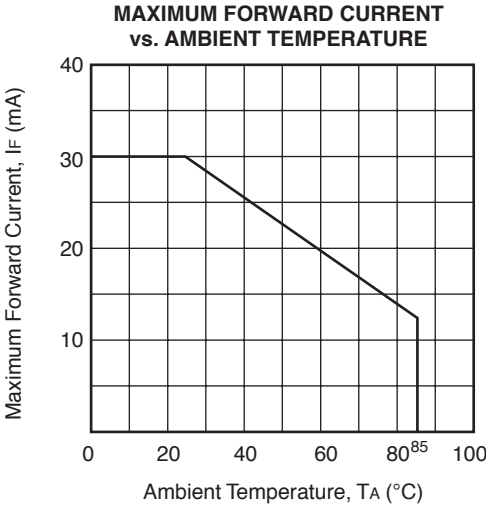
C_L includes probe and stray wiring capacitance.



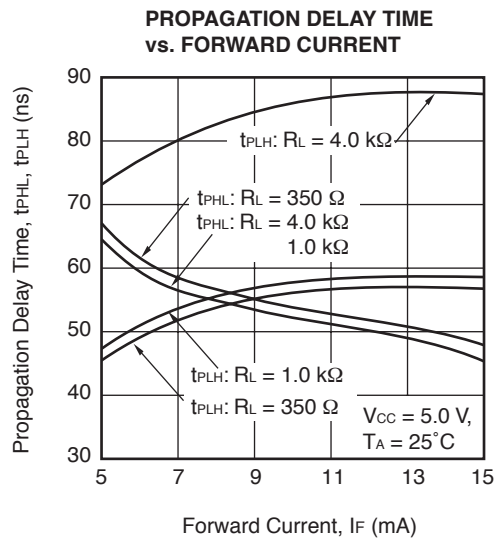
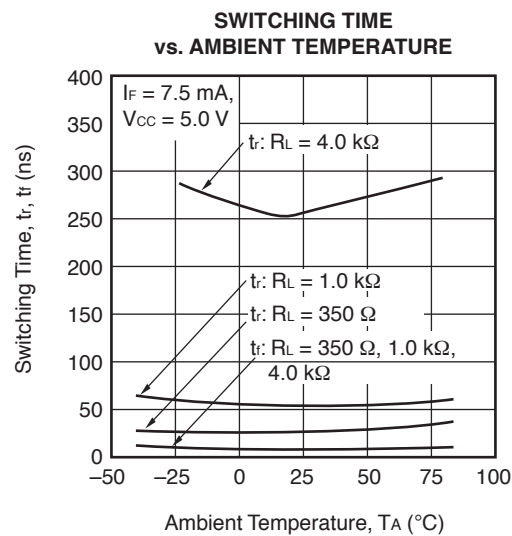
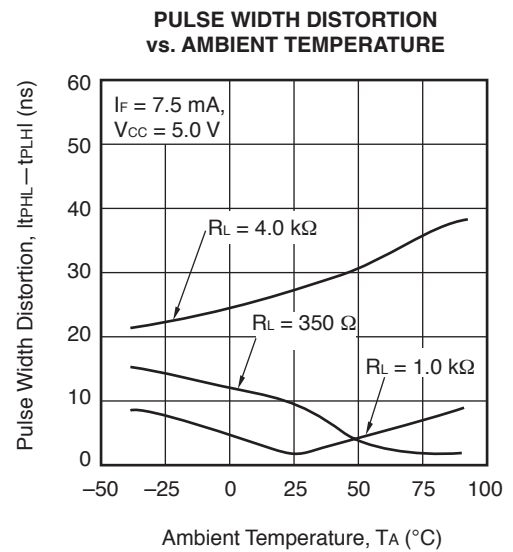
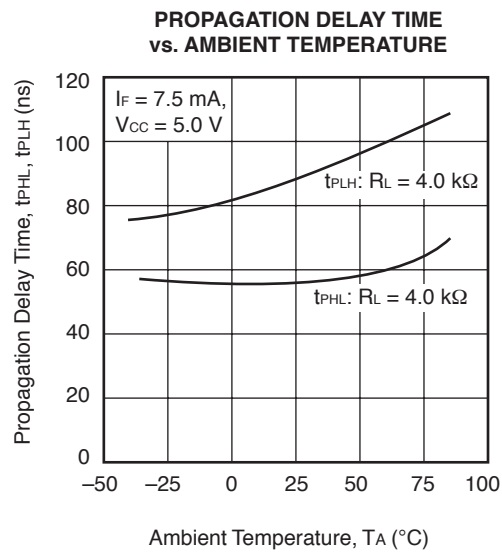
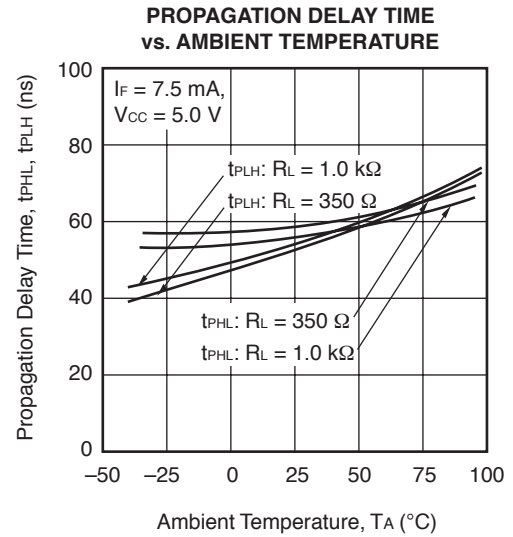
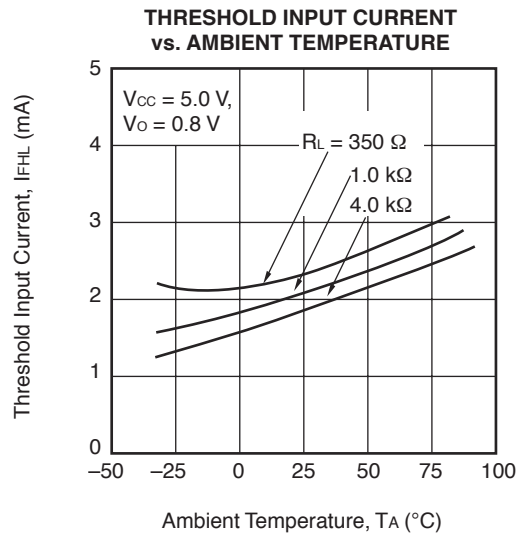
USAGE CAUTIONS

- Protect against static electricity when handling this product.
- Bypass capacitor greater than 0.1 μF is used between V_{CC} and GND near device (lead distance: 10 mm MIN).

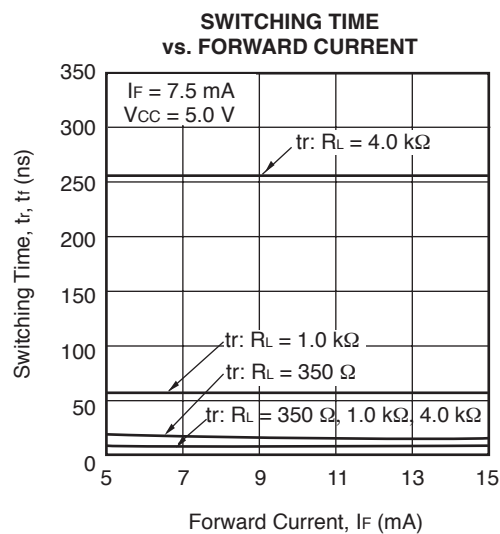
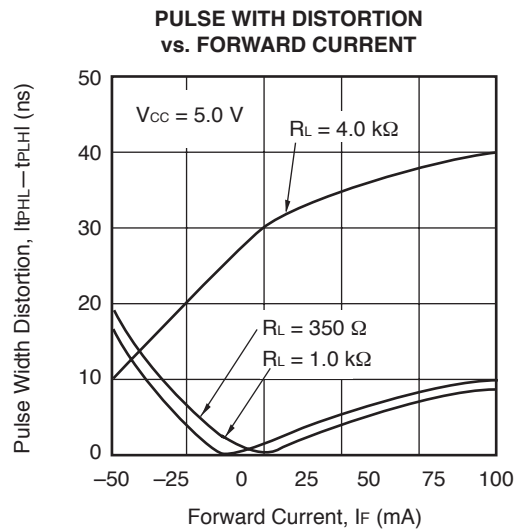
TYPICAL PERFORMANCE CURVES (T_A = 25°C)



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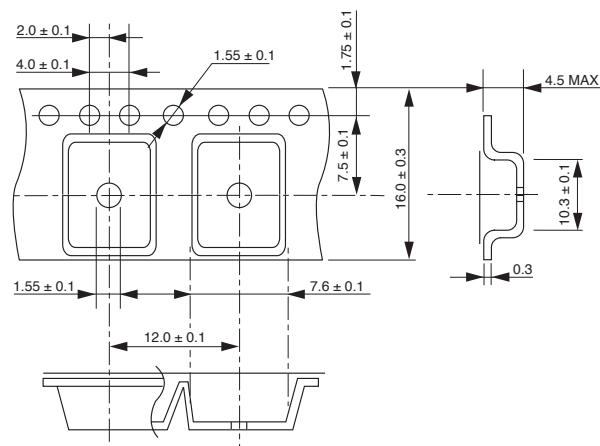
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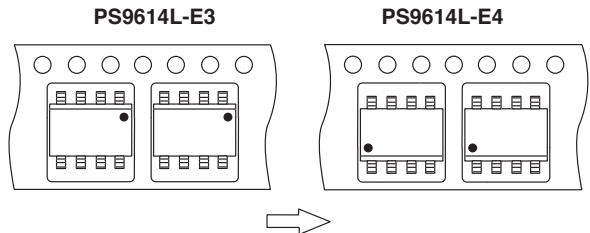
Note: Graphs indicate nominal characteristics.

TAPING SPECIFICATIONS (Units in mm)

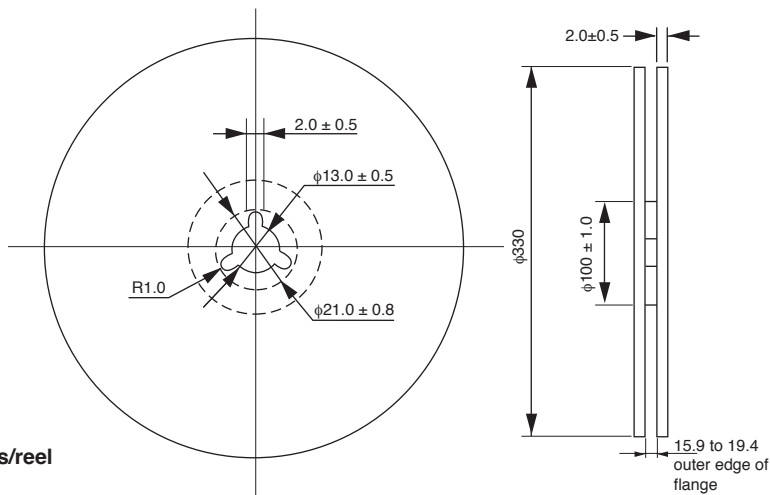
Tape Outline and Dimensions



Tape Direction



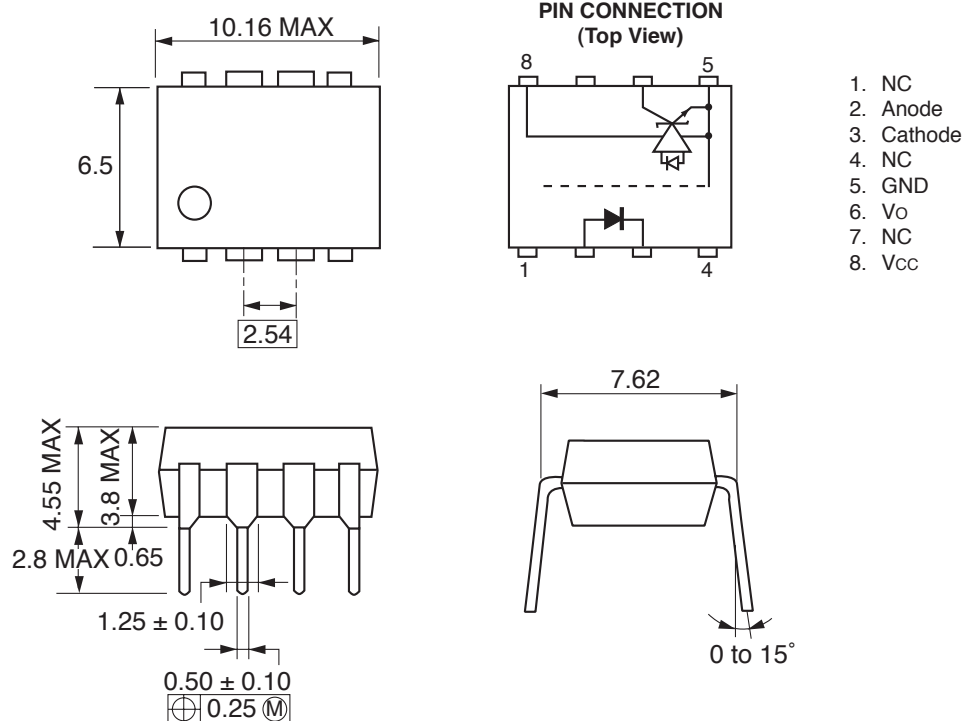
Reel Outline and Dimensions



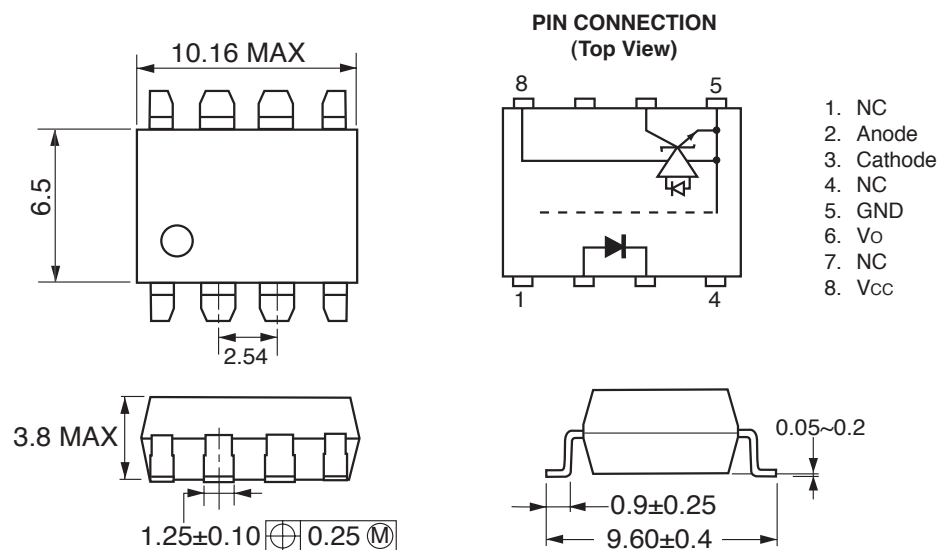
Packing: 1000 pcs/reel

PACKAGE OUTLINE (Units in mm)

PS9614



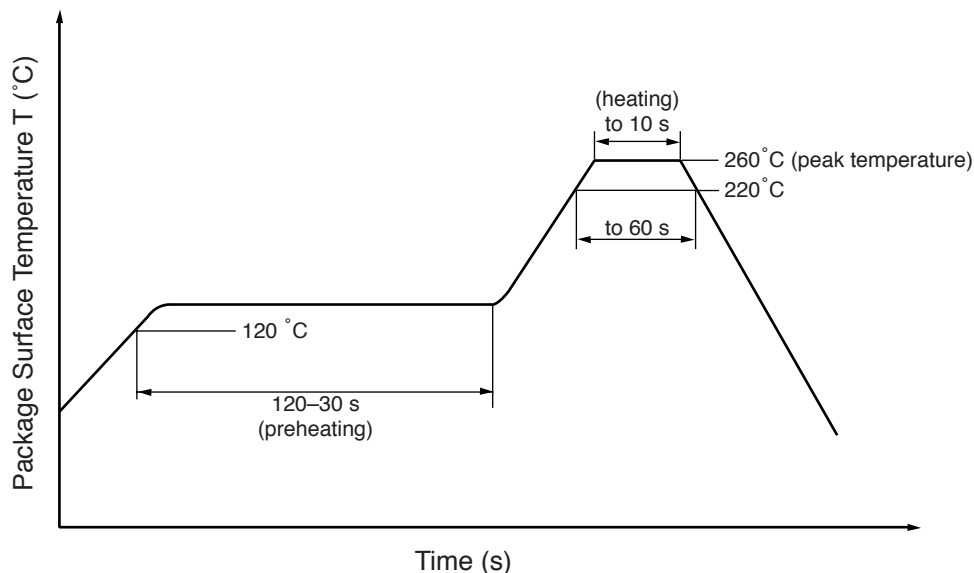
PS9614L



RECOMMENDED SOLDERING CONDITIONS

(1) Infrared reflow soldering

- Peak reflow temperature 260 °C or below (package surface temperature)
- Time of peak reflow temperature 10 seconds or less
- Time of temperature higher than 220 °C 60 seconds or less
- Time to preheat temperature from 120 to 180°C 120±30 s
- Number of reflows Three
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt % is recommended.)



(2) Wave Soldering

- Temperature 260°C or below
- Time 10 seconds or less
- Preheating conditions 120°C or below (package surface temperature)
- Number of times One (allowed to be dipped in solder including plastic mold portion)
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt % is recommended.)

(3) Cautions

• Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

Life Support Applications

These NEC products are not intended for use in life support devices, appliances, or systems where the malfunction of these products can reasonably be expected to result in personal injury. The customers of CEL using or selling these products for use in such applications do so at their own risk and agree to fully indemnify CEL for all damages resulting from such improper use or sale.

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