

SURFACE MOUNT LED LAMP

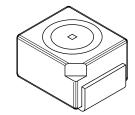
PLCC - 2 PACKAGE

PACKAGE DIMENSIONS 0.118 (3.0) 0.083 (2.1) 0.067 (1.7) 0.102 (2.6) 0.091 (2.3) .041 (0.1) 0.083 (2.1) 0.035 (0.9) 0.028 (0.7) 0.134 (3.4) 0.094 (2.4) 0.118 (3.0) 0.043 (1.1) 0.020 (0.5) 0.024 (0.6) 0.016 (0.4) 0.007 (.18) 0.005 (.12) - CATHODE NOTE: Dimensions for all drawings are in inches (mm).

ORANGE QTLP670C-8 PURE GREEN QTLP670C-5

FEATURES

- Non-diffused package excellent for back-lighting and coupling to light pipe
- · Low package profile
- · Low power dissipation
- Wide viewing angle of 120°



DESCRIPTION

This surface mount lamp is designed with a flat top and sides for automatic placement equipment. It is compatible with convective IR and vapor phase reflow soldering and conductive epoxy attachment process. The package size and configuration conform to EIA-535 BAAC standard specification for case size 3528 tantalum capacitor.

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise specified)				
Parameter	Symbol	Rating	Unit	
Operating Temperature	T _{OPR}	-40 to +85	°C	
Storage Temperature	T _{STG}	-40 to +100	°C	
Lead Soldering Time - Reflow	T _{SOL}	240 for 5 sec	°C	
Continuous Forward Current	I _F	30	mA	
Peak Forward Current	1	160	mA	
(f = 1.0 KHz, Duty Factor = 1/10)	¹ F			
Reverse Voltage	V _R (I _R = 10 μA)	5	V	
Power Dissipation	P _D	100	mW	

ELECTRICAL / OPTICAL CHARACTERISTICS (TA =25°C)				
Part Number	QTLP670C-8	QTLP670C-5	Condition	
	ORANGE	PURE GREEN		
Luminous Intensity (mcd)			I _F = 20 mA	
Minimum	9	4		
Typical	15	6		
Forward Voltage (V)			I _F = 20 mA	
Maximum	2.8	2.8		
Typical	2.0	2.0		
Peak Wavelength (nm)	610	555	I _F = 20 mA	
Spectral Line Half Width (nm)	40	30	I _F = 20 mA	
Viewing Angle (°)	120	120	$I_F = 20 \text{ mA}$	



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TYPICAL PERFORMANCE CURVES

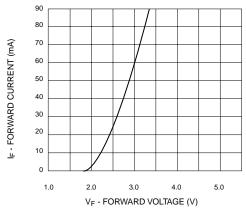


Fig. 1 Forward Current vs. Forward Voltage

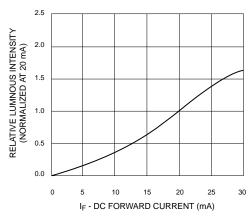


Fig. 2 Relative Luminous Intensity vs.
DC Forward Current

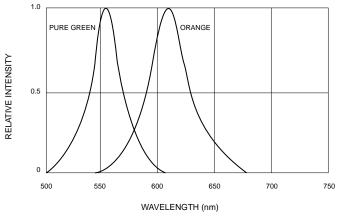
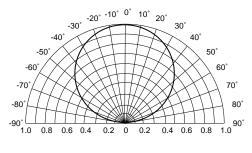


Fig. 3 Relative Intensity vs. Peak Wavelength



REL. LUMINOUS INTENSITY (%)

Fig.4 Radiation Diagram

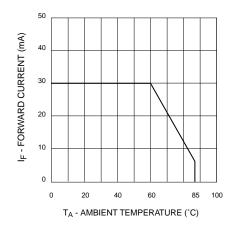


Fig. 5 Current Derating Curve

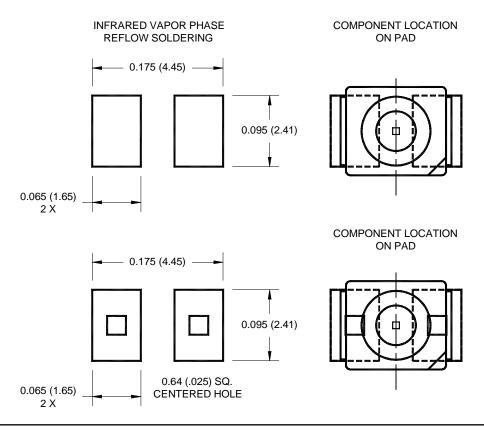


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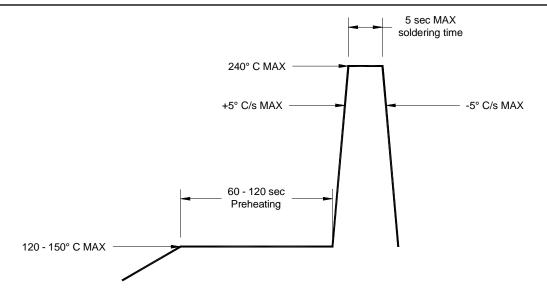
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RECOMMENDED PRINTED CIRCUIT BOARD PATTERN



RECOMMENDED IR REFLOW SOLDERING PROFILE





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