RICOH

R3119x SERIES

36V INPUT VOLTAGE DETECTOR

NO.EA-187-160229

OUTLINE

R3119x Series are CMOS-based 36V input (absolute maximum ratings: 50V) voltage detector with high detector threshold accuracy and ultra-low supply current. Each of those ICs consists of a voltage reference unit, a comparator, resistors for detector threshold setting, an output driver and a hysteresis circuit.

There are two types: R3119xxxxA has the C_D pin for setting the output delay time. R3119xxxxE has the SENSE pin.

The supply current of IC is only 3.3µA. The detector threshold is fixed in the IC and can be set with a step of 0.1V in the range of 2.3V to 12V. Detector threshold accuracy is 1.5%. The output type is Nch Open drain type. The R3119x is offered in a small-size 6-pin DFN(PLP)1820-6 package in addition to a 5-pin SOT-23-5 package.

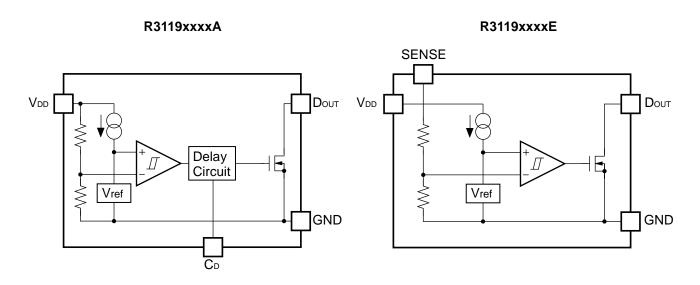
FEATURES

Supply Current	Τyp. 3.3μA
Operating Voltage Range	1.2V to 36.0V (C _D pin type: R3119xxxxA)
	2.1V to 6.0V (SENSE pin type: R3119xxxxE)
Operating Temperature Range	40°C to 105°C
Detector Threshold Range	2.3V to 12.0V (0.1V steps)
	(For other voltages, please refer to MARK INFORMATIONS.)
Detector Threshold Accuracy	±1.5% (Topt=25°C)
Temperature-Drift Coefficient of Detector Threshold	dTyp. ±100ppm/°C
• Output Delay Time (Power ON Reset Delay Time).	Typ. 85ms (C⊳=0.01μF, C⊳ pin type)
Output Delay Time Accuracy	50% to 80% (C _D pin type: R3119xxxxA)
Output Type	Nch Open Drain
Package	SOT-23-5, DFN(PLP)1820-6

APPLICATIONS

- · CPU and Logic Circuit Reset
- · Battery Checker
- Battery Back-up Circuit
- Power Failure Detector for Digital home appliances

BLOCK DIAGRAMS



SELECTION GUIDE

The package type, the detector threshold and the version for the ICs can be selected at the users' request.

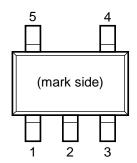
Product Name Package		Quantity per Reel	Pb Free	Halogen Free	
R3119Nxxx*-TR-FE	SOT-23-5	3,000 pcs	Yes	Yes	
R3119Kxxx*-TR	DFN(PLP)1820-6	5,000 pcs	Yes	Yes	

xxx: The detector threshold can be designated in the range from 2.3V(023) to 12.0V(120) in 0.1V steps. (For other voltages, please refer to MARK INFORMATIONS.)

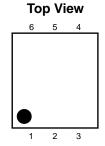
- * : Designation of Version
 - (A) with C_D pin type
 - (E) with SENSE pin type

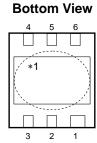
PIN CONFIGURATIONS





●DFN(PLP)1820-6





PIN DESCRIPTIONS

• SOT-23-5

Pin No.	Symbol	Description					
1	V_{DD}	Input Pin					
2	GND*	Ground Pin					
3	GND*	Ground Pin					
4	D оит	Output Pin ("L" at detection)					
5	С	R3119NxxxA Connecting pin with external capacitor for setting delay time					
5	SENSE	R3119NxxxE	Voltage Detector Voltage Sense Pin				

^{*)} No. 2 and No.3 pins must be wired to the GND plane when it is mounted on board.

● DFN(PLP)1820-6

Pin No.	Symbol	Description					
1	GND	Ground Pin					
2	NC	No Connection					
3	V _{DD}	Input Pin					
4	С	R3119KxxxA	Connecting pin with external capacitor for setting delay time				
4	SENSE	R3119KxxxE	Voltage Detector Voltage Sense Pin				
5	NC	No Connection					
6	D оит	Output Pin ("	L" at detection)				

^{*)} Tab is GND level. (They are connected to the reverse side of this IC.) The tab is better to be connected to the GND, but leaving it open is also acceptable.

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ABSOLUTE MAXIMUM RATINGS

Symbol	Item	Rating	Unit	
V _{DD}	Cupply Voltage	R3119xxxxA	-0.3 to 50.0	V
V DD	Supply Voltage	R3119xxxxE	-0.3 to 7.0	V
Vouт	Output Voltage (Dout Pin)		-0.3 to 7.0	V
Vcd	Output Voltage (C _D Pin)	Output Voltage (C _D Pin) R3119xxxxA		V
Vsense	Input Voltage (SENSE Pin) R3119xxxxE		-0.3 to 50.0	V
louт	Output Current (Dout Pin)	Output Current (Dout Pin)		
PD	Power Dissipation (SOT-23-5)*		420	mW
PD	Power Dissipation (DFN(PLP)1820-6)*		880	IIIVV
Topt	Operating Temperature Range	-40 to 105	°C	
Tstg	Storage Temperature Range	-55 to 125	°C	

^{*)} For Power Dissipation, please refer to PACKAGE INFORMATION.

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field.

The functional operation at or over these absolute maximum ratings is not assured.

ELECTRICAL CHARACTERISTICS

• R3119xxxxA (Cp pin type)

The specification in ____ is checked and guaranteed by design engineering at $-40^{\circ}\text{C} \le \text{Topt} \le 105^{\circ}\text{C}$.

Topt=25°C

Symbol	Item	Conditions			Min.	Тур.	Max.	Unit
-V _{DET}	Detector Threehold	V pip	Topt=25	5°C	×0.985		×1.015	>
- V DE I	Detector Threshold	V _{DD} pin	-40°C	≤ Topt ≤ 105°C	×0.970		×1.020	V
V _{HYS}	Detector Threshold Hysteresis				3.5	5	6.5	%
Iss	Supply Current	V _{DD} = -V _{DET} -0.1	V			3.3	5.6	
155	Зарріу Сапепі	V _{DD} = -V _{DET} +1.0	V			3.3	5.5	μА
V _{DDH}	Maximum Operating Voltage						36	>
V _{DDL}	Minimum Operating	Topt=25°C					1.2	V
V DDL	Voltage*	-40°C ≤ Topt ≤	105°C				1.25	٧
		V _{DD} =1.5V, V _{DS} =0.05V			230			μА
	Output Current	2.3V ≤ -V _{DET} <	2.6V	V _{DD} =2.2V V _{DS} =0.5V	2.8			
Іоит	(Driver Output Pin)	2.6V ≤ -V _{DET} <	3.0V	V _{DD} =2.5V V _{DS} =0.5V	3.3			mA
		3.0V ≤ - V _{DET}		V _{DD} =2.9V V _{DS} =0.5V	3.5			
ILEAK	Nch Driver Leakage Current	VDD=36V, VDS=6.0V				0.2	μΑ	
Δ -V _{DET} / Δ Topt	Detector Threshold Temperature Coefficient	$-40^{\circ}C \le T_{opt} \le 105^{\circ}C$				±100		ppm /°C
tdelay	Detector Output Delay Time	$V_{DD}=1.5V \rightarrow -V_{DET}+2.0V$ $C_{D}=0.01\mu F$		45	85	150	ms	

All of unit are tested and specified under load conditions such that Tj≈Topt=25°C except for Detector Threshold Temperature Coefficient.

*) This value is the minimum input voltage when the output voltage is 0.1V or less at detection. (The pull-up resistance; $100k\Omega$, the pull-up voltage; 5.0V)

RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

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• R3119xxxxE (SENSE pin type)

The specification in ____ is checked and guaranteed by design engineering at $-40^{\circ}\text{C} \le \text{Topt} \le 105^{\circ}\text{C}$.

Topt=25°C

Symbol	Item	Conditions			Min.	Тур.	Max.	Unit
V _{DD}	Operating Voltage				2.1*		6	٧
	Detector Threehold	SENSE pin Topt=25°C		×0.985		×1.015	V	
-VDET	Detector Threshold	V _{DD} =6V	1,, 6,,		×0.970		×1.020	V
V _H ys	Detector Threshold Hysteresis	V _{DD} =6V			3.5	5	6.5	%
Iss	Supply Current	VDD=6V, VSENSE	== -V det-(0.1V		3.3	5.5	
188	Supply Current	VDD=6V, VSENSE	== -V DET+	1.0V		3.3	5.5	μΑ
RSENSE	Sense Resistor				4.5		120	МΩ
Іоит	Output Current			V _{DD} =2.1V V _{DS} =0.05V	420			μΑ
1001	(Driver Output Pin)	Vsense < -Vdet		V _{DD} =2.2V V _{DS} =0.5V	2.8			mA
ILEAK	Nch Driver Leakage Current	VDD=6V, VSENSE=36V, VDS=6.0V					0.2	μΑ
Δ -V _{DET} / Δ Topt	Detector Threshold Temperature Coefficient	$-40^{\circ}C \le T_{opt} \le 105^{\circ}C$				±100		ppm /°C
t PLH	Output Delay Time	$V_{DD}=6V$ $V_{SENSE}=1.5V \rightarrow -V_{DET}+2.0V$			15		μS	
Vsense	Input Voltage (SENSE Pin)			0		36	>	

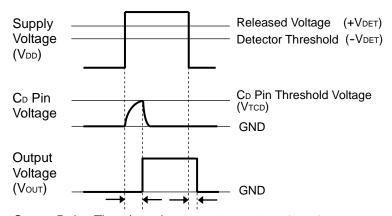
All of unit are tested and specified under load conditions such that Tj≈Topt=25°C except for Detector Threshold Temperature Coefficient and Output Delay Time.

*) Minimum operating voltage of "SENSE pin type" is minimum supply voltage to obtain correct detection voltage.

RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

TIMING CHART



Output Delay Time (tdelay) Detect Delay Time (treset)

When the supply voltage, which is higher than released voltage, is forced to V_{DD} pin, charge to an external capacitor starts, then C_D pin voltage increases. Until the C_D pin voltage reaches to C_D pin threshold voltage, output voltage maintains "L". When the C_D pin voltage becomes higher than C_D pin threshold voltage, output voltage is reversed from "L" to "H". Where the time interval between the rising edge of supply voltage and output voltage reverse point means output delay time.

When the output voltage reverses from "L" to "H", the external capacitor starts to discharge. Therefore, when lower voltage than the detector threshold voltage is forced to V_{DD} pin, the output voltage reverses from "H" to "L" thus the detect delay time is constant not being affected by the external capacitor.

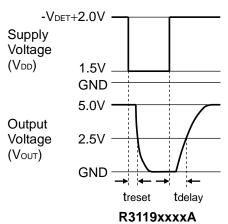
Output Delay Time

Output Delay Time (tdelay) can be calculated with the next formula using the external capacitor: $\text{tdelay} \left(s \right) = 8.5 \times 10^6 \times C_D \left(F \right)$

DEFINITION OF OUTPUT DELAY TIME

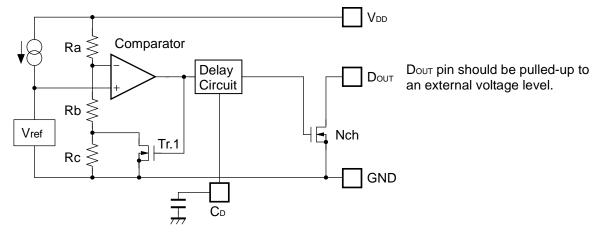
Output Delay Time (tdelay) is defined as follows:

Under the condition of the output pin (Dout) is pulled up through a resistor of $100k\Omega$ to 5V, the time interval between the rising edge of VDD pulse from 1.5V to (-VDET)+2.0V pulse voltage is supplied, the becoming of the output voltage to 2.5V.

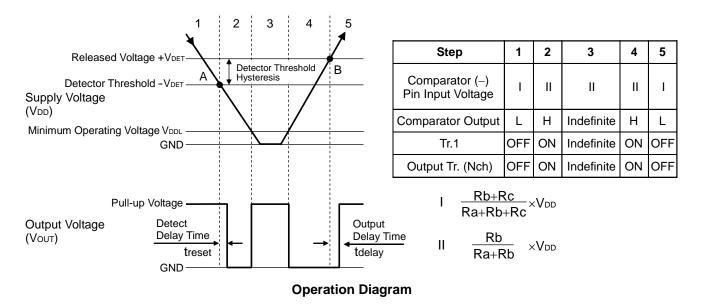


OPERATION

• Operation of R3119xxxxA (C_D pin type)



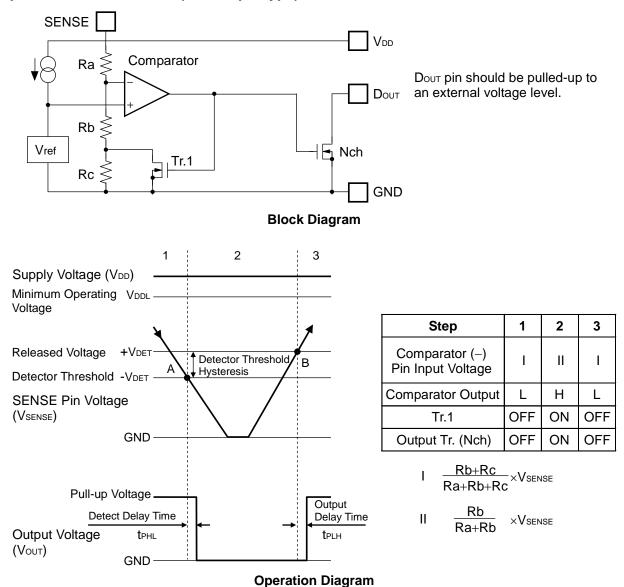
Block Diagram of External Capacitor Connection



Explanation of operation

- Step 1. The output voltage is equal to the pull-up voltage.
- Step 2. At Point "A", Vref ≥ VDD×(Rb+Rc)/(Ra+Rb+Rc) is true, as a result, the output of comparator is reversed from "L" to "H", therefore the output voltage becomes the GND level. The voltage level of Point A means a detector threshold voltage (-VDET).
- Step 3. When the supply voltage is lower than the minimum operating voltage, the operation of the output transistor becomes indefinite. The output voltage is equal to the pull-up voltage.
- Step 4. The output voltage is equal to the GND level.
- Step 5. At Point "B", Vref ≤ VDD×Rb/(Ra+Rb) is true, as a result, the output of comparator is reversed from "H" to "L", then the output voltage is equal to the pull-up voltage. The voltage level of Point B means a released voltage (+VDET).
- *) The difference between a released voltage and a detector threshold voltage is a detector threshold hysteresis.

• Operation of R3119xxxxE (SENSE pin type)



Explanation of operation

- Step 1. SENSE pin voltage is larger than detector threshold; the output voltage is equal to the pull-up voltage.
- Step 2. At Point "A", Vref ≥ Vsense×(Rb+Rc)/(Ra+Rb+Rc) is true, as a result, the output of comparator is reversed from "L" to "H", therefore the output voltage becomes the GND level. The voltage level of Point A means a detector threshold voltage (-VDET). (When the supply voltage is higher than the minimum operating voltage, the output voltage is equal to the GND level.)
- Step 3. At Point "B", Vref ≤ Vsense×Rb/(Ra+Rb) is true, as a result, the output of comparator is reversed from "H" to "L", then the output voltage is equal to the pull-up voltage. The voltage level of Point B means a released voltage (+VDET).
- *) The difference between a released voltage and a detector threshold voltage is a detector threshold hysteresis.

Power supply injection order

The R3119xxxxE Series supervise the voltage of the SENSE pin. V_{DD} pin and SENSE pin can be used at the same voltage level. Likewise, V_{DD} pin and SENSE pin can be used at the different voltage level. If the V_{DD} pin and SENSE pin are used at different voltage level, regarding the start-up sequence, force the voltage level to V_{DD} pin prior to the SENSE pin.

If the SENSE pin voltage is equal or more than the released voltage ($+V_{DET}$), D_{OUT} pin becomes "H"(Fig.1). Besides, a voltage beyond V_{DD} pin is also acceptable to SENSE pin.

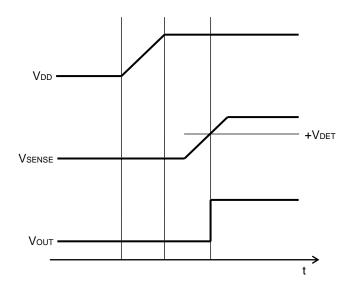
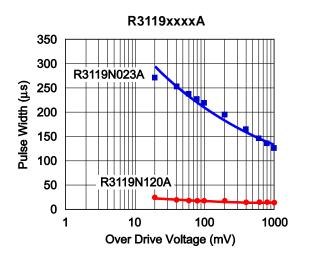
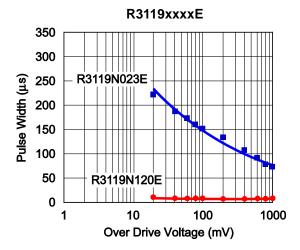


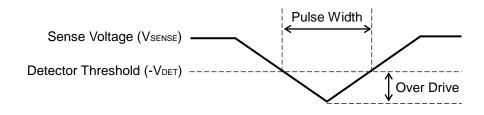
Fig.1 Turn on sequence

Detector Operation vs. glitch input voltage to the VDD pin or SENSE pin

When the R3119x is at released, if the pulse voltage which the detector threshold or lower voltage, the graph below means that the relation between pulse width and the amplitude of the swing to keep the released state for the R3119x.





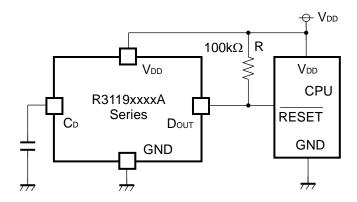


VSENSE Input Waveform

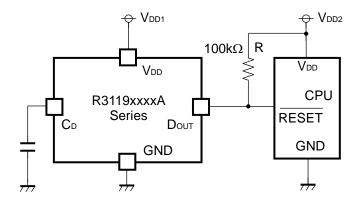
This graph shows the maximum pulse conditions to keep the released voltage. If the pulse with larger amplitude or wider width than the graph above, is input to the V_{DD} pin (R3119xxxxA) or to the SENSE pin (R3119xxxxE), the reset signal may be output.

TYPICAL APPLICATION

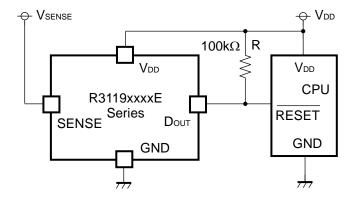
- R3119xxxxA (Cp pin type)
- (1) Input Voltage to R3119xxxxA is equal to Input Voltage to CPU



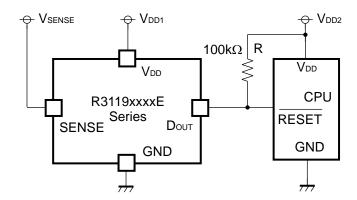
(2) Input Voltage to R3119xxxxA is unequal to Input Voltage to CPU



- R3119xxxxE (SENSE pin type)
- (1) Input Voltage to R3119xxxxE is equal to Input Voltage to CPU

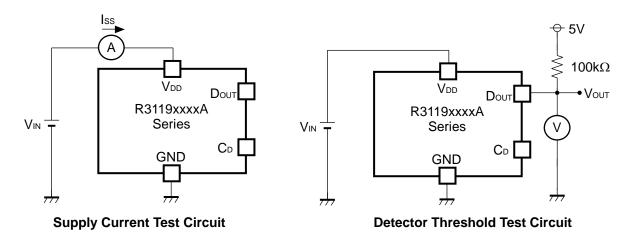


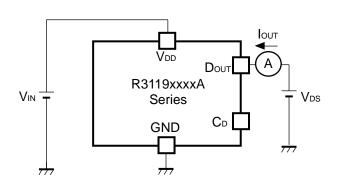
(2) Input Voltage to R3119xxxxE is unequal to Input Voltage to CPU

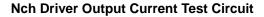


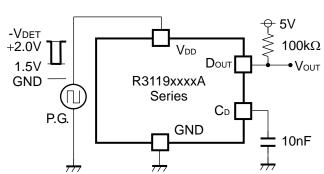
TEST CIRCUITS

• R3119xxxxA (C_D pin type)



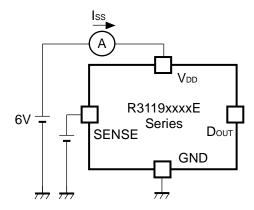




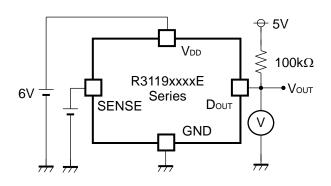


Output Delay Time Test Circuit

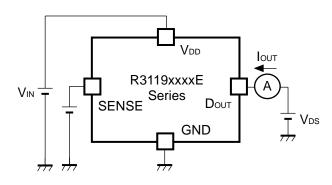
• R3119xxxxE (SENSE pin type)



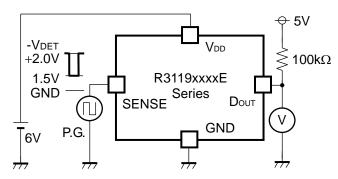
Supply Current Test Circuit



Detector Threshold Test Circuit



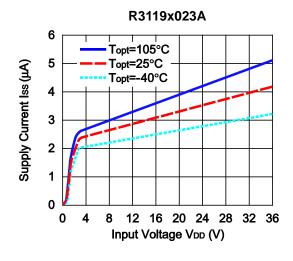
Nch Driver Output Current Test Circuit

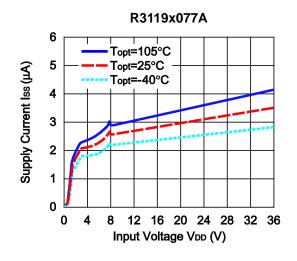


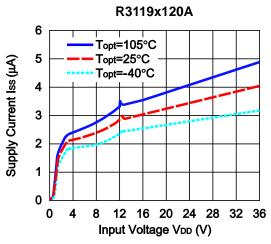
Output Delay Time Test Circuit

TYPICAL CHARACTERISTICS

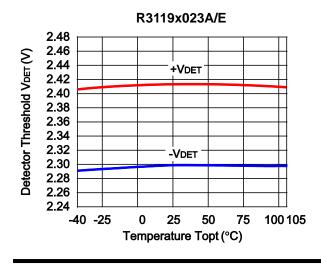
1) Supply Current vs. Input Voltage

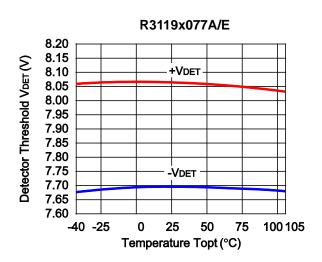




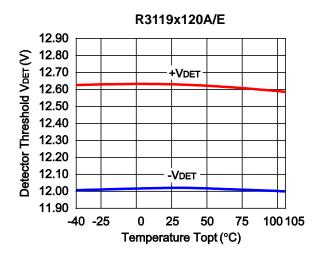


2) Detector Threshold vs. Temperature

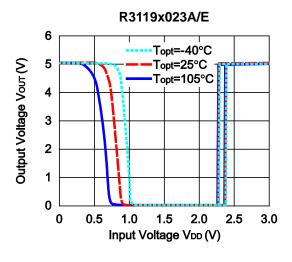


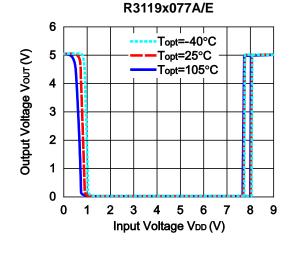


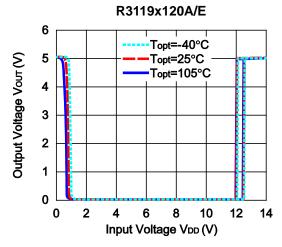
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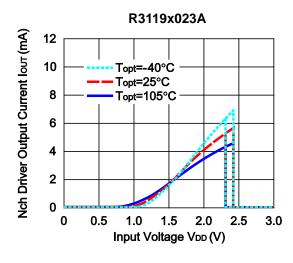
3) Output Voltage vs. Input Voltage

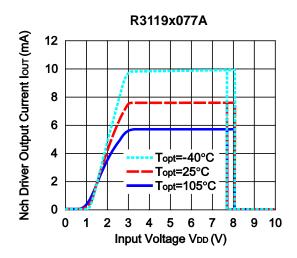


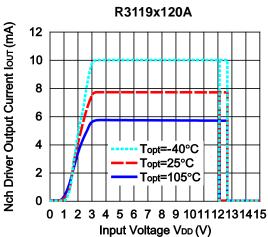




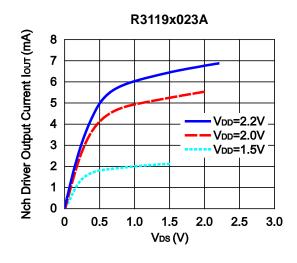
4) Nch Driver Output Current vs. Input Voltage

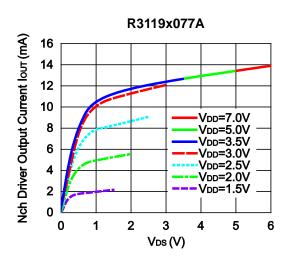




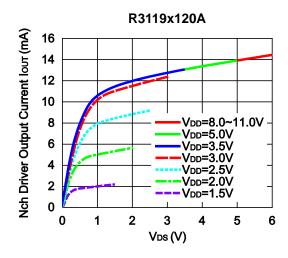


5) Nch Driver Output Current vs. VDS

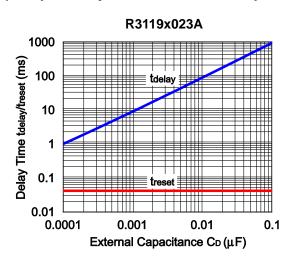


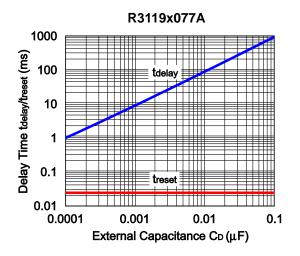


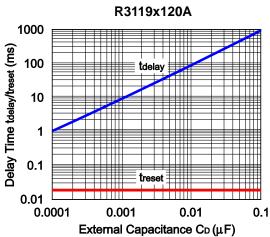
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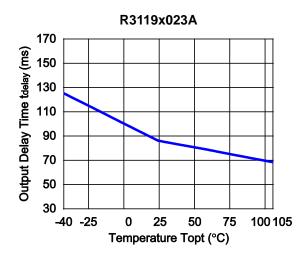
6) Output Delay Time vs. External Capacitance (Topt=25°C)

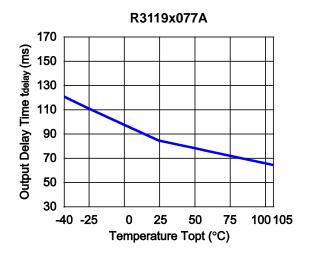


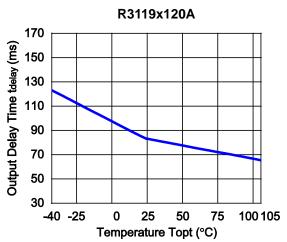




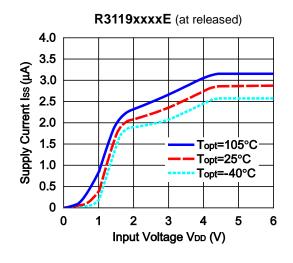
7) Output Delay Time vs. Temperature (CD=0.01μF)

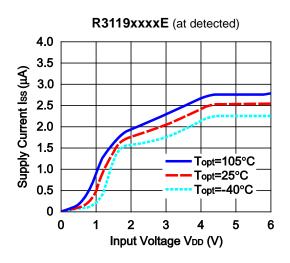






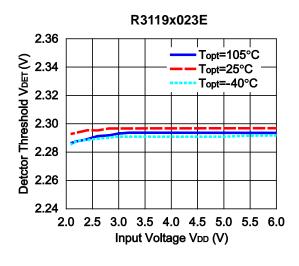
8) Supply Current vs. Input Voltage

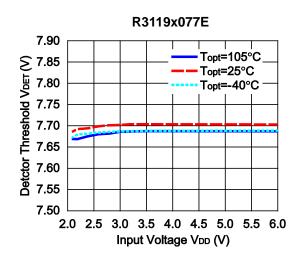


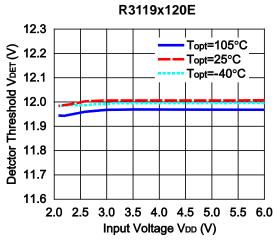


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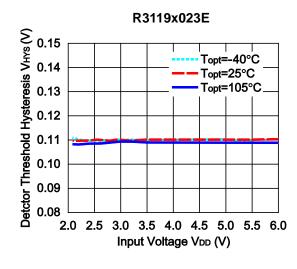
9) Detector Threshold vs. Input Voltage

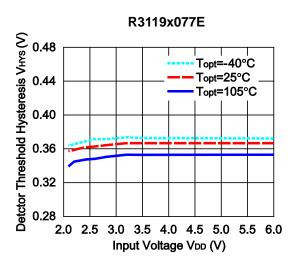


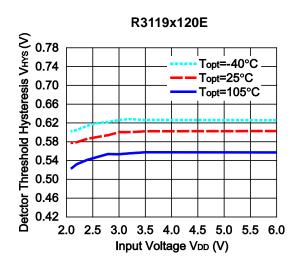




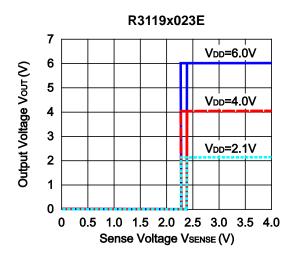
10) Detector Threshold Hysteresis vs. Input Voltage

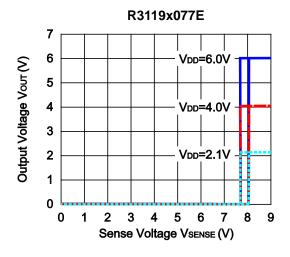


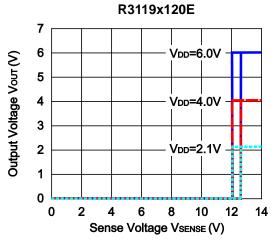




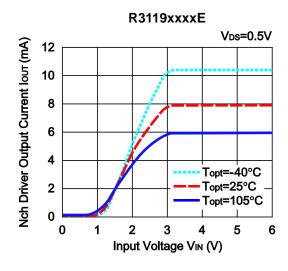
11) Output Voltage vs. SENSE pin Input Voltage (Topt=25°C) (Dout pull up to VDD with 100kΩ)



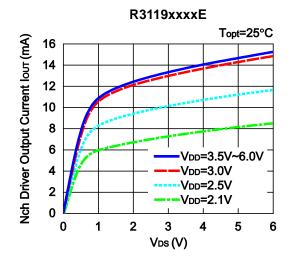




12) Nch Driver Output Current vs. Input Voltage



13) Nch Driver Output Current vs. VDS



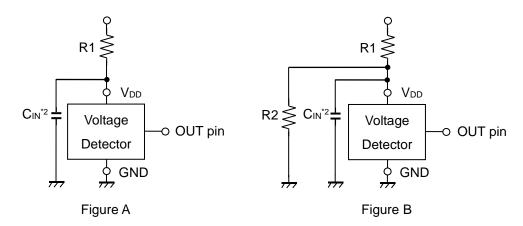
TECHNICAL NOTES

When connecting resistors to the device's input pin

When connecting a resistor (R1) to an input of this device, the input voltage decreases by [Device's Consumption Current] x [Resistance Value] only. And, the cross conduction current*1, which occurs when changing from the detecting state to the release state, is decreased the input voltage by [Cross Conduction Current] x [Resistance Value] only. And then, this device will enter the re-detecting state if the input voltage reduction is larger than the difference between the detector voltage and the released voltage.

When the input resistance value is large and the VDD is gone up at mildly in the vicinity of the released voltage, repeating the above operation may result in the occurrence of output.

As shown in Figure A/B, set R1 to become 100 k Ω or less as a guide, and connect C_{IN} of 0.1 μ F and more to between the input pin and GND. Besides, make evaluations including temperature properties under the actual usage condition, with using the evaluation board like this way. As a result, make sure that the cross conduction current has no problem.



^{*1} In the CMOS output type, a charging current for OUT pin is included.

^{*2} Note the bias dependence of capacitors.



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