COMPLIANT



Heatsink Encased Wirewound Power Resistors

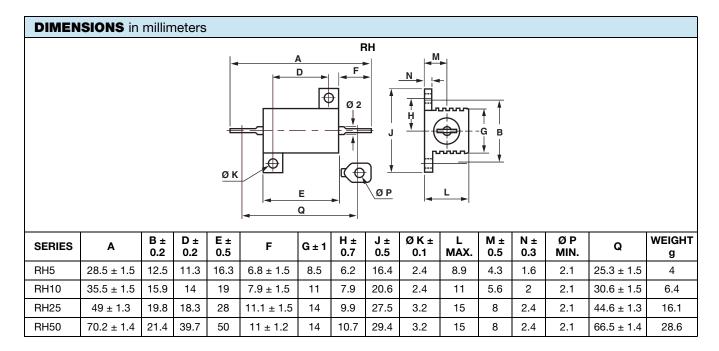


FEATURES

- 5 W to 50 W at 25 °C
- NF C 83-210
- According to CECC 40 203
- High stability < 0.05 % year
- Low temperature coefficient typically ± 15 ppm/°C
- Wide range of values from 0.006 Ω to 130 $k\Omega$
- Termination = Sn/Ag/Cu
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

Encased in a compact and light heatsink offering complete environmental protection, great mechanical strength and easy mounting. Non inductive versions can be supplied under the RHNI designation (please indicate required specifications and frequency range upon ordering).

NF F 16101, 10/1988 and 16102, 04/1992: Not applicable (our parts contain less than 10 g of combustible materials).



STANDARD ELECTRICAL SPECIFICATIONS					
MODEL	RESISTANCE RANGE Ω	RATED POWER P _{25°C} W	TOLERANCE ± %		
RH5	0.01 to12K	10	0.5, 1, 2, 5		
RH10	0.006 to 20K	12.5	0.5, 1, 2, 5		
RH25	0.006 to 62K	25	0.5, 1, 2, 5		
RH50	0.006 to 130K	50	0.5, 1, 2, 5		

Vishay Sfernice

TECHNICAL SPECIFICATIONS						
VISHAY SFERNICE MODEL ANI	O STYLE		RH5	RH10	RH25	RH50
POWER RATING	MIL	25 °C	5W	10 W	20 W	30 W
Chassis Mounted Resistors	Limits	70 °C	4 W	8 W	16 W	24 W
413 cm ² for RH5 and RH10 536 cm ² for RH25 and RH50	Vishay Sfernice	25 °C	10 W	12.5 W	25 W	50 W
	Limits	70 °C	8 W	10 W	20 W	40 W
Unmounted Resistors	Vishay Sfernice Limits	25 °C	4 W	6 W	9W	12 W
		70 °C	3.2 W	4.8 W	7.2 W	9.6 W
Rated Maximum Voltage (V _{RMS})			160 V	250 V	550 V	1285 V
Dielectric Strength V _{RMS}			1000 V	1500 V	2500 V	2500 V
	Vishay Sfernice		0.01 Ω 12 kΩ	0.006 Ω 20 kΩ	0.006 Ω 62 kΩ	0.006 Ω 130 kΩ
	E 96	± 0.1 %	1 Ω		1 Ω	
Minimum Ohmic Values in Relation to Tolerance	E 96	± 0.5 %	0.1 Ω		0.1 Ω	
	E 96	±1%	0.1 Ω		0.05 Ω	
	E 48	± 2 %	0.01 Ω		0.01 Ω	
	E 24	± 5 %	0.01 Ω		0.01 Ω	
	E 12	± 10 %	0.01 Ω 0.008 Ω		0.006 Ω	

PERFORMANCE						
MIL	TYPICAL DRIFTS					
TESTS	CONDITIONS		REQUIREMENTS	THICAL DRIFTS		
Operating Temperature Range	-	55 °C + 200 °C		-	-	
Momentary Overload		5 P _r /5 s		± (0.25 % + 0.05 Ω)	± (0.1 % + 0.05 Ω)	
Climatic Sequence	- 55 °C + 200 °C 5 cycles		itic Sequence		± (0.25 % + 0.05 Ω)	± (0.1 % + 0.05 Ω)
Load Life Test at High Temperature	2 h at + 275 °C		\pm (1 % + 0.05 Ω) Ins. resistance \geq 1 GΩ	± (0.1 % + 0.05 Ω)		
Humidity (Steady State)	56 days		\pm (1 % + 0.05) Ins. resistance \geq 100 M Ω	± (0.5 % + 0.05 Ω)		
Resistance to Moisture	Climatic sequences test, with load and polarisation		± (1 % + 0.05 Ω)	± (0.5 % + 0.05 Ω)		
Temperature Coefficient	5 Ω to 10 Ω > 10 Ω				± 15 ppm/°C	
Load Life	1000 h 25 °C	$P_{n}MIL$	Vishay	± (1 % + 0.05 Ω)	± (0.1 % + 0.05 Ω)	
at Maximum Temperature	200 °C	30 % of P _n	Sfernice	Ins. resistance \geq 1 G Ω	± (0.5 % + 0.05 Ω)	

MOMENTARY OVERLOAD

1. Momentary overload (> 2 s):

See example in table below. In all cases, it should be understood that:

- The 12 P_n overload applies only to ohmic values 0.1.
- The overload voltage shall not be higher than that used for the dielectric strength test (see Standard Electrical Specifications).

2. Short time overload (< 2 s):

For times shorter than 2 s, higher overloads can be sustained in some cases. Consult Vishay Sfernice.

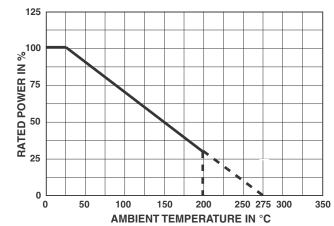
POWER LOADING	DURATION		
2.5 P _n	10 s		
5 P _n	5 s		
12 <i>P</i> _n	2 s		





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POWER RATING



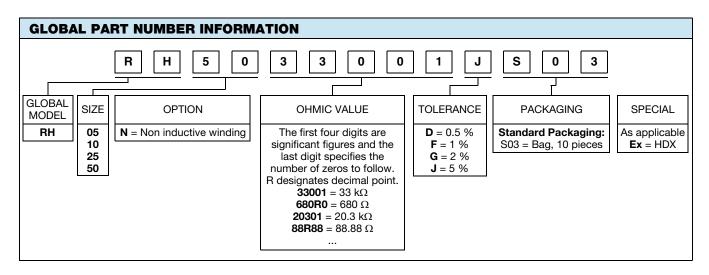
TEMPERATURE RISE 250 N 200 N 200 N 200 N 200 N 200 N 200 N RATED POWER IN W (Mounted on heatsink chassis)

MARKING

Vishay Sfernice trademark, model, style, nominal resistance (in Ω), tolerance (in %), manufacturing date.

PACKAGING
Bag of 10 units

ORDERING INFORMATION							
RH	05	N	18R00	J	S03		
MODEL	STYLE	NON INDUCTIVE WINDING Optional	OHMIC VALUE	TOLERANCE	PACKAGING		





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