Unit: mm

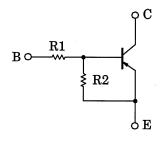
TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT Process) (Bias Resistor built-in Transistor)

RN2130MFV

Switching Applications Inverter Circuit Applications Interface Circuit Applications

- **Driver Circuit Applications**
- With built-in bias resistorsSimplify circuit design
- Reduce a quantity of parts and manufacturing process
- Complementary to RN1130MFV

Equivalent Circuit



Absolute Maximum Ratings (Ta = 25°C)

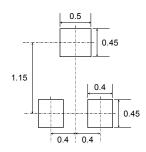
Characteristic	Symbol	Rating	Unit	
Collector-base voltage	V _{CBO}	-50	V	
Collector-emitter voltage	V _{CEO}	-50	V	
Emitter-base voltage	V _{EBO}	-10	V	
Collector current	IC	-100	mA	
Collector power dissipation	P _C (Note1)	150	mW	
Junction temperature	Tj	150	°C	
Storage temperature range	T _{stg}	-55 to 150	°C	

Note1: Mounted on FR4 board (25.4 mm × 25.4 mm × 1.6 mmt)

Note2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Land Pattern Example Unit: mm



1.BASE
VESM
2.EMITTER
3.COLLECTOR

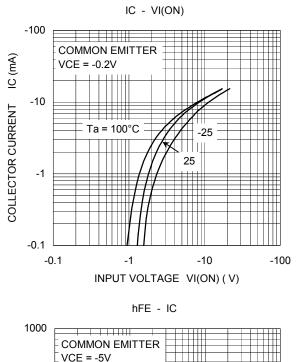
JEDEC
JEITA
TOSHIBA
2-1L1A

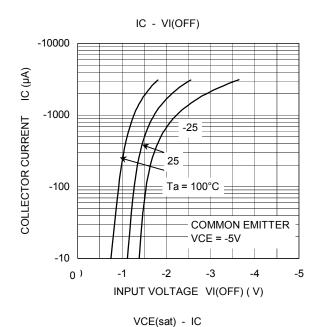
Weight: 1.5 mg (typ.)

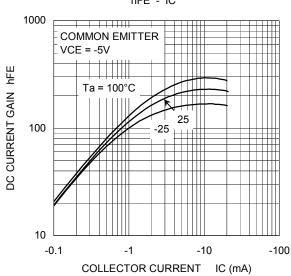
Start of commercial production 2005-04

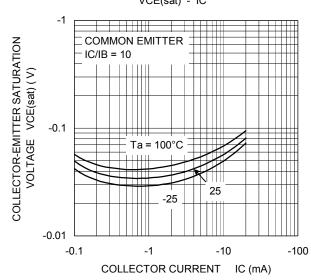
Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	$V_{CB} = -50 \text{ V}, I_{E} = 0$	_	_	-100	nA
	I _{CEO}	$V_{CE} = -50 \text{ V}, I_B = 0$	_	_	-500	nA
Emitter cut-off current	I _{EBO}	V _{EB} = -10 V, I _C = 0	-38	_	-72	μΑ
DC current gain	h _{FE}	$V_{CE} = -5 \text{ V}, I_{C} = -10 \text{ mA}$	100	_	_	_
Collector-emitter saturation voltage	V _{CE} (sat)	$I_C = -5 \text{ mA}, I_B = -0.5 \text{ mA}$	_	-0.1	-0.3	V
Input voltage (ON)	V _{I(ON)}	$V_{CE} = -0.2 \text{ V}, I_{C} = -5 \text{ mA}$	-1.7	_	-8.2	V
Input voltage (OFF)	V _{I(OFF)}	$V_{CE} = -5 \text{ V}, I_{C} = -0.1 \text{ mA}$	-1.0	_	-1.6	V
Collector output capacitance	C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MH}_z$	_	0.9	_	pF
Input resistor	R1	_	70	100	130	kΩ
Resistor ratio	R1/R2	_	0.8	1.0	1.2	_









3 2014-03-01

Marking

Type Name	Marking	
RN2130MFV	Type Name Y2	

4 2014-03-01

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