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February 2015

MBRS140 Schottky Rectifier

Features

- Compact Surface Mount with J-bend Leads (SMB)
- 1.5 W Power Dissipation Package
- 1.0 A, Forward Voltage less than 600 mV



Ordering Information

Part Number	Top Mark	Package	Packing Method
MBRS140	B140	DO-214AA (SMB)	Tape and Reel

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{RRM}	Maximum Repetitive Reverse Voltage 40		V
I _{F(AV)}	Average Rectified Forward Current at T _L = 120°C 1.0		Α
I _{FSM}	Non-Repetitive Peak Forward Surge Current (8.3 ms, Single Half-Sine-Wave) 40		А
T _{STG}	Storage Temperature Range -65 to +150		°C
T _J	Operating Junction Temperature	-65 to +125	°C

Thermal Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
$R_{\theta JL}$	Thermal Resistance, Junction-to-Lead	12	°C/W

Electrical Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
V _F	Forward Voltage	I _F = 1.0 A		600	mV
I _R	Reverse Current	V _R = 40 V		1.0	mA
		$V_R = 40 \text{ V}, T_A = 100^{\circ}\text{C}$		10	

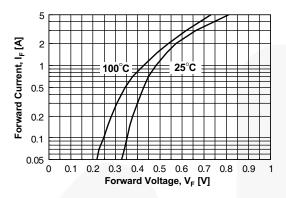
125°C ≣

100°C

75°C

25°C **■**

Typical Performance Characteristics



0.0001 10 15 20 25 30 Reverse Voltage, $V_R[V]$

Figure 2. Reverse Current vs. Reverse Voltage

Reverse Current, I_R [mA]

0.1

0.01

0.001

Figure 1. Forward Voltage Characteristics

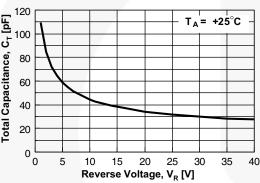


Figure 3. Total Capacitance

Physical Dimension

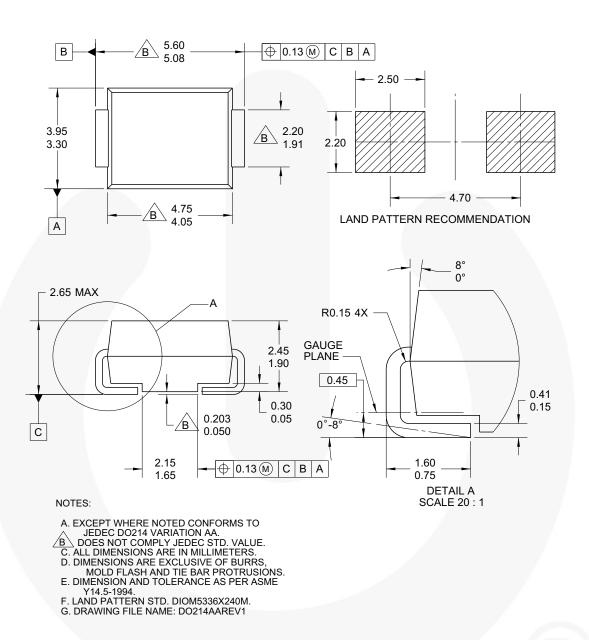


Figure 4. 2-LEAD, SMB, JEDEC DO-214, VARIATION AA





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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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