



## HIGH RIPPLE-REJECTION AND LOW DROPOUT CMOS VOLTAGE REGULATOR

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Rev.6.1\_01

The S-1112/1122 Series is a positive voltage regulator with a low dropout voltage, high-accuracy output voltage, and low current consumption developed based on CMOS technology.

A built-in low on-resistance transistor provides a low dropout voltage and large output current, and a built-in overcurrent protection circuit prevents the load current from exceeding the current capacity of the output transistor. An ON/OFF circuit ensures a long battery life. Compared with the voltage regulators using the conventional CMOS technology, a larger variety of capacitors are available, including small ceramic capacitors. Small SNT-6A(H) (S-1112 Series only) and SOT-23-5 packages realize high-density mounting. In SOT-23-5, the lineup includes the S-1112 and S-1122 Series, which differ in pin configuration.

### ■ Features

- Output voltage: 1.5 V to 5.5 V, selectable in 0.1 V step
- Output voltage accuracy:  $\pm 1.0\%$
- Dropout voltage: 190 mV typ. (3.0 V output product,  $I_{OUT} = 100$  mA)
- Current consumption: During operation: 50  $\mu$ A typ., 90  $\mu$ A max.  
During power-off: 0.1  $\mu$ A typ., 1.0  $\mu$ A max.
- Output current: Possible to output 150 mA ( $V_{IN} \geq V_{OUT(S)} + 1.0$  V)\*1
- Output capacitor: A ceramic capacitor of 0.47  $\mu$ F or more can be used.
- Ripple rejection: 80 dB typ. (f = 1.0 kHz)
- Built-in overcurrent protection circuit: Limits overcurrent of output transistor.
- Built-in ON/OFF circuit: Ensures long battery life.
- Operation temperature range:  $T_a = -40^\circ\text{C}$  to  $+85^\circ\text{C}$
- Lead-free, Sn 100%, halogen-free\*2

\*1. Attention should be paid to the power dissipation of the package when the output current is large.

\*2. Refer to “■ Product Name Structure” for details.

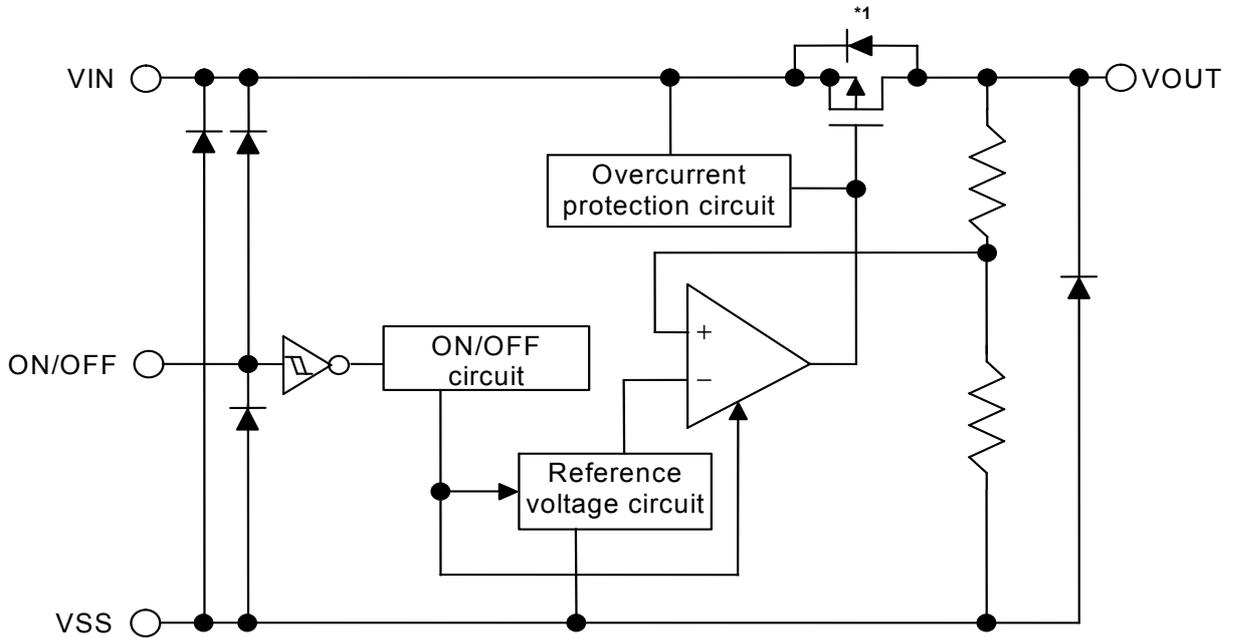
### ■ Applications

- Constant-voltage power supply for battery-powered device
- Constant-voltage power supply for personal communication device
- Constant-voltage power supply for home electric/electronic appliance
- Constant-voltage power supply for cellular phone

### ■ Packages

- SNT-6A(H)
- SOT-23-5

■ **Block Diagram**



\*1. Parasitic diode

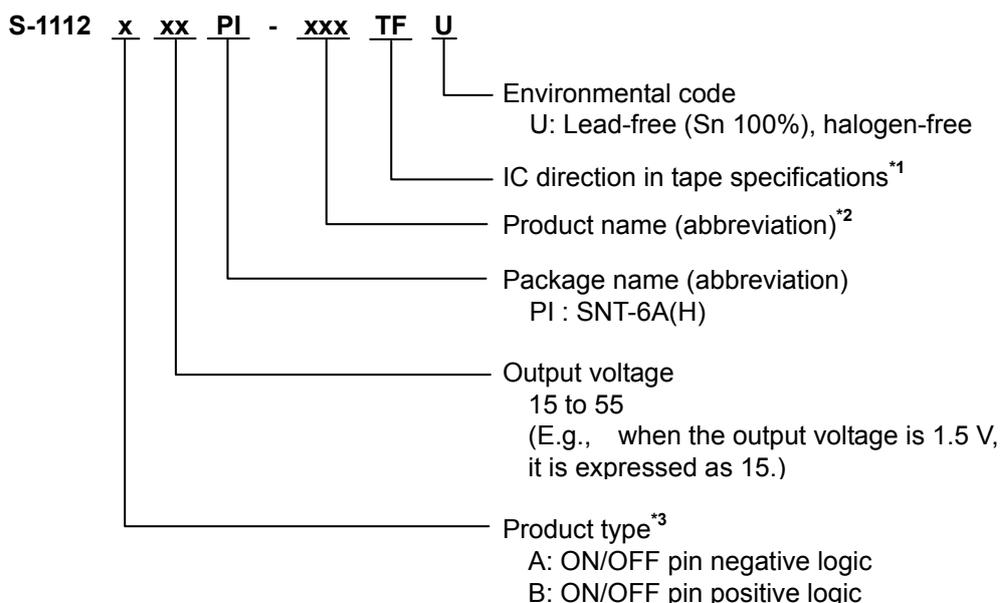
**Figure 1**

## ■ Product Name Structure

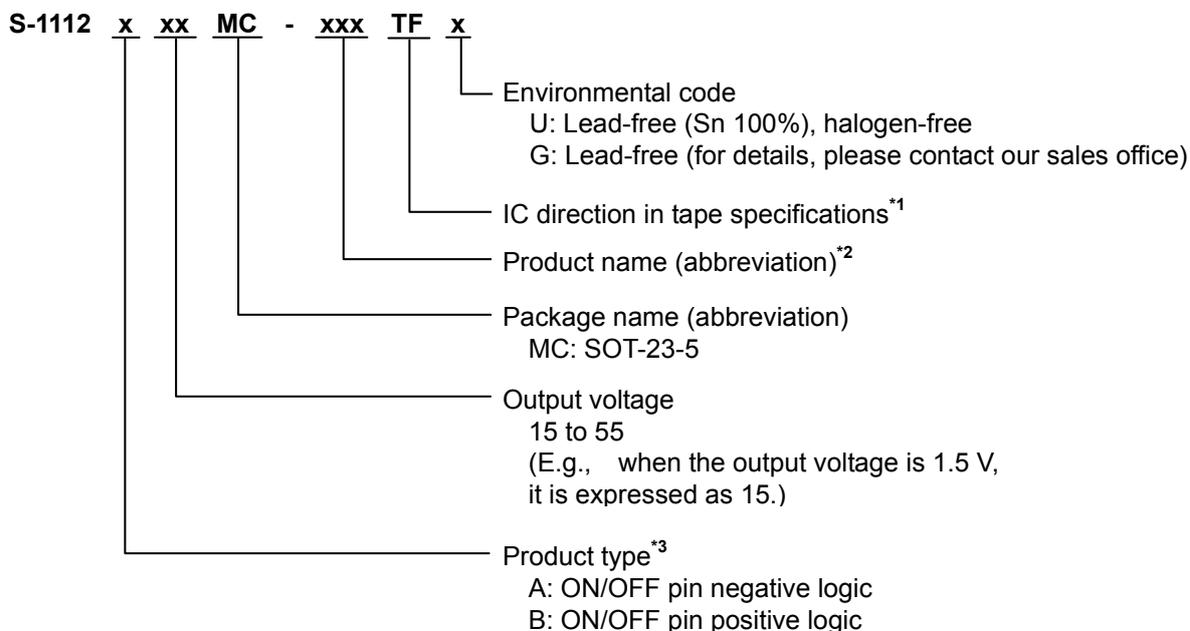
Users can select the product type, output voltage, and package type for the S-1112/1122 Series. Refer to “**1. Product name**” regarding the contents of product name, “**2. Packages**” regarding the package drawings, “**3. Product name lists**” regarding details of product name.

### 1. Product name

#### (1) SNT-6A(H) for S-1112 Series



#### (2) SOT-23-5 for S-1112 Series

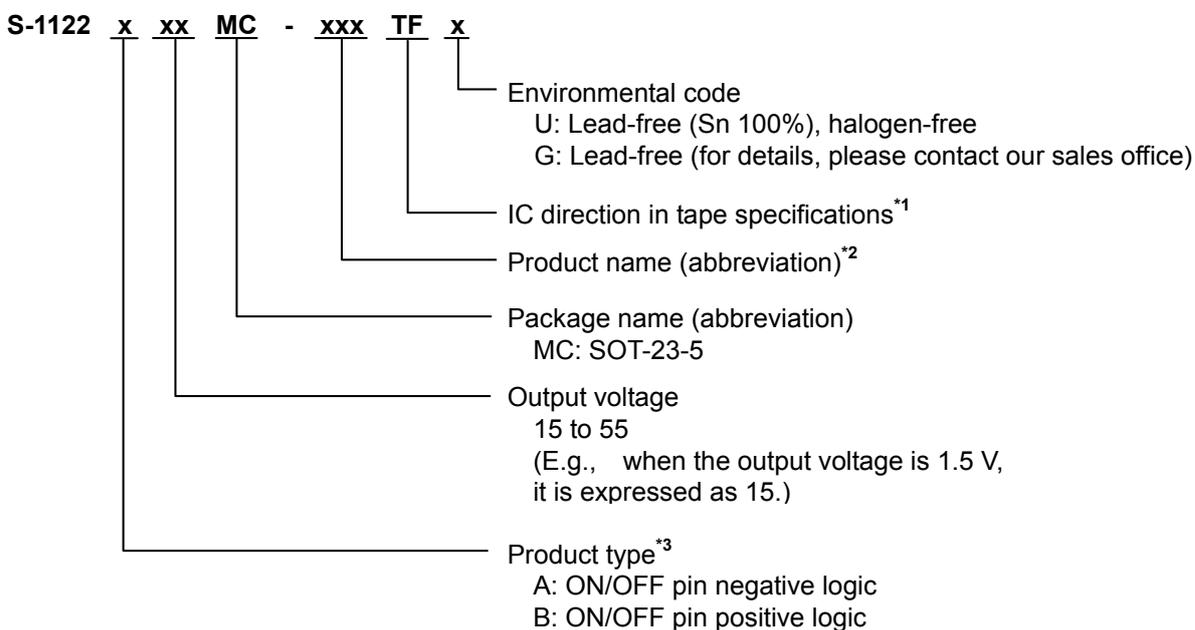


\*1. Refer to the tape drawing.

\*2. Refer to the product name lists.

\*3. Refer to “**3. ON/OFF pin**” in “**■ Operation**”.

**(3) S-1122 Series**



\*1. Refer to the tape drawing.

\*2. Refer to the product name lists.

\*3. Refer to "3. ON/OFF pin" in "■ Operation".

**2. Packages**

Package name	Drawing code			
	Package	Tape	Reel	Land
SNT-6A(H)	PI006-A-P-SD	PI006-A-C-SD	PI006-A-R-SD	PI006-A-L-SD
SOT-23-5	MP005-A-P-SD	MP005-A-C-SD	MP005-A-R-SD	—

# HIGH RIPPLE-REJECTION AND LOW DROPOUT CMOS VOLTAGE REGULATOR

Rev.6.1\_01

S-1112/1122 Series

### 3. Product name lists

#### (1) S-1112 Series

Table 1

Output Voltage	SNT-6A(H)	SOT-23-5
1.5 V $\pm$ 1.0%	S-1112B15PI-L6ATFU	S-1112B15MC-L6ATFx
1.6 V $\pm$ 1.0%	S-1112B16PI-L6BTFU	S-1112B16MC-L6BTFx
1.7 V $\pm$ 1.0%	S-1112B17PI-L6CTFU	S-1112B17MC-L6CTFx
1.8 V $\pm$ 1.0%	S-1112B18PI-L6DTFU	S-1112B18MC-L6DTFx
1.9 V $\pm$ 1.0%	S-1112B19PI-L6ETFU	S-1112B19MC-L6ETFx
2.0 V $\pm$ 1.0%	S-1112B20PI-L6FTFU	S-1112B20MC-L6FTFx
2.1 V $\pm$ 1.0%	S-1112B21PI-L6GTFU	S-1112B21MC-L6GTFx
2.2 V $\pm$ 1.0%	S-1112B22PI-L6HTFU	S-1112B22MC-L6HTFx
2.3 V $\pm$ 1.0%	S-1112B23PI-L6ITFU	S-1112B23MC-L6ITFx
2.4 V $\pm$ 1.0%	S-1112B24PI-L6JTFU	S-1112B24MC-L6JTFx
2.5 V $\pm$ 1.0%	S-1112B25PI-L6KTFU	S-1112B25MC-L6KTFx
2.6 V $\pm$ 1.0%	S-1112B26PI-L6LTFU	S-1112B26MC-L6LTFx
2.7 V $\pm$ 1.0%	S-1112B27PI-L6MTFU	S-1112B27MC-L6MTFx
2.8 V $\pm$ 1.0%	S-1112B28PI-L6NTFU	S-1112B28MC-L6NTFx
2.85 V $\pm$ 1.0%	S-1112B2JPI-L7PTFU	S-1112B2JMC-L7PTFx
2.9 V $\pm$ 1.0%	S-1112B29PI-L6OTFU	S-1112B29MC-L6OTFx
3.0 V $\pm$ 1.0%	S-1112B30PI-L6PTFU	S-1112B30MC-L6PTFx
3.1 V $\pm$ 1.0%	S-1112B31PI-L6QTFU	S-1112B31MC-L6QTFx
3.2 V $\pm$ 1.0%	S-1112B32PI-L6RTFU	S-1112B32MC-L6RTFx
3.3 V $\pm$ 1.0%	S-1112B33PI-L6STFU	S-1112B33MC-L6STFx
3.4 V $\pm$ 1.0%	S-1112B34PI-L6TTFU	S-1112B34MC-L6TTFx
3.5 V $\pm$ 1.0%	S-1112B35PI-L6UTFU	S-1112B35MC-L6UTFx
3.6 V $\pm$ 1.0%	S-1112B36PI-L6VTFU	S-1112B36MC-L6VTFx
3.7 V $\pm$ 1.0%	S-1112B37PI-L6WTFU	S-1112B37MC-L6WTFx
3.8 V $\pm$ 1.0%	S-1112B38PI-L6XTFU	S-1112B38MC-L6XTFx
3.9 V $\pm$ 1.0%	S-1112B39PI-L6YTFU	S-1112B39MC-L6YTFx
4.0 V $\pm$ 1.0%	S-1112B40PI-L6ZTFU	S-1112B40MC-L6ZTFx
4.1 V $\pm$ 1.0%	S-1112B41PI-L7ATFU	S-1112B41MC-L7ATFx
4.2 V $\pm$ 1.0%	S-1112B42PI-L7BTFU	S-1112B42MC-L7BTFx
4.3 V $\pm$ 1.0%	S-1112B43PI-L7CTFU	S-1112B43MC-L7CTFx
4.4 V $\pm$ 1.0%	S-1112B44PI-L7DTFU	S-1112B44MC-L7DTFx
4.5 V $\pm$ 1.0%	S-1112B45PI-L7ETFU	S-1112B45MC-L7ETFx
4.6 V $\pm$ 1.0%	S-1112B46PI-L7FTFU	S-1112B46MC-L7FTFx
4.7 V $\pm$ 1.0%	S-1112B47PI-L7GTFU	S-1112B47MC-L7GTFx
4.8 V $\pm$ 1.0%	S-1112B48PI-L7HTFU	S-1112B48MC-L7HTFx
4.9 V $\pm$ 1.0%	S-1112B49PI-L7ITFU	S-1112B49MC-L7ITFx
5.0 V $\pm$ 1.0%	S-1112B50PI-L7JTFU	S-1112B50MC-L7JTFx
5.1 V $\pm$ 1.0%	S-1112B51PI-L7KTFU	S-1112B51MC-L7KTFx
5.2 V $\pm$ 1.0%	S-1112B52PI-L7LTFU	S-1112B52MC-L7LTFx
5.3 V $\pm$ 1.0%	S-1112B53PI-L7MTFU	S-1112B53MC-L7MTFx
5.4 V $\pm$ 1.0%	S-1112B54PI-L7NTFU	S-1112B54MC-L7NTFx
5.5 V $\pm$ 1.0%	S-1112B55PI-L7OTFU	S-1112B55MC-L7OTFx

**Remark 1.** Please contact our sales office for type A products.

2. x: G or U

3. Please select products of environmental code = U for Sn 100%, halogen-free products.

**(2) S-1122 Series**

**Table 2**

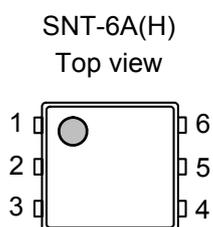
Output Voltage	SOT-23-5
1.5 V $\pm$ 1.0%	S-1122B15MC-L8ATF <sub>x</sub>
1.6 V $\pm$ 1.0%	S-1122B16MC-L8BTF <sub>x</sub>
1.7 V $\pm$ 1.0%	S-1122B17MC-L8CTF <sub>x</sub>
1.8 V $\pm$ 1.0%	S-1122B18MC-L8DTF <sub>x</sub>
1.9 V $\pm$ 1.0%	S-1122B19MC-L8ETF <sub>x</sub>
2.0 V $\pm$ 1.0%	S-1122B20MC-L8FTF <sub>x</sub>
2.1 V $\pm$ 1.0%	S-1122B21MC-L8GTF <sub>x</sub>
2.2 V $\pm$ 1.0%	S-1122B22MC-L8HTF <sub>x</sub>
2.3 V $\pm$ 1.0%	S-1122B23MC-L8ITF <sub>x</sub>
2.4 V $\pm$ 1.0%	S-1122B24MC-L8JTF <sub>x</sub>
2.5 V $\pm$ 1.0%	S-1122B25MC-L8KTF <sub>x</sub>
2.6 V $\pm$ 1.0%	S-1122B26MC-L8LTF <sub>x</sub>
2.7 V $\pm$ 1.0%	S-1122B27MC-L8MTF <sub>x</sub>
2.8 V $\pm$ 1.0%	S-1122B28MC-L8NTF <sub>x</sub>
2.9 V $\pm$ 1.0%	S-1122B29MC-L8OTF <sub>x</sub>
3.0 V $\pm$ 1.0%	S-1122B30MC-L8PTF <sub>x</sub>
3.1 V $\pm$ 1.0%	S-1122B31MC-L8QTF <sub>x</sub>
3.2 V $\pm$ 1.0%	S-1122B32MC-L8RTF <sub>x</sub>
3.3 V $\pm$ 1.0%	S-1122B33MC-L8STF <sub>x</sub>
3.4 V $\pm$ 1.0%	S-1122B34MC-L8TTF <sub>x</sub>
3.5 V $\pm$ 1.0%	S-1122B35MC-L8UTF <sub>x</sub>
3.6 V $\pm$ 1.0%	S-1122B36MC-L8VTF <sub>x</sub>
3.7 V $\pm$ 1.0%	S-1122B37MC-L8WTF <sub>x</sub>
3.8 V $\pm$ 1.0%	S-1122B38MC-L8XTF <sub>x</sub>
3.9 V $\pm$ 1.0%	S-1122B39MC-L8YTF <sub>x</sub>
4.0 V $\pm$ 1.0%	S-1122B40MC-L8ZTF <sub>x</sub>
4.1 V $\pm$ 1.0%	S-1122B41MC-L9ATF <sub>x</sub>
4.2 V $\pm$ 1.0%	S-1122B42MC-L9BTF <sub>x</sub>
4.3 V $\pm$ 1.0%	S-1122B43MC-L9CTF <sub>x</sub>
4.4 V $\pm$ 1.0%	S-1122B44MC-L9DTF <sub>x</sub>
4.5 V $\pm$ 1.0%	S-1122B45MC-L9ETF <sub>x</sub>
4.6 V $\pm$ 1.0%	S-1122B46MC-L9FTF <sub>x</sub>
4.7 V $\pm$ 1.0%	S-1122B47MC-L9GTF <sub>x</sub>
4.8 V $\pm$ 1.0%	S-1122B48MC-L9HTF <sub>x</sub>
4.9 V $\pm$ 1.0%	S-1122B49MC-L9ITF <sub>x</sub>
5.0 V $\pm$ 1.0%	S-1122B50MC-L9JTF <sub>x</sub>
5.1 V $\pm$ 1.0%	S-1122B51MC-L9KTF <sub>x</sub>
5.2 V $\pm$ 1.0%	S-1122B52MC-L9LTF <sub>x</sub>
5.3 V $\pm$ 1.0%	S-1122B53MC-L9MTF <sub>x</sub>
5.4 V $\pm$ 1.0%	S-1122B54MC-L9NTF <sub>x</sub>
5.5 V $\pm$ 1.0%	S-1122B55MC-L9OTF <sub>x</sub>

**Remark 1.** Please contact our sales office for type A products.

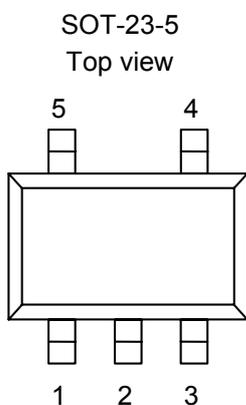
**2.** x: G or U

**3.** Please select products of environmental code = U for Sn 100%, halogen-free products.

## ■ Pin Configurations



**Figure 2**



**Figure 3**

**Table 3**

(S-1112 Series)

Pin No.	Symbol	Description
1	NC *1	No connection
2	VSS	GND pin
3	ON/OFF	ON/OFF pin
4	VIN	Input voltage pin
5	VSS	GND pin
6	VOUT	Output voltage pin

\*1. The NC pin is electrically open.

The NC pin can be connected to VIN pin or VSS pin.

**Table 4**

(S-1112 Series)

Pin No.	Symbol	Description
1	VIN	Input voltage pin
2	VSS	GND pin
3	ON/OFF	ON/OFF pin
4	NC*1	No connection
5	VOUT	Output voltage pin

\*1. The NC pin is electrically open.

The NC pin can be connected to VIN pin or VSS pin.

**Table 5**

(S-1122 Series)

Pin No.	Symbol	Description
1	VOUT	Output voltage pin
2	VSS	GND pin
3	VIN	Input voltage pin
4	ON/OFF	ON/OFF pin
5	NC*1	No connection

\*1. The NC pin is electrically open.

The NC pin can be connected to VIN pin or VSS pin.

■ **Absolute Maximum Ratings**

**Table 6**

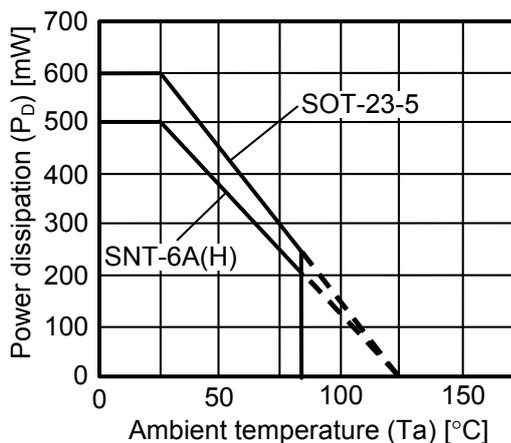
(Ta = 25°C unless otherwise specified)

Item		Symbol	Absolute Maximum Rating	Unit
Input voltage		V <sub>IN</sub>	V <sub>SS</sub> - 0.3 to V <sub>SS</sub> + 7	V
		V <sub>ON/OFF</sub>	V <sub>SS</sub> - 0.3 to V <sub>IN</sub> + 0.3	V
Output voltage		V <sub>OUT</sub>	V <sub>SS</sub> - 0.3 to V <sub>IN</sub> + 0.3	V
Power dissipation	SNT-6A(H)	P <sub>D</sub>	500*1	mW
	SOT-23-5		300 (When not mounted on board)	mW
			600*1	mW
Operation ambient temperature		T <sub>opr</sub>	-40 to +85	°C
Storage temperature		T <sub>stg</sub>	-40 to +125	°C

\*1. When mounted on board  
 [Mounted board]

- (1) Board size : 114.3 mm × 76.2 mm × t1.6 mm
- (2) Board name : JEDEC STANDARD51-7

**Caution** The absolute maximum ratings are rated values exceeding which the product could suffer physical damage. These values must therefore not be exceeded under any conditions.



**Figure 4 Power Dissipation of The Package (When Mounted on Board)**

# HIGH RIPPLE-REJECTION AND LOW DROPOUT CMOS VOLTAGE REGULATOR

Rev.6.1\_01

S-1112/1122 Series

## ■ Electrical Characteristics

**Table 7**

(Ta = 25°C unless otherwise specified)

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit	Test Circuit	
Output voltage*1	V <sub>OUT(E)</sub>	V <sub>IN</sub> = V <sub>OUT(S)</sub> + 1.0 V, I <sub>OUT</sub> = 30 mA	V <sub>OUT(S)</sub> × 0.99	V <sub>OUT(S)</sub>	V <sub>OUT(S)</sub> × 1.01	V	1	
Output current*2	I <sub>OUT</sub>	V <sub>IN</sub> ≥ V <sub>OUT(S)</sub> + 1.0 V	150*5	—	—	mA	3	
Dropout voltage*3	V <sub>drop</sub>	I <sub>OUT</sub> = 100 mA	1.5 V ≤ V <sub>OUT(S)</sub> ≤ 1.6 V	—	0.32	0.55	V	1
			1.7 V ≤ V <sub>OUT(S)</sub> ≤ 1.8 V	—	0.28	0.47	V	1
			1.9 V ≤ V <sub>OUT(S)</sub> ≤ 2.3 V	—	0.25	0.35	V	1
			2.4 V ≤ V <sub>OUT(S)</sub> ≤ 2.7 V	—	0.20	0.29	V	1
			2.8 V ≤ V <sub>OUT(S)</sub> ≤ 5.5 V	—	0.19	0.26	V	1
Line regulation	$\frac{\Delta V_{OUT1}}{\Delta V_{IN} \cdot V_{OUT}}$	V <sub>OUT(S)</sub> + 0.5 V ≤ V <sub>IN</sub> ≤ 6.5 V, I <sub>OUT</sub> = 30 mA	—	0.05	0.2	%/V	1	
Load regulation	ΔV <sub>OUT2</sub>	V <sub>IN</sub> = V <sub>OUT(S)</sub> + 1.0 V, 1.0 mA ≤ I <sub>OUT</sub> ≤ 80 mA	—	12	40	mV	1	
Output voltage temperature coefficient*4	$\frac{\Delta V_{OUT}}{\Delta T_a \cdot V_{OUT}}$	V <sub>IN</sub> = V <sub>OUT(S)</sub> + 1.0 V, I <sub>OUT</sub> = 10 mA, −40°C ≤ Ta ≤ 85°C	—	±100	—	ppm/°C	1	
Current consumption during operation	I <sub>SS1</sub>	V <sub>IN</sub> = V <sub>OUT(S)</sub> + 1.0 V, ON/OFF pin = ON, no load	—	50	90	μA	2	
Current consumption during power-off	I <sub>SS2</sub>	V <sub>IN</sub> = V <sub>OUT(S)</sub> + 1.0 V, ON/OFF pin = OFF, no load	—	0.1	1.0	μA	2	
Input voltage	V <sub>IN</sub>	—	2.0	—	6.5	V	—	
ON/OFF pin input voltage "H"	V <sub>SH</sub>	V <sub>IN</sub> = V <sub>OUT(S)</sub> + 1.0 V, R <sub>L</sub> = 1.0 kΩ	1.5	—	—	V	4	
ON/OFF pin input voltage "L"	V <sub>SL</sub>	V <sub>IN</sub> = V <sub>OUT(S)</sub> + 1.0 V, R <sub>L</sub> = 1.0 kΩ	—	—	0.3	V	4	
ON/OFF pin input current "H"	I <sub>SH</sub>	V <sub>IN</sub> = 6.5 V, V <sub>ON/OFF</sub> = 6.5 V	−0.1	—	0.1	μA	4	
ON/OFF pin input current "L"	I <sub>SL</sub>	V <sub>IN</sub> = 6.5 V, V <sub>ON/OFF</sub> = 0 V	−0.1	—	0.1	μA	4	
Ripple rejection	RR	V <sub>IN</sub> = V <sub>OUT(S)</sub> + 1.0 V, f = 1.0 kHz, ΔV <sub>rip</sub> = 0.5 V <sub>rms</sub> , I <sub>OUT</sub> = 30 mA	—	80	—	dB	5	
Short-circuit current	I <sub>short</sub>	V <sub>IN</sub> = V <sub>OUT(S)</sub> + 1.0 V, ON/OFF pin = ON, V <sub>OUT</sub> = 0 V	—	200	—	mA	3	

\*1. V<sub>OUT(S)</sub>: Set output voltage

V<sub>OUT(E)</sub>: Actual output voltage

Output voltage when fixing I<sub>OUT</sub>(= 30 mA) and inputting V<sub>OUT(S)</sub> + 1.0 V

\*2. The output current at which the output voltage becomes 95% of V<sub>OUT(E)</sub> after gradually increasing the output current.

\*3. V<sub>drop</sub> = V<sub>IN1</sub> − (V<sub>OUT3</sub> × 0.98)

V<sub>OUT3</sub> is the output voltage when V<sub>IN</sub> = V<sub>OUT(S)</sub> + 1.0 V and I<sub>OUT</sub> = 100 mA.

V<sub>IN1</sub> is the input voltage at which the output voltage becomes 98% of V<sub>OUT3</sub> after gradually decreasing the input voltage.

\*4. A change in the temperature of the output voltage [mV/°C] is calculated using the following equation.

$$\frac{\Delta V_{OUT}}{\Delta T_a} [\text{mV}/^\circ\text{C}]^*1 = V_{OUT(S)} [\text{V}]^2 \times \frac{\Delta V_{OUT}}{\Delta T_a \cdot V_{OUT}} [\text{ppm}/^\circ\text{C}]^*3 \div 1000$$

\*1. Change in temperature of output voltage

\*2. Set output voltage

\*3. Output voltage temperature coefficient

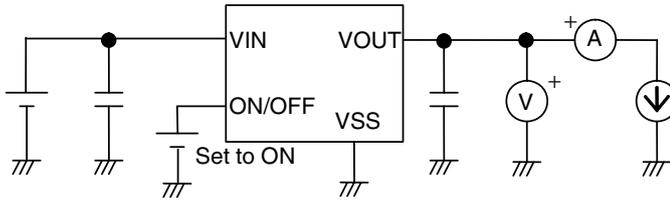
\*5. The output current can be at least this value.

Due to restrictions on the package power dissipation, this value may not be satisfied. Attention should be paid to the power dissipation of the package when the output current is large.

This specification is guaranteed by design.

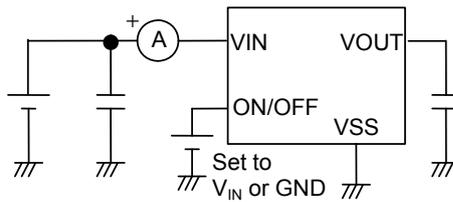
■ **Test Circuits**

1.



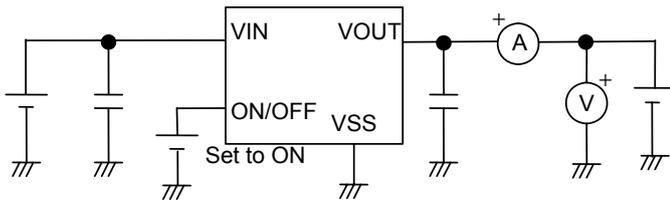
**Figure 5**

2.



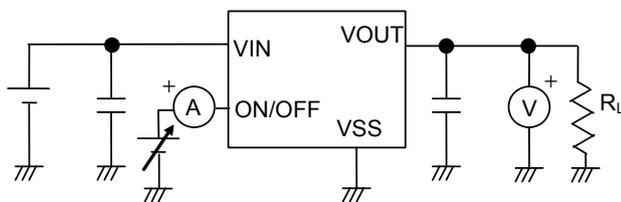
**Figure 6**

3.



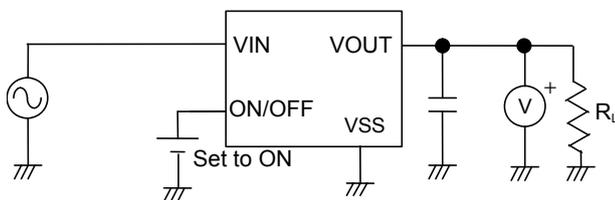
**Figure 7**

4.



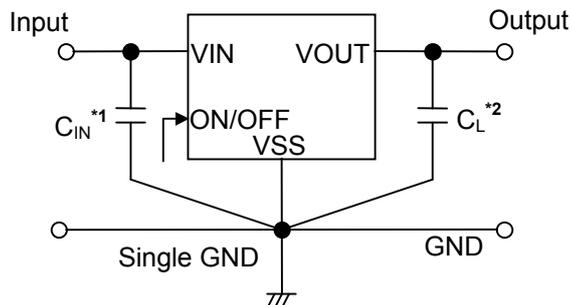
**Figure 8**

5.



**Figure 9**

■ Standard Circuit



\*1.  $C_{IN}$  is a capacitor for stabilizing the input.

\*2. A ceramic capacitor of 0.47  $\mu\text{F}$  or more can be used for  $C_L$ .

Figure 10

**Caution** The above connection diagram and constant will not guarantee successful operation. Perform thorough evaluation using the actual application to set the constant.

■ Condition of Application

Input capacitor ( $C_{IN}$ ):	1.0 $\mu\text{F}$ or more
Output capacitor ( $C_L$ ):	0.47 $\mu\text{F}$ or more
ESR of output capacitor:	10 $\Omega$ or less

**Caution** Generally a series regulator may cause oscillation, depending on the selection of external parts. Check that no oscillation occurs with the application using the above capacitor.

## ■ Explanation of Terms

### 1. Low dropout voltage regulator

This voltage regulator has the low dropout voltage due to its built-in low on-resistance transistor.

### 2. Low ESR

A capacitor whose ESR (Equivalent Series Resistance) is low. The S-1112/1122 Series enables use of a low ESR capacitor, such as a ceramic capacitor, for the output-side capacitor ( $C_L$ ). A capacitor whose ESR is 10  $\Omega$  or less can be used.

### 3. Output voltage ( $V_{OUT}$ )

The accuracy of the output voltage is ensured at  $\pm 1.0\%$  under the specified conditions of fixed input voltage\*1, fixed output current, and fixed temperature.

\*1. Differs depending the product.

**Caution** If the above conditions change, the output voltage value may vary and exceed the accuracy range of the output voltage. Refer to “■ Electrical Characteristics” and “■ Characteristics (Typical Data)” for details.

### 4. Line regulation $\left( \frac{\Delta V_{OUT1}}{\Delta V_{IN} \bullet V_{OUT}} \right)$

Indicates the dependency of the output voltage on the input voltage. That is, the values show how much the output voltage changes due to a change in the input voltage with the output current remaining unchanged.

### 5. Load regulation ( $\Delta V_{OUT2}$ )

Indicates the dependency of the output voltage on the output current. That is, the values show how much the output voltage changes due to a change in the output current with the input voltage remaining unchanged.

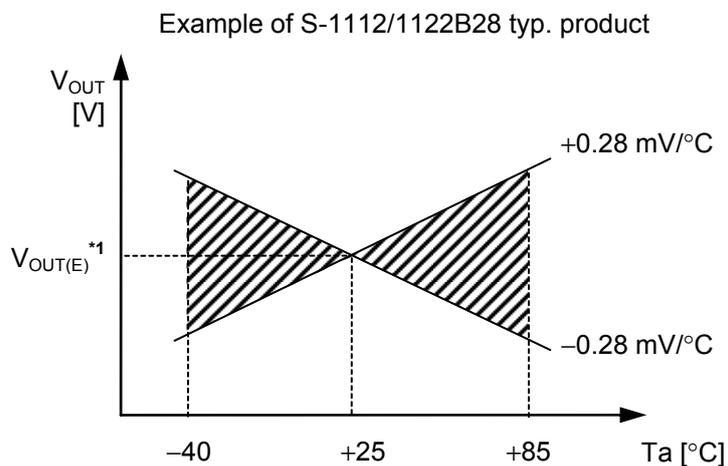
### 6. Dropout voltage ( $V_{drop}$ )

Indicates the difference between input voltage ( $V_{IN1}$ ) and the output voltage when; decreasing input voltage ( $V_{IN}$ ) gradually until the output voltage has dropped out to the value of 98% of output voltage ( $V_{OUT3}$ ), which is at  $V_{IN} = V_{OUT(S)} + 1.0$  V.

$$V_{drop} = V_{IN1} - (V_{OUT3} \times 0.98)$$

7. Output voltage temperature coefficient  $\left(\frac{\Delta V_{OUT}}{\Delta T_a \bullet V_{OUT}}\right)$

The shaded area in **Figure 11** is the range where  $V_{OUT}$  varies in the operation temperature range when the output voltage temperature coefficient is  $\pm 100$  ppm/ $^{\circ}\text{C}$ .



\*1.  $V_{OUT(E)}$  is the value of the output voltage measured at  $T_a = +25^{\circ}\text{C}$ .

**Figure 11**

A change in the temperature of the output voltage [ $\text{mV}/^{\circ}\text{C}$ ] is calculated using the following equation.

$$\frac{\Delta V_{OUT}}{\Delta T_a} [\text{mV}/^{\circ}\text{C}]^{*1} = V_{OUT(S)} [\text{V}]^{*2} \times \frac{\Delta V_{OUT}}{\Delta T_a \bullet V_{OUT}} [\text{ppm}/^{\circ}\text{C}]^{*3} \div 1000$$

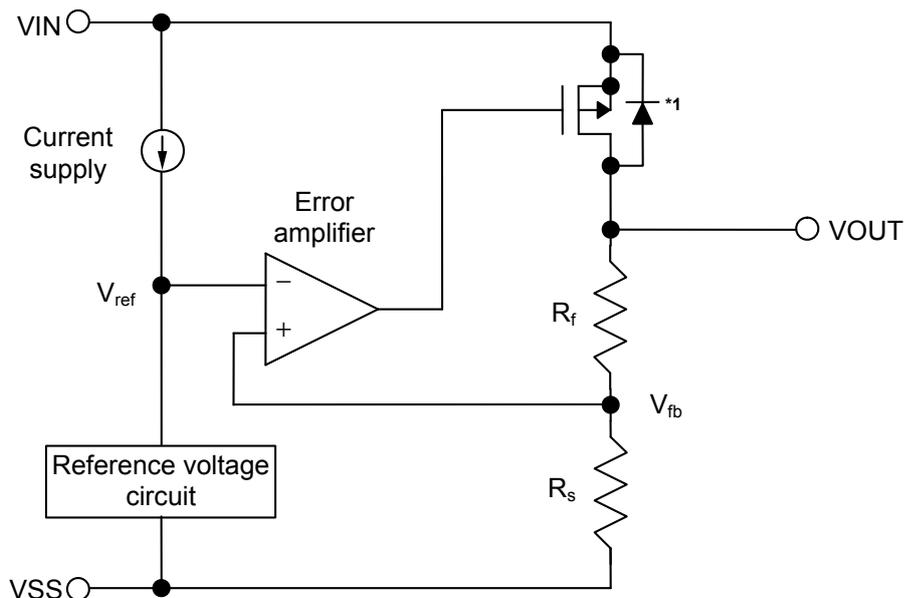
- \*1. Change in temperature of output voltage
- \*2. Set output voltage
- \*3. Output voltage temperature coefficient

■ **Operation**

**1. Basic operation**

Figure 12 shows the block diagram of the S-1112/1122 Series.

The error amplifier compares the reference voltage ( $V_{ref}$ ) with feedback voltage ( $V_{fb}$ ), which is the output voltage resistance-divided by feedback resistors ( $R_s$  and  $R_f$ ). It supplies the gate voltage necessary to maintain the constant output voltage which is not influenced by the input voltage and temperature change, to the output transistor.



\*1. Parasitic diode

**Figure 12**

**2. Output transistor**

In the S-1112/1122 Series, a low on-resistance P-channel MOS FET is used as the output transistor. Be sure that  $V_{OUT}$  does not exceed  $V_{IN} + 0.3$  V to prevent the voltage regulator from being damaged due to reverse current flowing from the VOUT pin through a parasitic diode to the VIN pin, when the potential of  $V_{OUT}$  became higher than  $V_{IN}$ .

### 3. ON/OFF pin

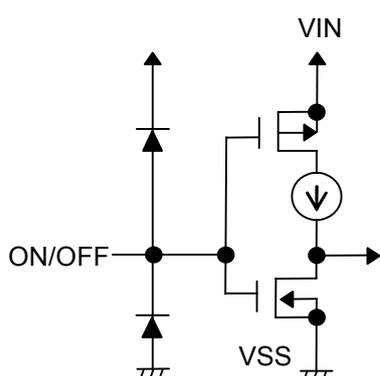
This pin starts and stops the regulator.

When the ON/OFF pin is set to OFF level, the entire internal circuit stops operating, and the built-in P-channel MOS FET output transistor between the VIN pin and the VOUT pin is turned off, reducing current consumption significantly. The VOUT pin becomes the V<sub>SS</sub> level due to the internally divided resistance of several MΩ between the VOUT pin and the VSS pin.

The structure of the ON/OFF pin is as shown in **Figure 13**. Since the ON/OFF pin is neither pulled down nor pulled up internally, do not use it in the floating status. In addition, note that the current consumption increases if a voltage of 0.3 V to V<sub>IN</sub> – 0.3 V is applied to the ON/OFF pin. When not using the ON/OFF pin, connect it to the VSS pin in the product A type, and connect it to the VIN pin in B type.

**Table 8**

Product Type	ON/OFF Pin	Internal Circuit	VOUT Pin Voltage	Current Consumption
A	“L”: ON	Operate	Set value	I <sub>SS1</sub>
A	“H”: OFF	Stop	V <sub>SS</sub> level	I <sub>SS2</sub>
B	“L”: OFF	Stop	V <sub>SS</sub> level	I <sub>SS2</sub>
B	“H”: ON	Operate	Set value	I <sub>SS1</sub>



**Figure 13**

### ■ Selection of Output Capacitor (C<sub>L</sub>)

The S-1112/1122 Series requires an output capacitor between the VOUT pin and the VSS pin for phase compensation. A ceramic capacitor with a capacitance of 0.47 μF or more can be used. When using an OS capacitor, a tantalum capacitor, or an aluminum electrolytic capacitor, the capacitance must be 0.47 μF or more, and the ESR must be 10 Ω or less.

The value of the output overshoot or undershoot transient response varies depending on the value of the output capacitor.

When selecting the output capacitor, perform sufficient evaluation, including evaluation of temperature characteristics, on the actual device.

■ **Precautions**

- Wiring patterns for the VIN pin, the VOUT pin and GND should be designed so that the impedance is low. When mounting an output capacitor between the VOUT pin and the VSS pin ( $C_L$ ) and a capacitor for stabilizing the input between the VIN pin and the VSS pin ( $C_{IN}$ ), the distance from the capacitors to these pins should be as short as possible.
- Note that generally the output voltage may increase when a series regulator is used at low load current (1.0 mA or less).
- Generally a series regulator may cause oscillation, depending on the selection of external parts. The following conditions are recommended for the S-1112/1122 Series. However, be sure to perform sufficient evaluation under the actual usage conditions for selection, including evaluation of temperature characteristics.

Input capacitor ( $C_{IN}$ ):	1.0 $\mu$ F or more
Output capacitor ( $C_L$ ):	0.47 $\mu$ F or more
Equivalent series resistance (ESR):	10 $\Omega$ or less

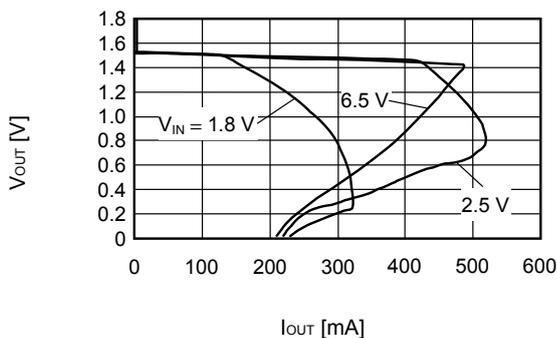
- The voltage regulator may oscillate when the impedance of the power supply is high and the input capacitance is small or an input capacitor is not connected.
- Overshoot may occur in the output voltage momentarily if the voltage is rapidly raised at power-on or when the power supply fluctuates. Sufficiently evaluate the output voltage at power-on with the actual device.
- The application conditions for the input voltage, the output voltage, and the load current should not exceed the package power dissipation.
- Do not apply an electrostatic discharge to this IC that exceeds the performance ratings of the built-in electrostatic protection circuit.
- In determining the output current, attention should be paid to the output current value specified in **Table 7** in “■ **Electrical Characteristics**” and footnote \*5 of the table.
- SII Semiconductor Corporation claims no responsibility for any disputes arising out of or in connection with any infringement by products including this IC of patents owned by a third party.

## ■ Characteristics (Typical Data)

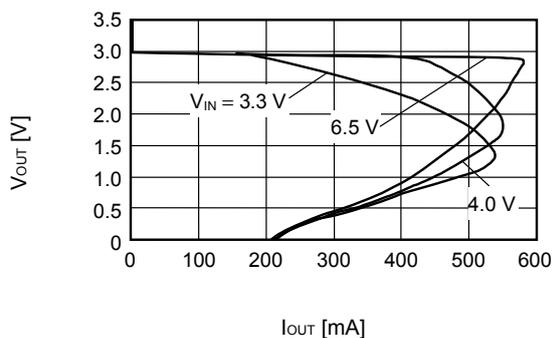
**Remark** The following, which describes the S-1112 Series as the typical product, shows typical data common to the S-1122 Series.

### (1) Output Voltage vs. Output current (when load current increases)

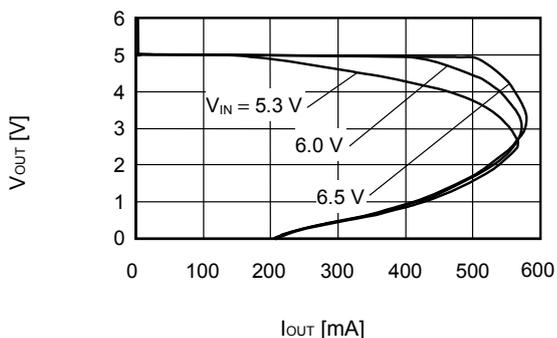
S-1112B15 (Ta = 25°C)



S-1112B30 (Ta = 25°C)



S-1112B50 (Ta = 25°C)

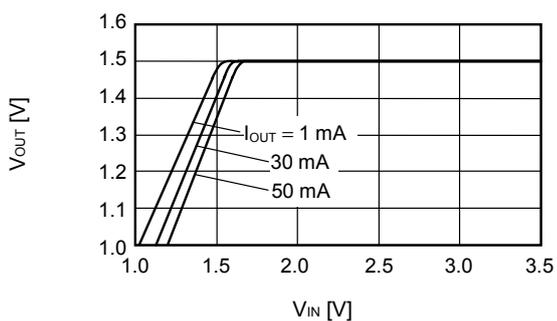


**Remark** In determining the output current, attention should be paid to the following.

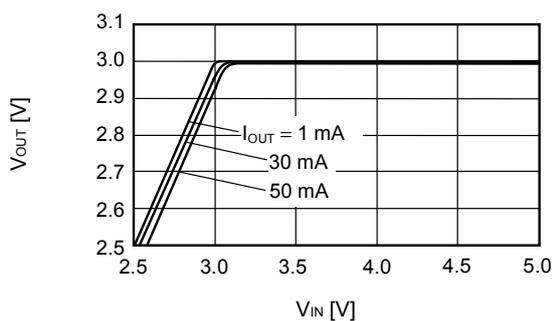
- 1) The minimum output current value and footnote \*5 of Table 7 in the "■ Electrical Characteristics"
- 2) The package power dissipation

### (2) Output voltage vs. Input voltage

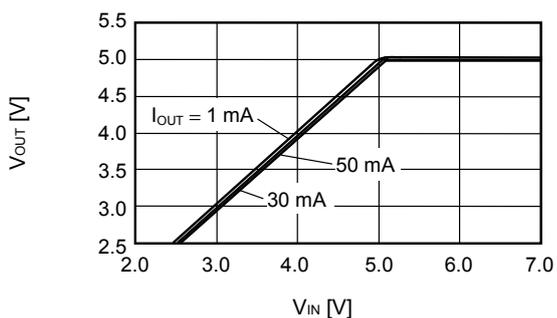
S-1112B15 (Ta = 25°C)



S-1112B30 (Ta = 25°C)

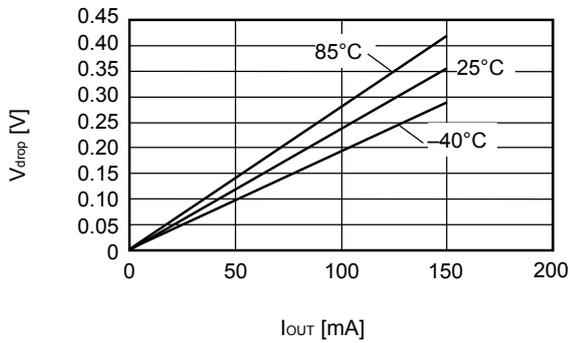


S-1112B50 (Ta = 25°C)

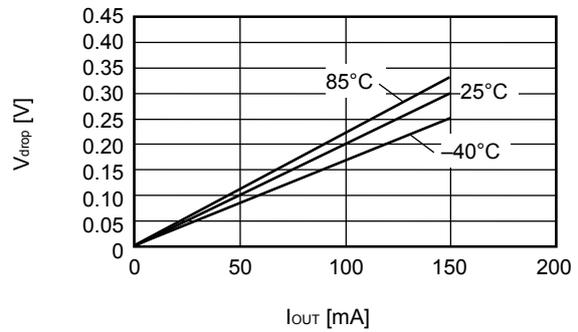


**(3) Dropout voltage vs. Output current**

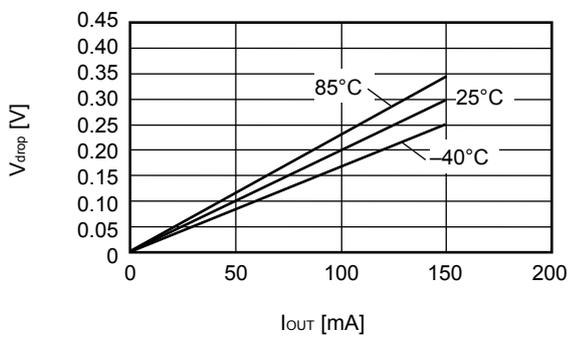
S-1112B15



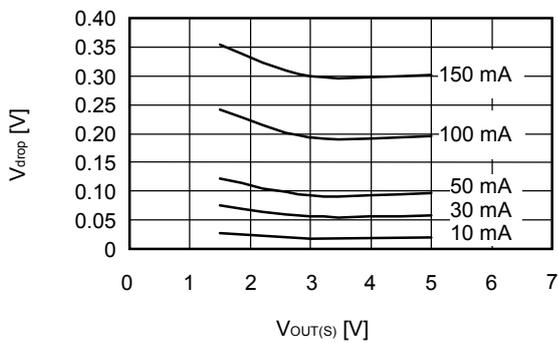
S-1112B30



S-1112B50



**(4) Dropout voltage vs. Set output voltage**



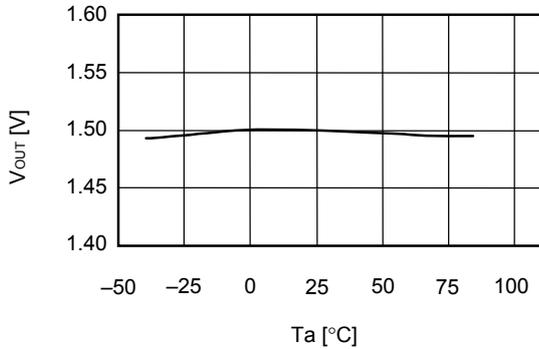
# HIGH RIPPLE-REJECTION AND LOW DROPOUT CMOS VOLTAGE REGULATOR

Rev.6.1\_01

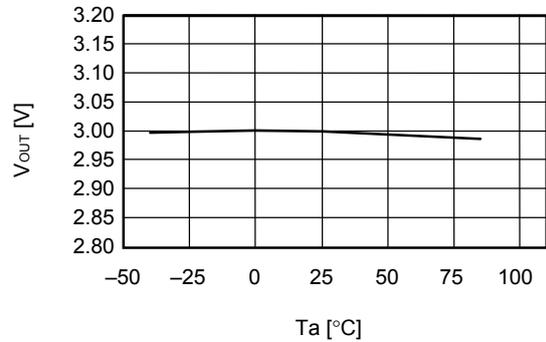
S-1112/1122 Series

## (5) Output voltage vs. Ambient temperature

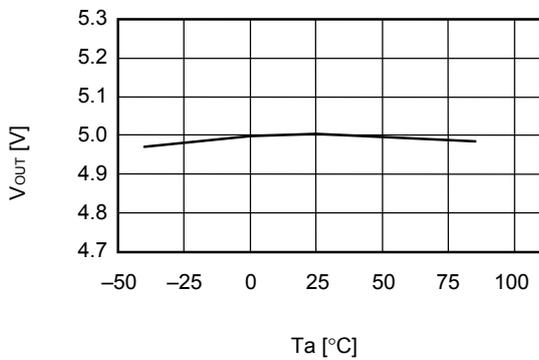
S-1112B15



S-1112B30

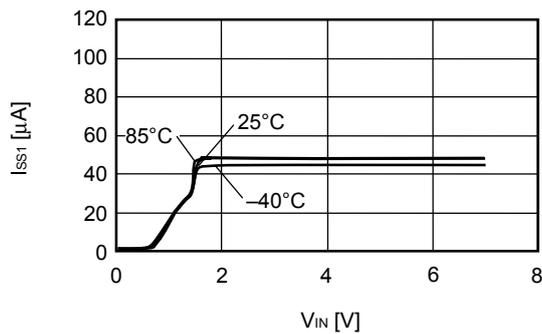


S-1112B50

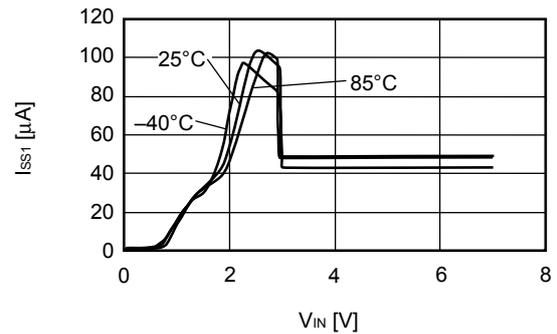


## (6) Current consumption vs. Input voltage

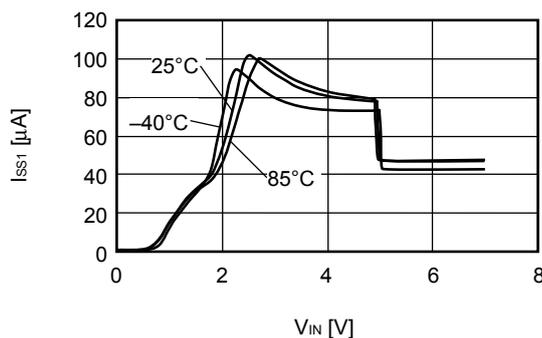
S-1112B15



S-1112B30



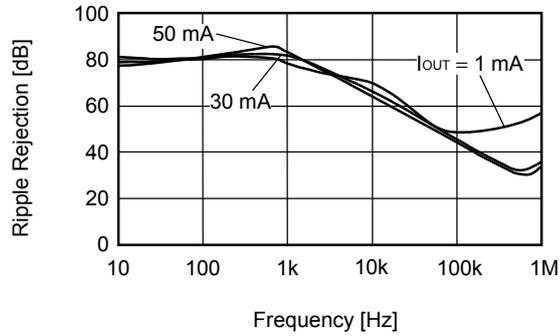
S-1112B50



**(7) Ripple rejection**

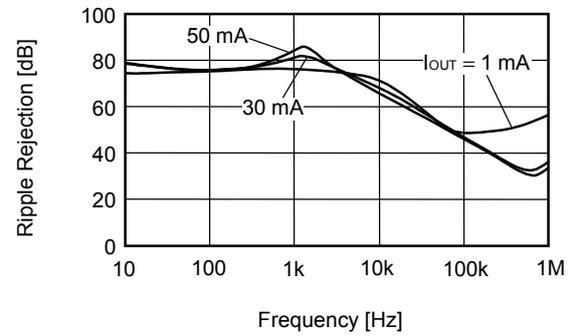
S-1112B15 (Ta = 25°C)

V<sub>IN</sub> = 2.5 V, C<sub>OUT</sub> = 0.47 μF



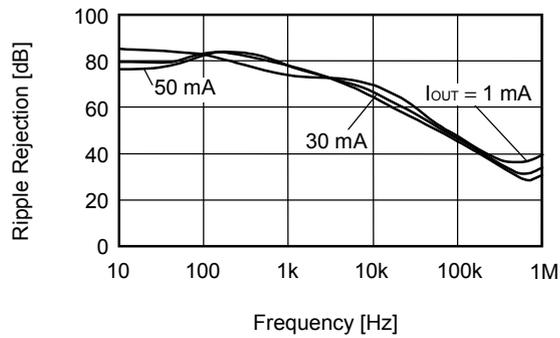
S-1112B30 (Ta = 25°C)

V<sub>IN</sub> = 4.0 V, C<sub>OUT</sub> = 0.47 μF



S-1112B50 (Ta = 25°C)

V<sub>IN</sub> = 6.0 V, C<sub>OUT</sub> = 0.47 μF

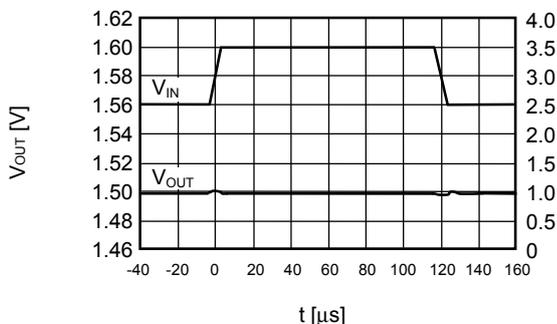


## Reference Data

### (1) Input transient response characteristics

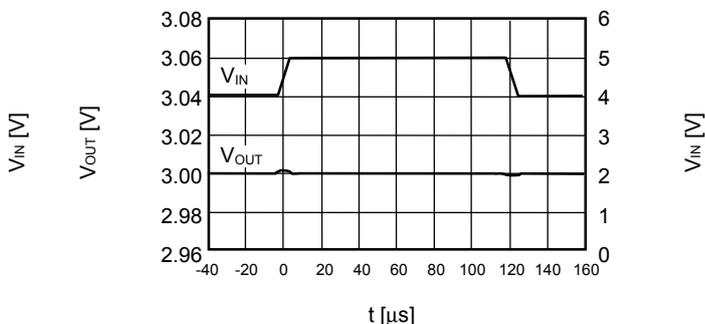
S-1112B15 (Ta = 25°C)

I<sub>OUT</sub> = 30 mA, t<sub>r</sub> = t<sub>f</sub> = 5.0 μs, C<sub>OUT</sub> = 0.47 μF, C<sub>IN</sub> = 0 μF



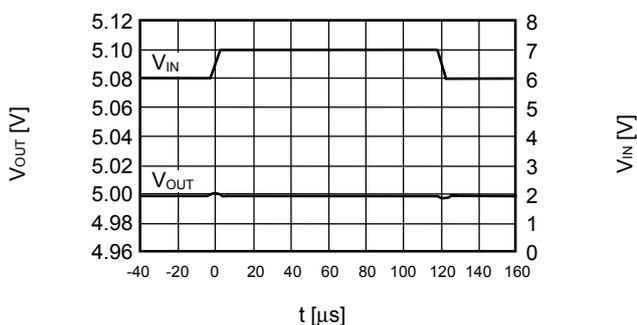
S-1112B30 (Ta = 25°C)

I<sub>OUT</sub> = 30 mA, t<sub>r</sub> = t<sub>f</sub> = 5.0 μs, C<sub>OUT</sub> = 0.47 μF, C<sub>IN</sub> = 0 μF



S-1112B50 (Ta = 25°C)

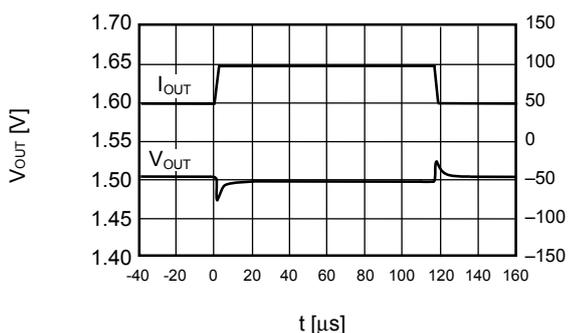
I<sub>OUT</sub> = 30 mA, t<sub>r</sub> = t<sub>f</sub> = 5.0 μs, C<sub>OUT</sub> = 0.47 μF, C<sub>IN</sub> = 0 μF



### (2) Load transient response characteristics

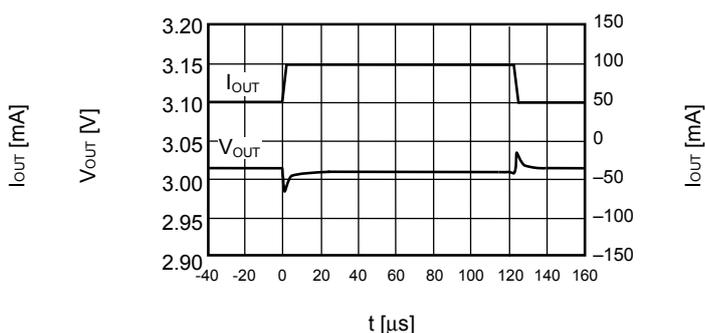
S-1112B15 (Ta = 25°C)

V<sub>IN</sub> = 2.5 V, C<sub>OUT</sub> = 0.47 μF, C<sub>IN</sub> = 1.0 μF, I<sub>OUT</sub> = 50 mA ↔ 100 mA



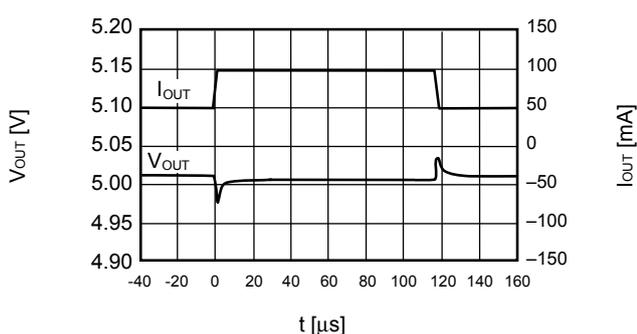
S-1112B30 (Ta = 25°C)

V<sub>IN</sub> = 4.0 V, C<sub>OUT</sub> = 0.47 μF, C<sub>IN</sub> = 1.0 μF, I<sub>OUT</sub> = 50 mA ↔ 100 mA



S-1112B50 (Ta = 25°C)

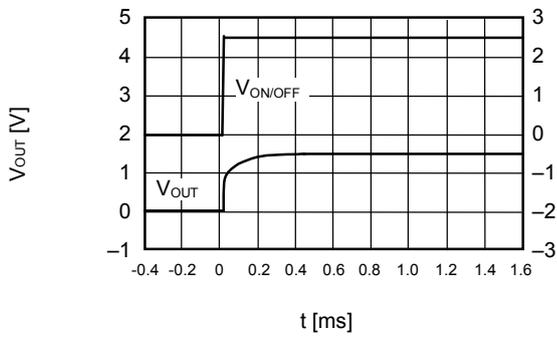
V<sub>IN</sub> = 6.0 V, C<sub>OUT</sub> = 0.47 μF, C<sub>IN</sub> = 1.0 μF, I<sub>OUT</sub> = 50 mA ↔ 100 mA



**(3) ON/OFF pin transient response characteristics**

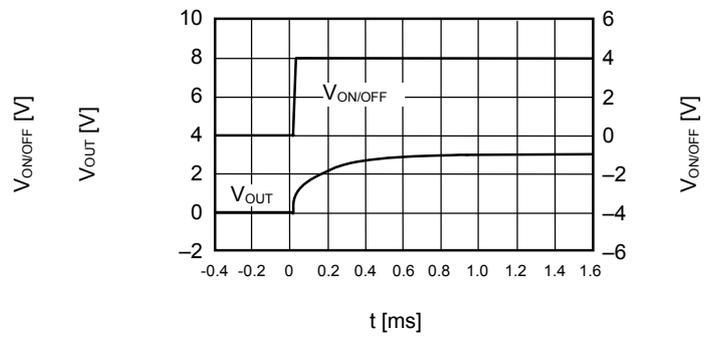
S-1112B15 ( $T_a = 25^\circ\text{C}$ )

$V_{IN} = 2.5\text{ V}$ ,  $C_{OUT} = 0.47\ \mu\text{F}$ ,  $C_{IN} = 1.0\ \mu\text{F}$ ,  $I_{OUT} = 100\ \text{mA}$

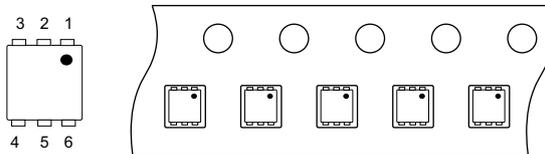
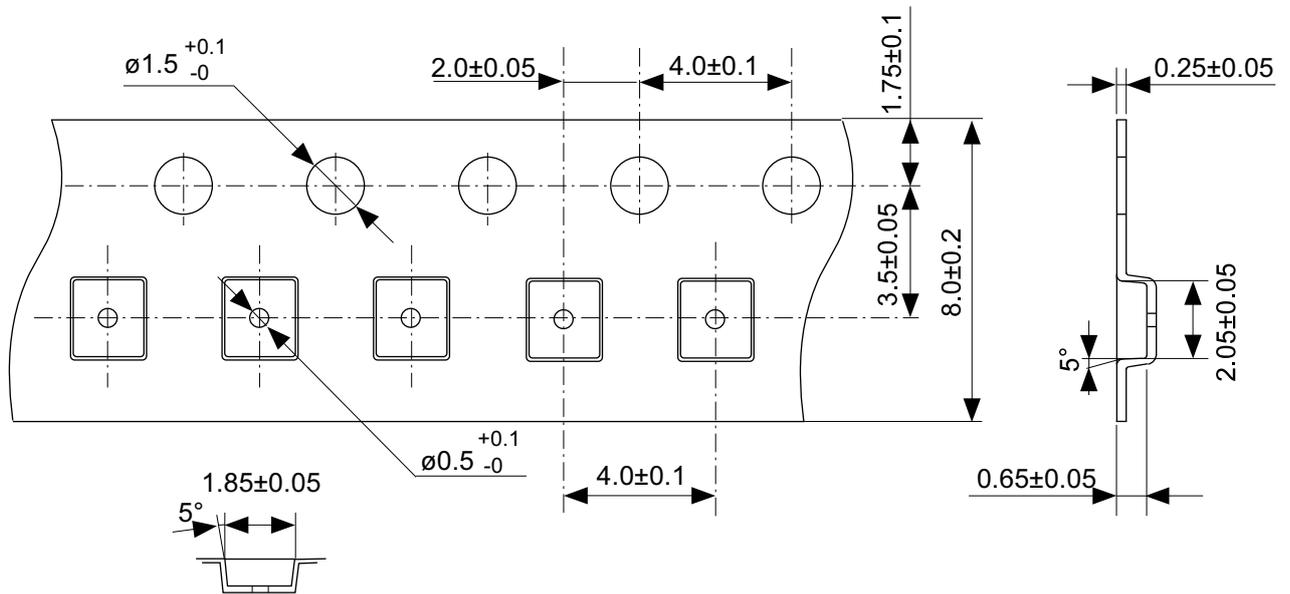


S-1112B30 ( $T_a = 25^\circ\text{C}$ )

$V_{IN} = 4.0\text{ V}$ ,  $C_{OUT} = 0.47\ \mu\text{F}$ ,  $C_{IN} = 1.0\ \mu\text{F}$ ,  $I_{OUT} = 100\ \text{mA}$



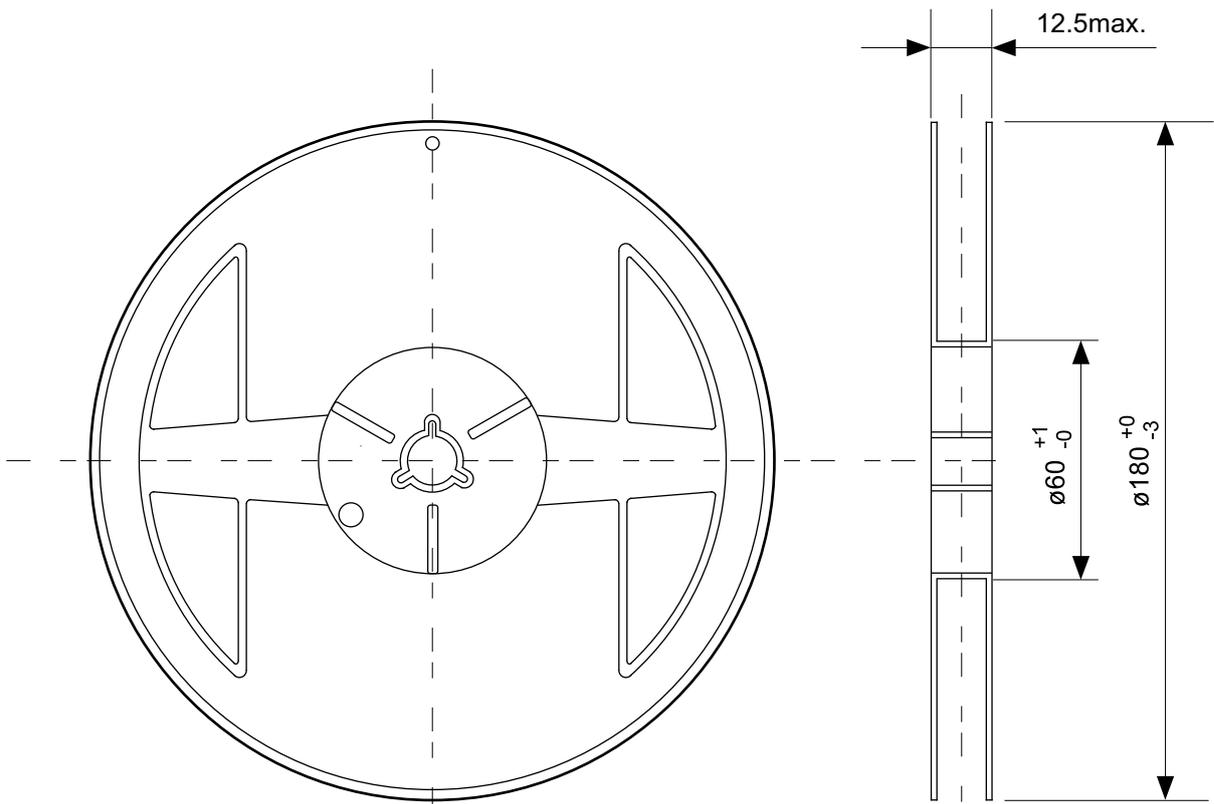




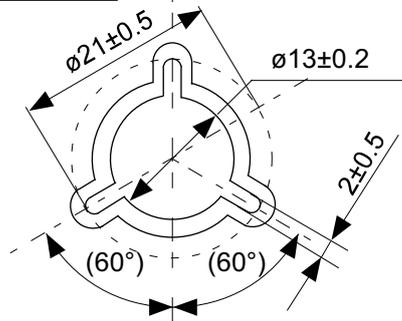
Feed direction →

No. PI006-A-C-SD-1.0

TITLE	SNT-6A(H)-A-Carrier Tape
No.	PI006-A-C-SD-1.0
SCALE	
UNIT	mm
SII Semiconductor Corporation	

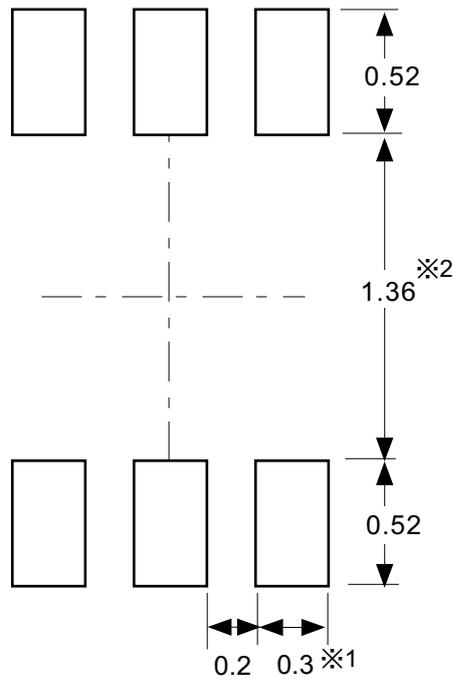


Enlarged drawing in the central part



No. PI006-A-R-SD-1.0

TITLE	SNT-6A(H)-A-Reel		
No.	PI006-A-R-SD-1.0		
SCALE		QTY.	5,000
UNIT	mm		
SII Semiconductor Corporation			



※1. ランドパターンの幅に注意してください (0.25 mm min. / 0.30 mm typ.).

※2. パッケージ中央にランドパターンを広げないでください (1.30 mm ~ 1.40 mm)。

- 注意
1. パッケージのモールド樹脂下にシルク印刷やハンダ印刷などしないでください。
  2. パッケージ下の配線上のソルダーレジストなどの厚みをランドパターン表面から0.03 mm以下にしてください。
  3. マスク開口サイズと開口位置はランドパターンと合わせてください。
  4. 詳細は "SNTパッケージ活用の手引き" を参照してください。

※1. Pay attention to the land pattern width (0.25 mm min. / 0.30 mm typ.).

※2. Do not widen the land pattern to the center of the package (1.30 mm to 1.40 mm).

- Caution**
1. Do not do silkscreen printing and solder printing under the mold resin of the package.
  2. The thickness of the solder resist on the wire pattern under the package should be 0.03 mm or less from the land pattern surface.
  3. Match the mask aperture size and aperture position with the land pattern.
  4. Refer to "SNT Package User's Guide" for details.

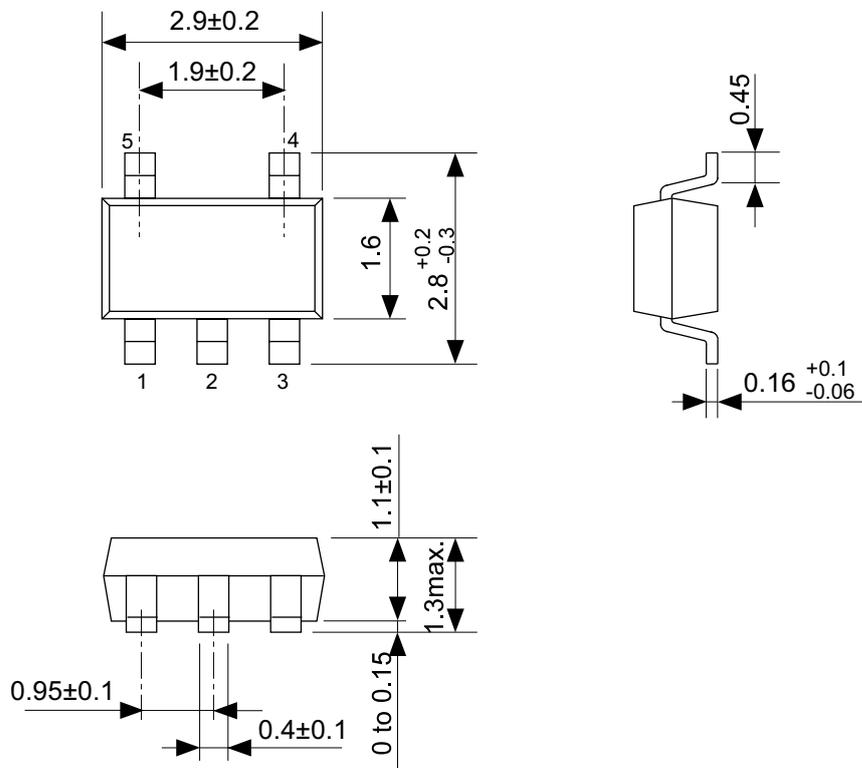
※1. 请注意焊盘模式的宽度 (0.25 mm min. / 0.30 mm typ.).

※2. 请勿向封装中间扩展焊盘模式 (1.30 mm ~ 1.40 mm)。

- 注意
1. 请勿在树脂型封装的下面印刷丝网、焊锡。
  2. 在封装下、布线上的阻焊膜厚度 (从焊盘模式表面起) 请控制在 0.03 mm 以下。
  3. 钢网的开口尺寸和开口位置请与焊盘模式对齐。
  4. 详细内容请参阅 "SNT 封装的应用指南"。

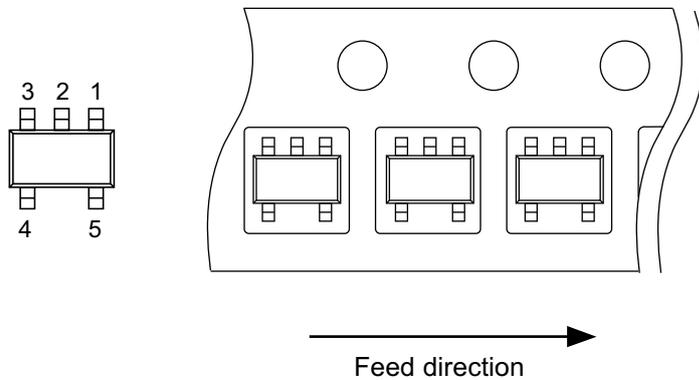
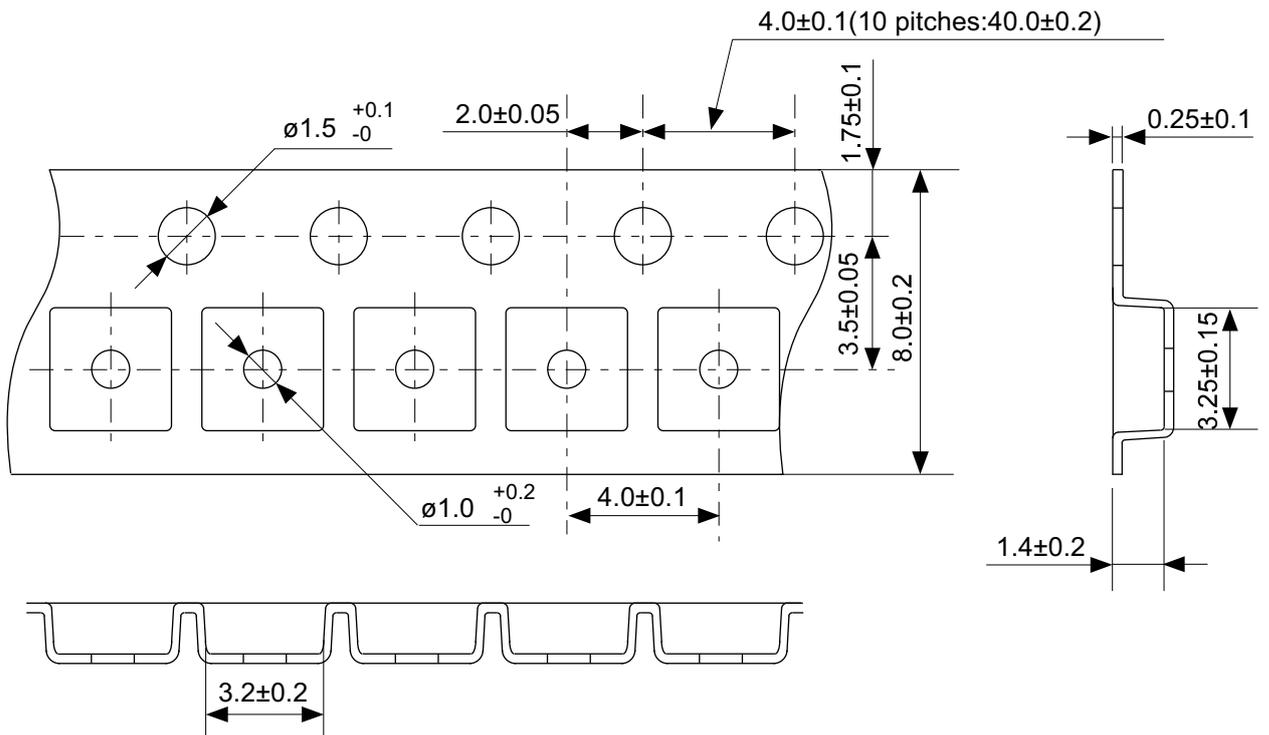
No. PI006-A-L-SD-4.1

TITLE	SNT-6A(H)-A -Land Recommendation
No.	PI006-A-L-SD-4.1
SCALE	
UNIT	mm
SII Semiconductor Corporation	



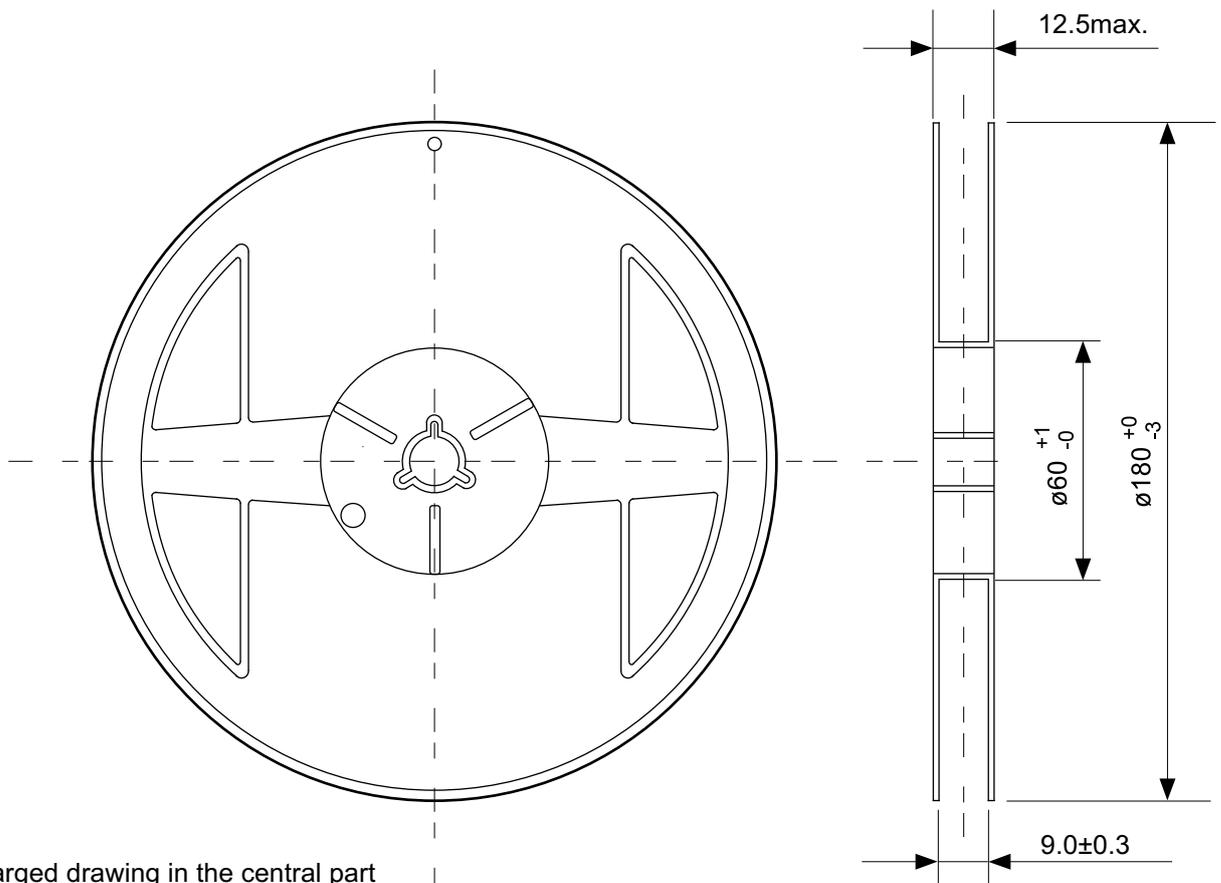
No. MP005-A-P-SD-1.2

TITLE	SOT235-A-PKG Dimensions
No.	MP005-A-P-SD-1.2
SCALE	
UNIT	mm
SII Semiconductor Corporation	

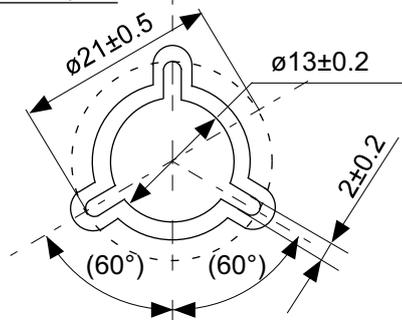


No. MP005-A-C-SD-2.1

TITLE	SOT235-A-Carrier Tape
No.	MP005-A-C-SD-2.1
SCALE	
UNIT	mm
SII Semiconductor Corporation	



Enlarged drawing in the central part



No. MP005-A-R-SD-1.1

TITLE	SOT235-A-Reel		
No.	MP005-A-R-SD-1.1		
SCALE		QTY.	3,000
UNIT	mm		
SII Semiconductor Corporation			

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2. The circuit examples and the usages described herein are for reference only, and do not guarantee the success of any specific mass-production design.  
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3. SII Semiconductor Corporation is not responsible for damages caused by the incorrect information described herein.
4. Take care to use the products described herein within their specified ranges. Pay special attention to the absolute maximum ratings, operation voltage range and electrical characteristics, etc.  
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5. When using the products described herein, confirm their applications, and the laws and regulations of the region or country where they are used and verify suitability, safety and other factors for the intended use.
6. When exporting the products described herein, comply with the Foreign Exchange and Foreign Trade Act and all other export-related laws, and follow the required procedures.
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8. The products described herein are not designed to be used as part of any device or equipment that may affect the human body, human life, or assets (such as medical equipment, disaster prevention systems, security systems, combustion control systems, infrastructure control systems, vehicle equipment, traffic systems, in-vehicle equipment, aviation equipment, aerospace equipment, and nuclear-related equipment), excluding when specified for in-vehicle use or other uses. Do not use those products without the prior written permission of SII Semiconductor Corporation. Especially, the products described herein cannot be used for life support devices, devices implanted in the human body and devices that directly affect human life, etc.  
Prior consultation with our sales office is required when considering the above uses.  
SII Semiconductor Corporation is not responsible for damages caused by unauthorized or unspecified use of our products.
9. Semiconductor products may fail or malfunction with some probability.  
The user of these products should therefore take responsibility to give thorough consideration to safety design including redundancy, fire spread prevention measures, and malfunction prevention to prevent accidents causing injury or death, fires and social damage, etc. that may ensue from the products' failure or malfunction.  
The entire system must be sufficiently evaluated and applied on customer's own responsibility.
10. The products described herein are not designed to be radiation-proof. The necessary radiation measures should be taken in the product design by the customer depending on the intended use.
11. The products described herein do not affect human health under normal use. However, they contain chemical substances and heavy metals and should therefore not be put in the mouth. The fracture surfaces of wafers and chips may be sharp. Take care when handling these with the bare hands to prevent injuries, etc.
12. When disposing of the products described herein, comply with the laws and ordinances of the country or region where they are used.
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