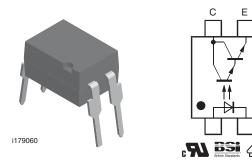
# SFH655A



Vishay Semiconductors

# **Optocoupler, Photodarlington Output**



### DESCRIPTION

The SFH655A is optically coupled isolators with a gallium arsenide infrared LED and a silicon photodarlington detector. Switching can be achieved while maintaining a high degree of isolation between driving and load circuits.

This optocouplers can be used to replace reed and mercury relays with advantages of long life, high speed switching and elimination of magnetic fields.

### FEATURES

- High isolation test voltage 5300 V<sub>RMS</sub>
- Standard plastic DIP-4 package
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

### AGENCY APPROVALS

- UL file no. E52744 system code H, double protection
- DIN EN 60747-5-2 (VDE 0884), IEC 60747-5-5
- DIN EN 60747-5-5 (VDE 0884) pending
- BSI IEC 60950; IEC 60065

| #     A     -     #     #     #     #     DIP     Option 9 |  |  |  |  |  |
|--|--|--|--|--|--|
|  |  |  |  |  |  |
| CTR (%)  |  |  |  |  |  |
| > 600  |  |  |  |  |  |
| SFH655A  |  |  |  |  |  |
| SFH655A-X009   |  |  |  |  |  |
|  |  |  |  |  |  |

#### Note

• For additional information on the available options refer to option information.

| PARAMETER                           | TEST CONDITION        | SYMBOL            | VALUE | UNIT  |
|-------------------------------------|-----------------------|-------------------|-------|-------|
| INPUT                               |                       |                   |       |       |
| Peak reverse voltage                |                       | V <sub>RM</sub>   | 6     | V     |
| Forward continuous current          |                       | I <sub>F</sub>    | 60    | mA    |
| Surge forward current               | t <sub>p</sub> ≤ 10μs | I <sub>FSM</sub>  | 2.5   | A     |
| Derate linearly from 25 °C          |                       |                   | 1.33  | mW/°C |
| Power dissipation                   |                       | P <sub>diss</sub> | 100   | mW    |
| OUTPUT                              |                       |                   |       |       |
| Collector emitter breakdown voltage |                       | BV <sub>CEO</sub> | 55    | V     |
| Emitter collector breakdown voltage |                       | BV <sub>ECO</sub> | 6     | V     |
| Collector (load) current            |                       | Ι <sub>C</sub>    | 125   | mA    |
| Derate linearly from 25 °C          |                       |                   | 2     | mW/°C |
| Power dissipation                   |                       | P <sub>diss</sub> | 150   | mW    |

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| ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)     |  |                   |                    |                  |  |  |  |  |  |
|---|--|-------------------|--------------------|------------------|--|--|--|--|--|
| PARAMETER   | TEST CONDITION   | SYMBOL            | VALUE              | UNIT             |  |  |  |  |  |
| COUPLER   |  |                   |                    |                  |  |  |  |  |  |
| Derate linearly from 25 °C  |  |                   | 3.33               | mW/°C            |  |  |  |  |  |
| Total power dissipation   |  | P <sub>tot</sub>  | 250                | mW               |  |  |  |  |  |
| Isolation voltage   |  | V <sub>IORM</sub> | 890                | VP               |  |  |  |  |  |
| Isolation test voltage between input and output, climate acc. to IEC 60068 - 1:1988 |  | V <sub>ISO</sub>  | 5300               | V <sub>RMS</sub> |  |  |  |  |  |
| Creepage distance   |  |                   | ≥7                 | mm               |  |  |  |  |  |
| Clearance distance  |  |                   | ≥7                 | mm               |  |  |  |  |  |
| Comparative tracking index<br>acc. to DIN IEC 112/VDE 0303, part 1:06-84            |  |                   | ≥ 175              |                  |  |  |  |  |  |
| Isolation resistance  | $V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 25 ^{\circ}\text{C}$  | R <sub>IO</sub>   | ≥ 10 <sup>12</sup> | Ω                |  |  |  |  |  |
| Isolation resistance  | $V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$ | R <sub>IO</sub>   | ≥ 10 <sup>11</sup> | Ω                |  |  |  |  |  |
| Storage temperature range   |  | T <sub>stg</sub>  | - 55 to + 150      | °C               |  |  |  |  |  |
| Operating temperature range   |  | T <sub>amb</sub>  | - 55 to + 100      | °C               |  |  |  |  |  |
| Soldering temperature <sup>(1)</sup>  | max. 10 s, dip soldering distance to seating plane $\geq$ 1.5 mm | T <sub>sld</sub>  | 260                | °C               |  |  |  |  |  |

#### Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

<sup>(1)</sup> Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

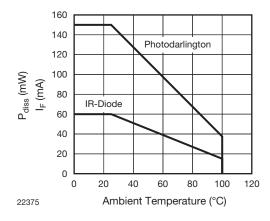


Fig. 1 - Power Dissipation vs. Ambient Temperature



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| <b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified) |   |         |                    |      |      |      |      |  |
|---|---|---------|--------------------|------|------|------|------|--|
| PARAMETER   | TEST CONDITION                          | PART    | SYMBOL             | MIN. | TYP. | MAX. | UNIT |  |
| INPUT   |   |         |                    |      |      |      |      |  |
| Forward voltage   | I <sub>F</sub> = 10 mA                  |         | V <sub>F</sub>     |      | 1.15 | 1.5  | V    |  |
| Reverse current   | V <sub>R</sub> = 6 V                    |         | I <sub>R</sub>     |      | 0.02 | 10   | μA   |  |
| Capacitance   | $V_R = 0 V$ , f = 1 MHz                 |         | Co                 |      | 50   |      | pF   |  |
| OUTPUT  |   |         |                    |      |      |      |      |  |
| Collector emitter breakdown voltage   | I <sub>CE</sub> = 100 μA                |         | BV <sub>CEO</sub>  | 55   |      |      | V    |  |
| Emitter collector breakdown voltage   | I <sub>EC</sub> = 10 μA                 |         | BV <sub>ECO</sub>  | 6    |      |      | V    |  |
| Collector emitter dark current  | V <sub>CE</sub> = 40 V                  |         | I <sub>CEO</sub>   |      | 12   | 400  | nA   |  |
| Collector emitter capacitance   | V <sub>CE</sub> = 0 V, f = 1 MHz        |         | C <sub>CE</sub>    |      | 13.5 |      | pF   |  |
| COUPLER   |   |         |                    |      |      |      |      |  |
| Collector emitter<br>saturation voltage   | $I_{\rm F} = 20$ mA, $I_{\rm C} = 5$ mA | SFH655A | V <sub>CEsat</sub> |      |      | 1    | V    |  |
| Coupling capacitance  | V <sub>I-O</sub> = 0 V, f = 1 MHz       |         | C <sub>C</sub>     |      | 0.45 |      | pF   |  |

#### Note

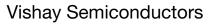
• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

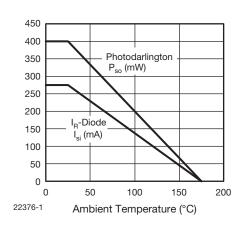
| CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified) |  |         |     |     |  |  |      |  |  |
|---|--|---------|-----|-----|--|--|------|--|--|
| PARAMETER TEST CONDITION PART SYMBOL MIN. TYP. MAX. UNIT                      |  |         |     |     |  |  | UNIT |  |  |
| Current transfer ratio  | $I_F = 1 \text{ mA}, V_{CE} = 2 \text{ V}$ | SFH655A | CTR | 600 |  |  | %    |  |  |

| PARAMETER  | TEST CONDITION   | SYMBOL          | MIN.             | TYP.      | MAX. | UNIT |
|--|--|-----------------|------------------|-----------|------|------|
| Partial discharge test voltage - routine test              | 100 %, t <sub>test</sub> = 1 s   | V <sub>pd</sub> | 1.669            |           |      | kV   |
| Partial discharge test voltage -<br>lot test (sample test) | $t_{Tr} = 60 \text{ s, } t_{test} = 10 \text{ s,}$<br>(see fig. 2)             | V <sub>pd</sub> | 1.424            |           |      | kV   |
|  | V <sub>IO</sub> = 500 V  | R <sub>IO</sub> | 10 <sup>12</sup> |           |      | Ω    |
| Insulation resistance                                      | $V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$               | R <sub>IO</sub> | 10 <sup>11</sup> |           |      | Ω    |
|  | V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 150 °C<br>(construction test only) | R <sub>IO</sub> | 10 <sup>9</sup>  |           |      | Ω    |
| Climatic classification<br>(according to IEC 68 part 1)    |  |                 |                  | 55/100/21 |      |      |
| Comparative tracking index                                 |  | CTI             | 175              |           | 399  |      |
| Creepage distance  | standard DIP-4   |                 | 7                |           |      | mm   |
| Clearance distance   | standard DIP-4   |                 | 7                |           |      | mm   |
| Insulation thickness, reinforced rated                     | per IEC 60950 2.10.5.1   |                 | 0.4              |           |      | mm   |
| Forward current  |  | I <sub>si</sub> |                  |           | 275  | mA   |
| Power dissipation  |  | P <sub>so</sub> |                  |           | 400  | mW   |
| Safety temperature   |  | T <sub>si</sub> |                  |           | 175  | °C   |

#### Note

 According to DIN EN 60747-5-2 (VDE 0884) (see fig. 2). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.





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Fig. 2 - Derating Diagram

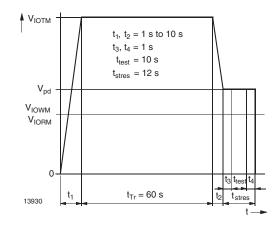
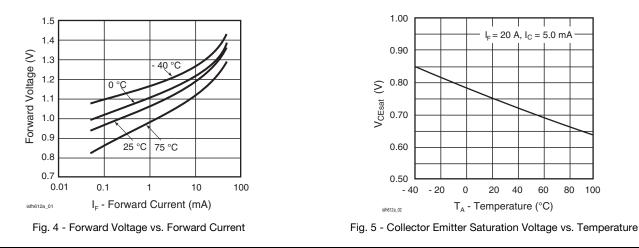


Fig. 3 - Test Pulse Diagram for Sample Test according to DIN EN 60747-5-2 (VDE 0884); IEC 60747-5-5

| SWITCHING CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified) |   |         |                  |      |      |      |      |  |
|--|---|---------|------------------|------|------|------|------|--|
| PARAMETER  | TEST CONDITION  | PART    | SYMBOL           | MIN. | TYP. | MAX. | UNIT |  |
| Turn-on time<br>(fig. 10, test circuit 1)  | $V_{CC}$ = 10 V, I <sub>C</sub> = 2 mA, R <sub>L</sub> = 100 $\Omega$ | SFH612A | t <sub>on</sub>  |      | 16   |      | μs   |  |
| Turn-off time<br>(fig. 10, test circuit 1)                                       | $V_{CC}$ = 10 V, I <sub>C</sub> = 2 mA, R <sub>L</sub> = 100 $\Omega$ | SFH612A | t <sub>off</sub> |      | 15   |      | μs   |  |
| Rise time<br>(fig. 10, test circuit 1)   | $V_{CC}$ = 10 V, I <sub>C</sub> = 2 mA, R <sub>L</sub> = 100 $\Omega$ | SFH612A | t <sub>r</sub>   |      | 14   |      | μs   |  |
| Fall time<br>(fig. 10, test circuit 1)   | $V_{CC}$ = 10 V, I <sub>C</sub> = 2 mA, R <sub>L</sub> = 100 $\Omega$ | SFH612A | t <sub>f</sub>   |      | 14   |      | μs   |  |
| Turn-on time<br>(fig. 11, test circuit 2)  | $V_{CC}$ = 2 V, $I_{C}$ = 10 mA, $R_{L}$ = 100 $\Omega$               | SFH655A | t <sub>on</sub>  |      | 31   |      | μs   |  |
| Turn-off time<br>(fig. 11, test circuit 2)                                       | $V_{CC}$ = 2 V, $I_{C}$ = 10 mA, $R_{L}$ = 100 $\Omega$               | SFH655A | t <sub>off</sub> |      | 55   |      | μs   |  |
| Rise time<br>(fig. 11, test circuit 2)   | $V_{CC}$ = 2 V, $I_{C}$ = 10 mA, $R_{L}$ = 100 $\Omega$               | SFH655A | t <sub>r</sub>   |      | 27   | 250  | μs   |  |
| Fall time<br>(fig. 11, test circuit 2)   | $V_{CC}$ = 2 V, $I_{C}$ = 10 mA, $R_{L}$ = 100 $\Omega$               | SFH655A | t <sub>f</sub>   |      | 56   | 200  | μs   |  |

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)



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4 For technical questions, contact: <u>optocoupleranswers@vishay.com</u>

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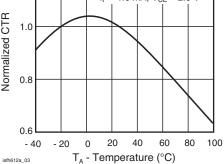


Fig. 6 - Normalized CTR vs. Temperature

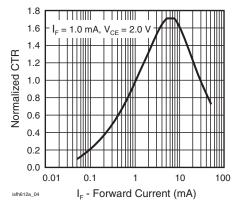


Fig. 7 - Normalized CTR vs. Forward Current

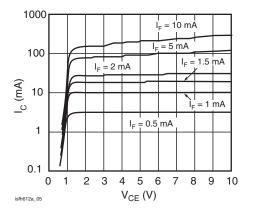


Fig. 8 - Collector Current vs. Collector Emitter Voltage

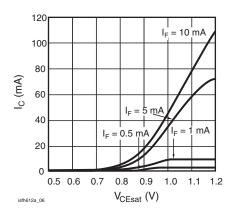


Fig. 9 - Collector Current vs. Collector Emitter Saturation Voltage

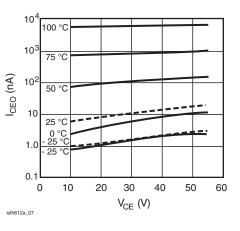


Fig. 10 - Collector Emitter Dark Current vs. Collector Emitter Voltage over Temperature

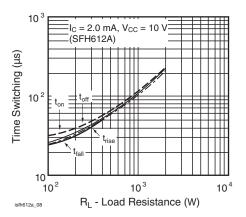


Fig. 11 - Switching Time vs. Load Resistor

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# SFH655A

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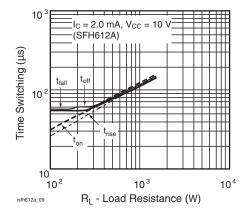
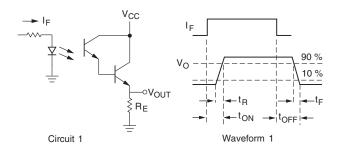
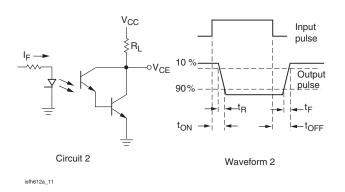


Fig. 12 - Switching Time vs. Load Resistor



isfh612a\_10

Fig. 13 - Switching Time Test Circuit and Waveforms





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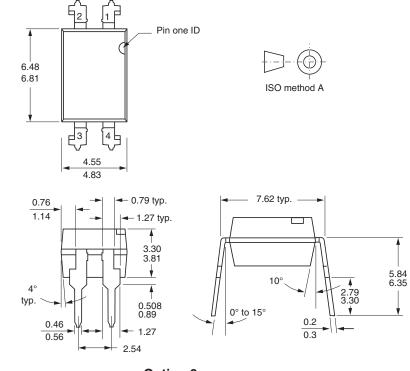
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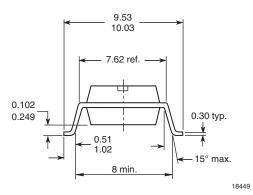
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### **PACKAGE DIMENSIONS** in millimeters

i178027



**Option 9** 



### **PACKAGE MARKING**



### Note

• VDE logo is only printed on option 1 parts. Option information is not marked on the part.



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