

N-Channel 12 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^a	Q _g (Typ.)			
	0.095 at V _{GS} = 4.5 V	1.32				
12	0.104 at V _{GS} = 2.5 V	1.26	5.25			
	0.114 at V _{GS} = 1.8 V	0.88				

FEATURES

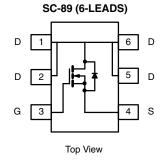
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC

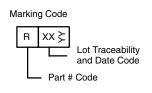


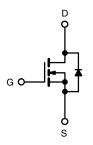
ROHS COMPLIANT HALOGEN FREE

APPLICATIONS

· Load Switch for Portable Devices







Ordering Information: Si1054X-T1-GE3 (Lead (Pb)-free and Halogen-free)

N-Channel MOSFET

ABSOLUTE MAXIMUM RATING	S (T _A = 25 °C, un	less otherwise	e noted)		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	12	V	
Gate-Source Voltage		V _{GS}	± 8	v	
Continuous Drain Current (T _{.I} = 150 °C)	T _A = 25 °C	-	1.32 ^{b, c}		
Continuous Diairi Current (1) = 130 °C)	T _A = 70 °C	- I _D -	1.05 ^{b, c}		
Pulsed Drain Current		I _{DM}	6	A	
Continuous Source-Drain Diode Current	T _A = 25 °C	Is	0.2 ^{b, c}		
N	T _A = 25 °C	P _D	0.236 ^{b, c}	w	
Maximum Power Dissipation ^a	T _A = 70 °C] '	0.151 ^{b, c}	v	
Operating Junction and Storage Temperature R	T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Typical	Maximum	Unit		
Manifestor Location to Austrianth d	t ≤ 5 s	R _{thJA}	440	530	°C/W	
Maximum Junction-to-Ambient ^{b, d}	Steady State	' 'thJA	540	650	C/VV	

Notes

- a. Based on $T_A = 25$ °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. Maximum under steady state conditions is 650 $^{\circ}\text{C/W}.$

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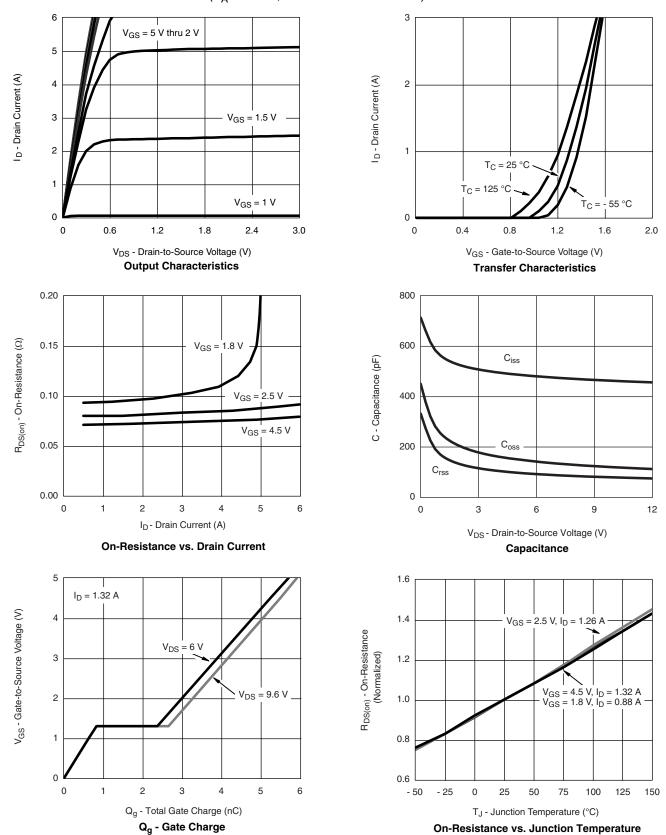
Parameter Symbol		Test Conditions	Тур.	Max.	Unit			
Static								
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	12			V		
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I - 250 uA		12.23		mV/°C		
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 2.76				
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.4		1	V		
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA		
Zana Oata Wallana Busin Oamant	I _{DSS}	V _{DS} = 12 V, V _{GS} = 0 V			1	nA		
Zero Gate Voltage Drain Current		V _{DS} = 12 V, V _{GS} = 0 V, T _J = 85 °C			10	μΑ		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	6			Α		
		V _{GS} = 4.5 V, I _D = 1.32 A		0.079	0.095			
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 2.5 V, I _D = 1.26 A		0.087	0.104	Ω		
		V _{GS} = 1.8 V, I _D = 0.88 A		0.095	0.114			
Forward Transconductance	9 _{fs}	V _{DS} = 4.5 V, I _D = 1.32 A		6.25		S		
Dynamic ^b	-		,	1	'			
Input Capacitance	C _{iss}			480				
Output Capacitance	C _{oss}	$V_{DS} = 6 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		142		pF		
Reverse Transfer Capacitance	C _{rss}			92				
Total Oats Observe	Vpg = 6 V Vgg = 5 V lp = 1 32 A		5.71	8.57				
Total Gate Charge	Q_g			5.25	7.9	nC		
Gate-Source Charge	Q_{gs}	$V_{DS} = 6 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 1.32 \text{ A}$		0.83				
Gate-Drain Charge	Q _{gd}			1.54				
Gate Resistance	R_{g}	f = 1 MHz		3.5	5.25	Ω		
Turn-On Delay Time	t _{d(on)}			5.5	8.25			
Rise Time	t _r	$V_{DD} = 6 \text{ V}, R_{L} = 5.71 \Omega$		13	19.5	ns		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 1.05 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		37	55.5			
Fall Time	t _f			14	21			
Drain-Source Body Diode Characteristics								
Pulse Diode Forward Current ^a	I _{SM}				6	Α		
Body Diode Voltage	V _{SD}	I _S = 1.0 A		0.8	1.2	V		
Body Diode Reverse Recovery Time	t _{rr}			19.3	28.95	ns		
Body Diode Reverse Recovery Charge Q _{rr}		1 1 0 4 31/31 100 4/		5.8	8.7	nC		
Reverse Recovery Fall Time	t _a	$I_F = 1.0 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}$		7.4		ns		
Reverse Recovery Rise Time	t _b			11.9				

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



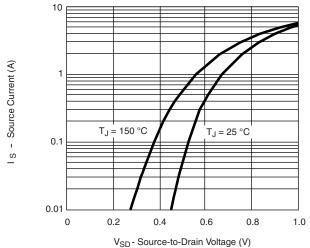
TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)

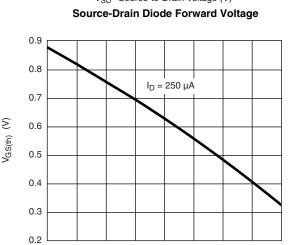


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TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)





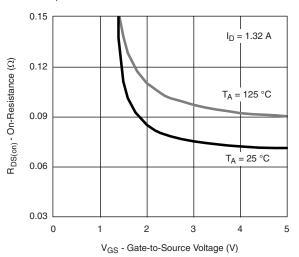
T_J - Temperature (°C)

Threshold Voltage

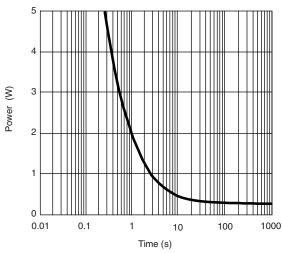
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100

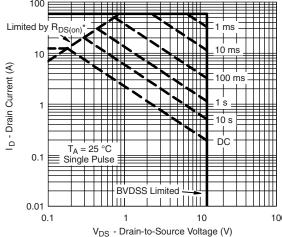
125



 $R_{DS(on)}$ vs. V_{GS} vs. Temperature



Single Pulse Power



* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient

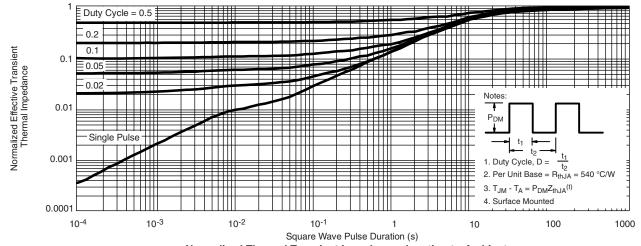
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- 25





TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)

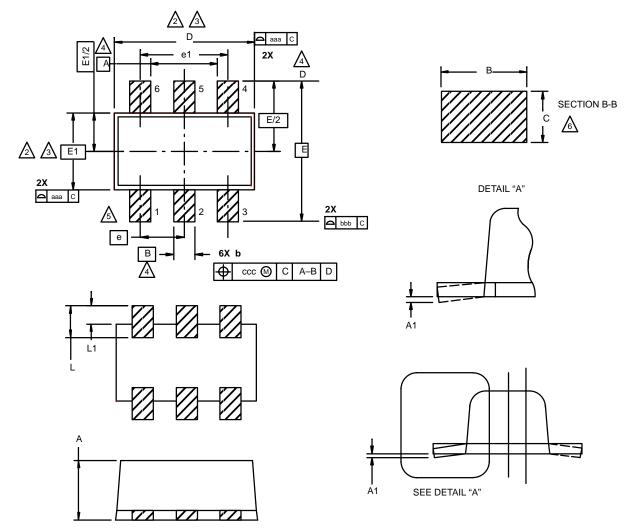


Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?69579.



SC89: 6- LEADS (SOT-563F)



NOTES:

1. Dimensions in millimeters.



Dimension D does not include mold flash, protrusions or gate burrs. Mold flush, protrusions or gate burrs shall not exceed 0.15 mm per dimension E1 does not include interlead flash or protrusion, interlead flash or protrusion shall not exceed 0.15 mm per side.



Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, the bar burrs, gate burrs and interlead flash, but including any mismatch between the top and the bottom of the plastic body.



Datums A, B and D to be determined 0.10 mm from the lead tip.



Terminal numbers are shown for reference only.



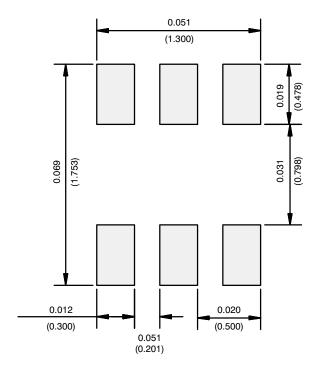
These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.

	MILLIM	IETERS			Tolerances Of Form And	
Dim	Min	Max	Note	Symbol	Position	
Α	0.56	0.60		aaa	0.10	
A1	0.00	0.10		bbb	0.10	
b	0.15	0.30		ccc	0.10	
С	0.10	0.18				
D	1.50	1.70	2, 3			
E	1.55	1.70				
E1	1.20 BSC		2, 3			
е	0.50 BSC					
e1	1.00 BSC					
L	0.35 BSC					
L1	0.20 BSC					
ECN: E-00499—Rev. B, 02-Jul-01						

DWG: 5880



RECOMMENDED MINIMUM PADS FOR SC-89: 6-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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Revision: 02-Oct-12 Document Number: 91000