

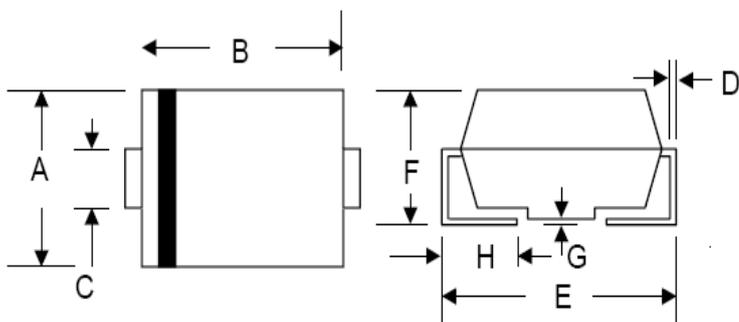
SK22 THRU SK210 SCHOTTKY RECTIFIER

Features:

- Schottky Barrier Chip
- Ideally Suited for Automatic Assembly
- Low Power Loss, High Efficiency
- For Use in Low Voltage Application
- Guard ring Die Construction
- Plastic Case Material has UL Flammability Classification Rating 94F-0
- Green products in compliance the ROHS directive
- This is a Pb – Free device
- All SMC parts are traceable to the wafer lot
- Additional testing can be offered upon request

Mechanical Data:

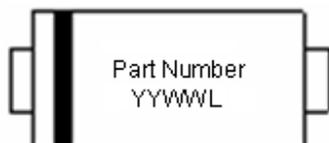
- Case: Low Profile Molded plastic
- Terminals: Plated leads solderable per MIL-STD-750, Method 2026 guaranteed
- Polarity: Color band or cathode Notch
- Mounting Position: Any

Mechanical Dimensions: In mm / Inches


SMB/DO-214AA				
Dim	Min	Max	Min	Max
A	3.30	3.94	0.130	0.155
B	4.06	4.70	0.160	0.185
C	1.91	2.11	0.075	0.083
D	0.152	0.305	0.006	0.012
E	5.08	5.59	0.2	0.220
F	2.13	2.44	0.084	0.096
G	0.051	0.203	0.002	0.008
H	0.76	1.27	0.029	0.05
	in mm		In inch	

SMB

Marking Diagram:



First row: Part Number (SK22, SK23, SK24, SK25, SK26, SK28, SK29, SK210)

Second row: YYWWL

YY is the manufacture year, WW is the manufacture week code, L is the wafer's Lot Number

Ordering Information:

Device	Package	Shipping
SK22 SK23 SK24 SK25 SK26 SK28 SK29 SK210	SMB (Pb-Free)	3000pcs / reel

For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification.



SK22 THRU SK210

Technical Data
Data Sheet N0155, Rev. A

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Maximum Ratings and Electrical characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	SK22	SK23	SK24	SK25	SK26	SK28	SK29	SK210	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	20	30	40	50	60	80	90	100	V
Maximum RMS voltage	V_{RMS}	14	21	28	35	42	56	64	71	V
Average Rectified Output Current @ $T_L = 105^\circ\text{C}$	I_O	2.0								A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	50								A
Forward Voltage @ $I_O = 2.0\text{ A}$	V_F	0.55		0.70		0.85				V
Peak Reverse Current @ $T_A = 25^\circ\text{C}$ At Rated DC Blocking Voltage @ $T_A = 100^\circ\text{C}$	I_{RM}	0.5 20								mA
Typical Thermal Resistance Junction to Ambient (Note 1)	$R_{\theta JA}$	75								K/W
Operating Temperature Range	T_J	-65 to +125								$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-65 to +150								$^\circ\text{C}$

Note: 1. mounted on P.C. Board with 8.0mm² copper pad areas.

- China - Germany - Korea - Singapore - United States •
- <http://www.smc-diodes.com> - sales@smc-diodes.com •

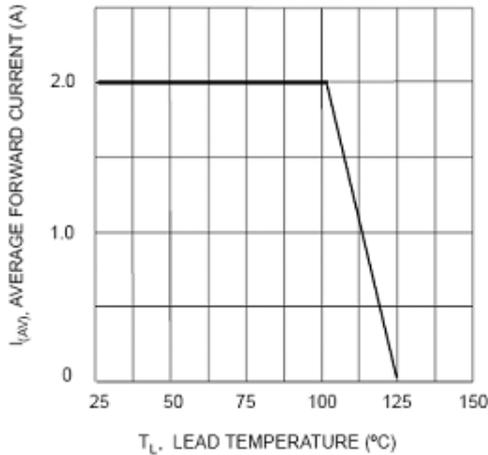


Fig. 1 Forward Current Derating Curve

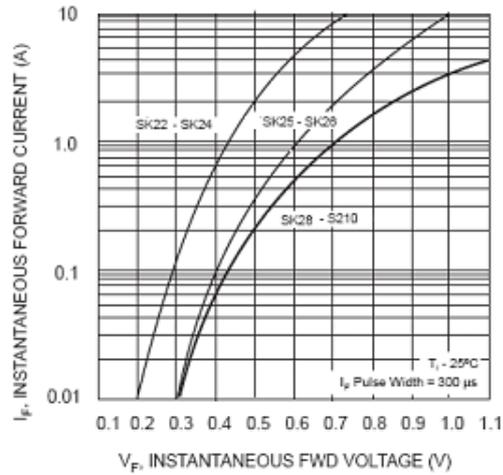


Fig. 2 Typ. Forward Characteristics

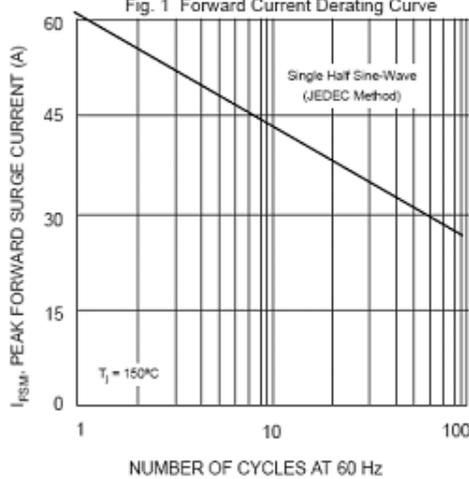


Fig. 3 Max Non-Repetitive Peak Fwd Surge Current

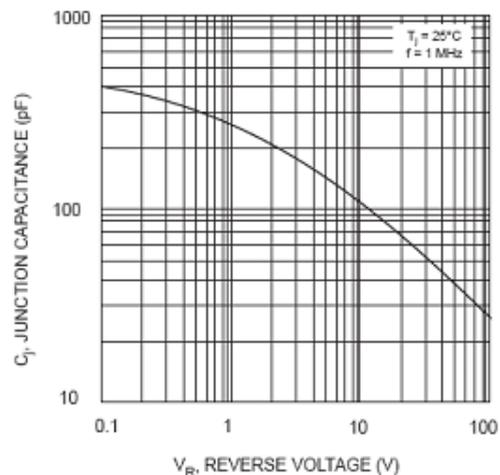


Fig. 4 Typical Junction Capacitance

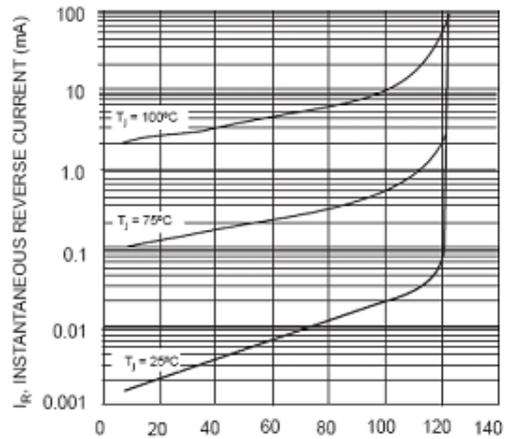


Fig. 5 Typical Reverse Characteristics



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