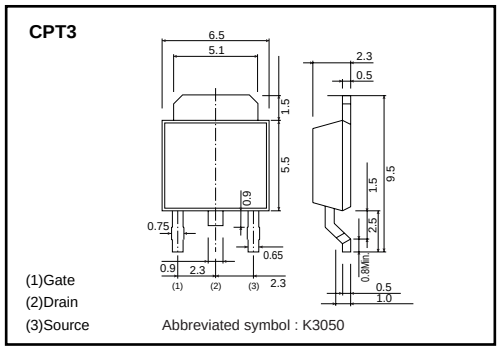
10V Drive Nch MOS FET

2SK3050

●Structure
Silicon N-channel MOSFET

●Features
1) Low on-resistance.
2) Fast switching speed.
3) Wide SOA (safe operating area).
4) Gate-source voltage (V_{GSS}) guaranteed to be ±30V.
5) Drive circuits can be simple.
6) Parallel use is easy.

●External dimensions (Unit : mm)

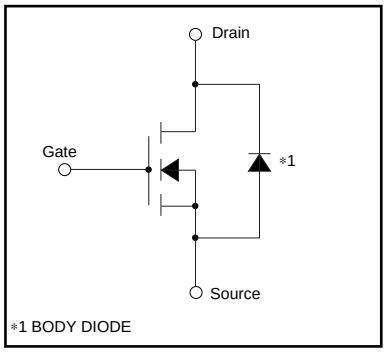


●Applications
Switching

●Packaging specifications

Type	Package	Taping
	Code	TL
	Basic ordering unit (pieces)	2500
2SK3050		○

●Inner circuit



●Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Drain-source voltage		V_{DSS}	600	V
Gate-source voltage		V_{GSS}	±30	V
Drain current	Continuous	I_D	2	A
	Pulsed	I_{DP} ∗1	6	A
Reverse drain current	Continuous	I_{DR}	2	A
	Pulsed	I_{DRP} ∗1	6	A
Source current (Body Diode)	Continuous	I_S	2	A
	Pulsed	I_{SP} ∗1	6	A
Avalanche Current		I_{AS} ∗2	2	A
Avalanche Energy		E_{AS} ∗2	21	mJ
Total power dissipation (Tc=25°C)		P_D	20	W
Channel temperature		Tch	150	°C
Storage temperature		Tstg	−55 to +150	°C

∗1 Pw≤10μs, Duty cycle≤1%
∗2 L≒10mH, V_{DD}=50V, R_G=25Ω, 1Pulse, Tch=25°C

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to case	Rth(ch-c)	6.25	°C/W

●**Electrical characteristics** (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I_{GSS}	–	–	±100	nA	V_{GS}=±30V, V_{DS}=0V
Drain-source breakdown voltage	$V_{(BR)DSS}$	600	–	–	V	I_D=1mA, V_{GS}=0V
Zero gate voltage drain current	I_{DSS}	–	–	100	μA	V_{DS}=600V, V_{GS}=0V
Gate threshold voltage	$V_{GS(th)}$	2.0	–	4.0	V	V_{DS}=10V, I_D=1mA
Static drain-source on-state resistance	$R_{DS(on)}$ *	–	4.4	5.5	Ω	I_D=1A, V_{GS}=10V
Forward transfer admittance	$\|Y_{fs}\|$ *	0.5	1.0	–	S	I_D=1A, V_{DS}=10V
Input capacitance	C_{iss}	–	280	–	pF	V_{DS}=10V
Output capacitance	C_{oss}	–	48	–	pF	V_{GS}=0V
Reverse transfer capacitance	C_{rss}	–	16	–	pF	f=1MHz
Turn-on delay time	$t_{d(on)}$ *	–	12	–	ns	I_D=1A, V_{DD} ≒300V
Rise time	t_r *	–	17	–	ns	V_{GS}=10V
Turn-off delay time	$t_{d(off)}$ *	–	29	–	ns	R_L=300Ω
Fall time	t_f *	–	105	–	ns	R_G=10Ω
Total gate charge	Q_g *	–	12.8	25.6	nC	V_{DD}=300V
Gate-source charge	Q_{gs} *	–	3.3	–	nC	V_{GS}=10V
Gate-drain charge	Q_{gd} *	–	5.5	–	nC	I_D=2A

* Pulsed

●**Body diode characteristics** (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V_{SD}	–	–	2.0	V	I_S= 2A, V_{GS}=0V
Reverse recovery time	t_{rr} *	–	460	–	ns	I_{DR}=2A, V_{GS}=0V
Reverse recovery charge	Q_{rr} *	–	2.0	–	μC	di/dt= 100A / μs

* Pulsed

●**Electrical characteristic curves**

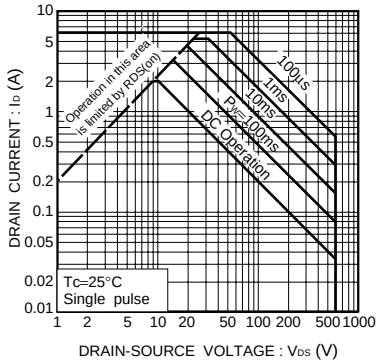


Fig.1 Maximum safe operating area

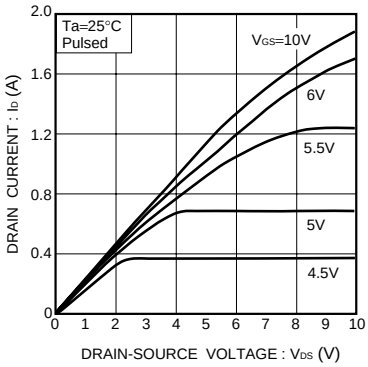


Fig.2 Typical output characteristics

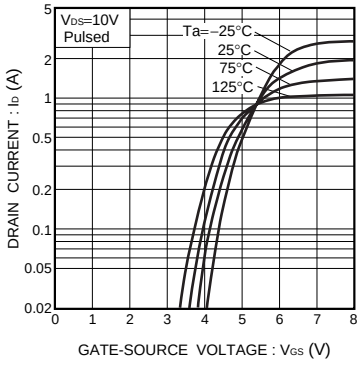


Fig.3 Typical transfer characteristics

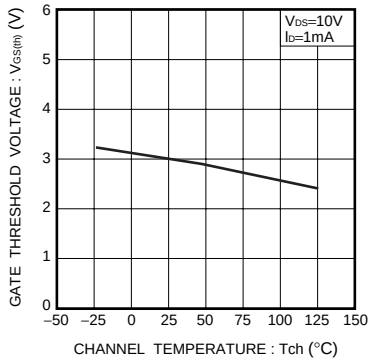


Fig.4 Gate threshold voltage vs. channel temperature

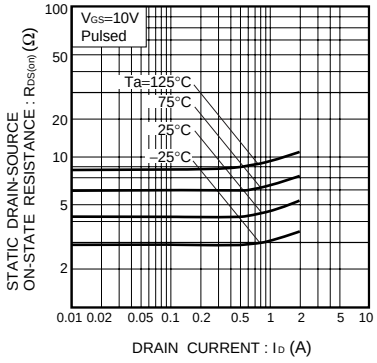


Fig.5 Static drain-source on-state resistance vs. drain current

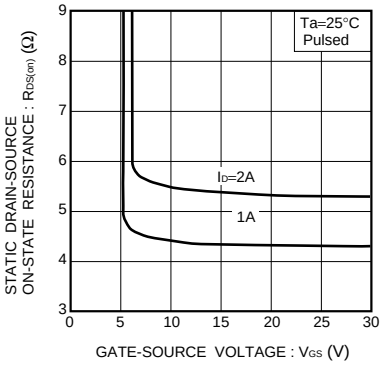


Fig.6 Static drain-source on-state resistance vs. gate-source voltage

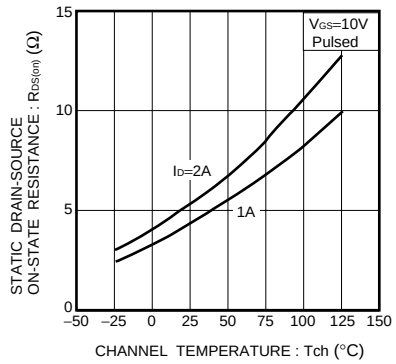


Fig.7 Static drain-source on-state resistance vs. channel temperature

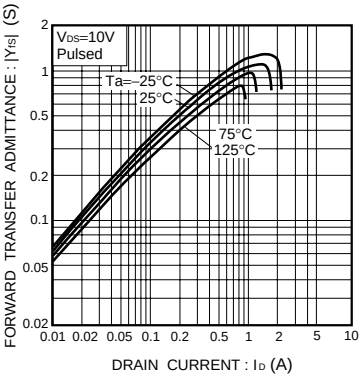


Fig.8 Forward transfer admittance vs. drain current

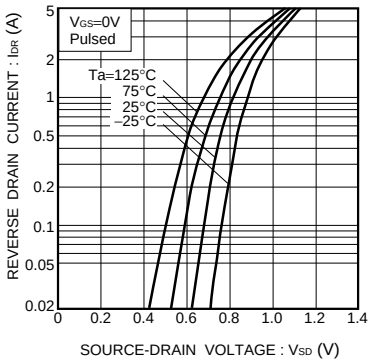


Fig.9 Reverse drain current vs. source-drain voltage (I)

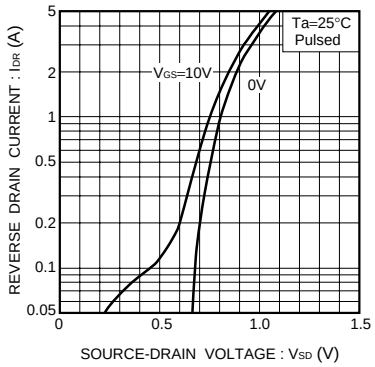


Fig.10 Reverse drain current vs. source-drain voltage (II)

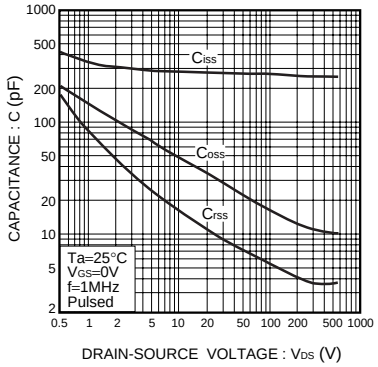


Fig.11 Typical capacitance vs. drain-source voltage

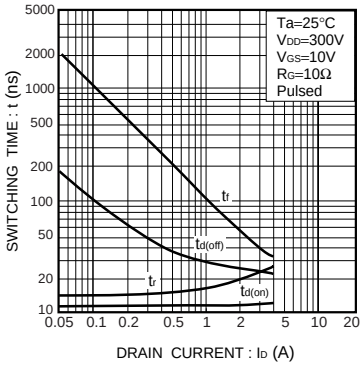


Fig.12 Switching characteristics

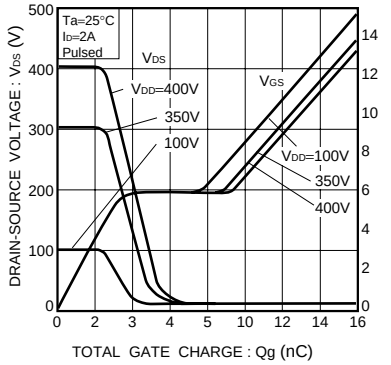


Fig.13 Dynamic input characteristics

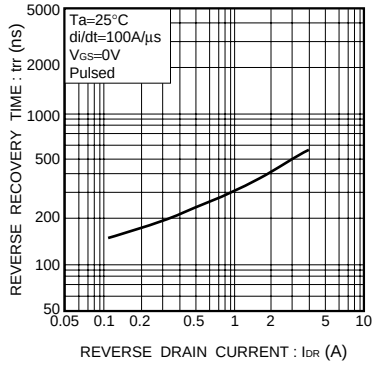


Fig.14 Reverse recovery time vs. reverse drain current

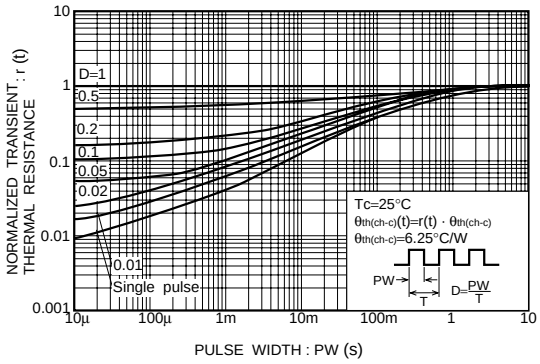


Fig.15 Normalized transient thermal resistance vs. pulse width

●**Switching characteristics measurement circuit**

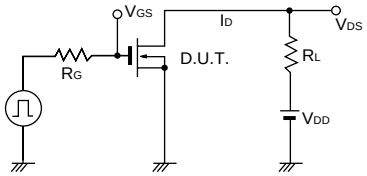


Fig.16 Switching time measurement circuit

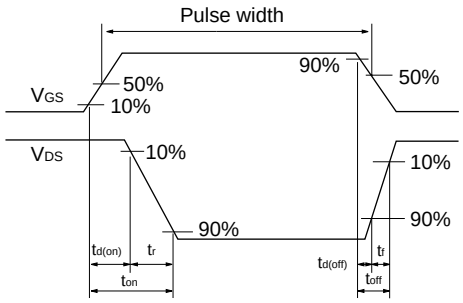


Fig.17 Switching time waveforms

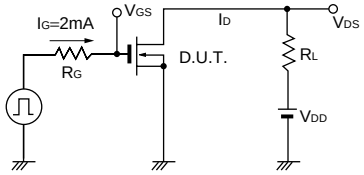


Fig.18 Gate charge measurement circuit

Notes

- No technical content pages of this document may be reproduced in any form or transmitted by any means without prior permission of ROHM CO.,LTD.
- The contents described herein are subject to change without notice. The specifications for the product described in this document are for reference only. Upon actual use, therefore, please request that specifications to be separately delivered.
- Application circuit diagrams and circuit constants contained herein are shown as examples of standard use and operation. Please pay careful attention to the peripheral conditions when designing circuits and deciding upon circuit constants in the set.
- Any data, including, but not limited to application circuit diagrams information, described herein are intended only as illustrations of such devices and not as the specifications for such devices. ROHM CO.,LTD. disclaims any warranty that any use of such devices shall be free from infringement of any third party's intellectual property rights or other proprietary rights, and further, assumes no liability of whatsoever nature in the event of any such infringement, or arising from or connected with or related to the use of such devices.
- Upon the sale of any such devices, other than for buyer's right to use such devices itself, resell or otherwise dispose of the same, no express or implied right or license to practice or commercially exploit any intellectual property rights or other proprietary rights owned or controlled by
- ROHM CO., LTD. is granted to any such buyer.
- Products listed in this document are no antiradiation design.

The products listed in this document are designed to be used with ordinary electronic equipment or devices (such as audio visual equipment, office-automation equipment, communications devices, electrical appliances and electronic toys).
Should you intend to use these products with equipment or devices which require an extremely high level of reliability and the malfunction of with would directly endanger human life (such as medical instruments, transportation equipment, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), please be sure to consult with our sales representative in advance.

About Export Control Order in Japan

Products described herein are the objects of controlled goods in Annex 1 (Item 16) of Export Trade Control Order in Japan.
In case of export from Japan, please confirm if it applies to "objective" criteria or an "informed" (by MITI clause) on the basis of "catch all controls for Non-Proliferation of Weapons of Mass Destruction.