GaAs Dual Gate MES FET

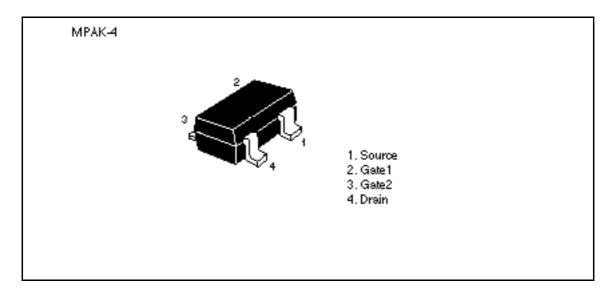
HITACHI

ADE-208-280 1st. Edition

Application

UHF TV tuner RF Amplifier

Outline





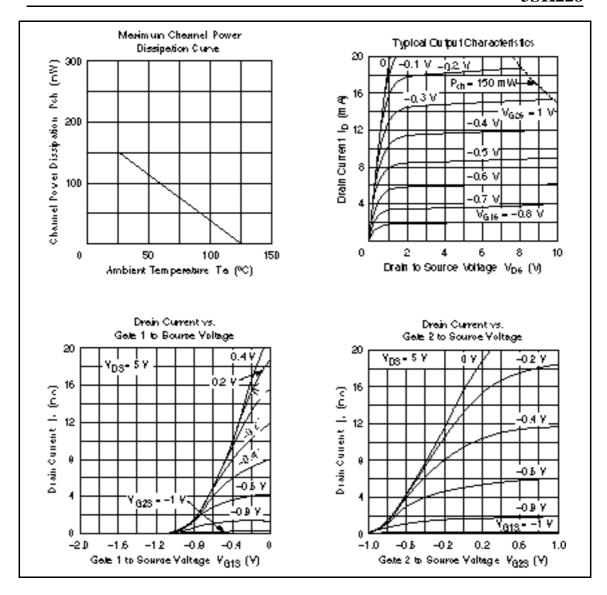
Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

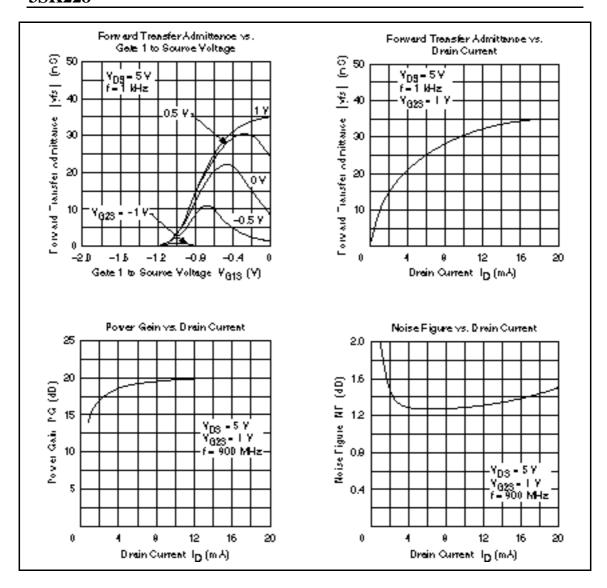
Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DS}	V _{DS} 12	
Gate 1 to source voltage	$V_{\sf G1S}$	-6	V
Gate 2 to source voltage	V_{G2S}	-6	V
Drain current	I _D	50	mA
Channel power dissipation	Pch	150	mW
Channel temperature	Tch	125	°C
Storage temperature	Tstg	-55 to +125	°C

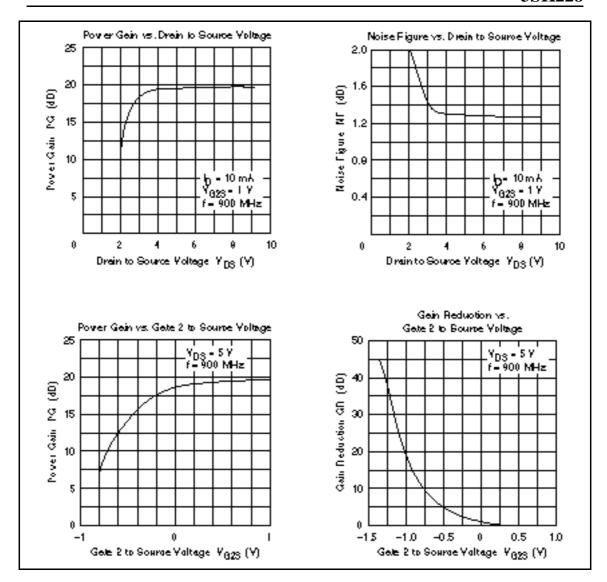
Electrical Characteristics ($Ta = 25^{\circ}C$)

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source cutoff current	I _{DSX}	_	_	50	μΑ	$V_{DS} = 12 \text{ V}, V_{G1S} = -3 \text{ V}, V_{G2S} = 0$
Gate 1 to source breakdown voltage	$V_{(BR)G1SS}$	-6	_	_	V	$I_{G1} = -10 \ \mu A, \ V_{G2S} = V_{DS} = 0$
Gate 2 to source breakdown voltage	$V_{(BR)G2SS}$	-6	_	_	V	$I_{G2} = -10 \ \mu A, \ V_{G1S} = V_{DS} = 0$
Gate 1 cutoff current	I _{G1SS}	_	_	– 5	μΑ	$V_{G1S} = -5 \text{ V}, V_{G2S} = V_{DS} = 0$
Gate 2 cutoff current	$I_{\rm G2SS}$	_	_	– 5	μΑ	$V_{G2S} = -5 \text{ V}, V_{G1S} = V_{DS} = 0$
Drain current	I _{DSS}	10	17	32	mA	$V_{DS} = 5 \text{ V}, V_{G1S} = V_{G2S} = 0$
Gate 1 to source cutoff voltage	$V_{\text{G1S(off)}}$	_	-1.1	-1.5	V	$V_{DS} = 5 \text{ V}, V_{G2S} = 0,$ $I_{D} = 100 \mu\text{A}$
Gate 2 to source cutoff voltage	$V_{\text{G2S(off)}}$	_	-1.1	-1.5	V	$V_{DS} = 5 \text{ V}, V_{G1S} = 0,$ $I_{D} = 100 \mu\text{A}$
Forward transfer admittance	y _{fs}	20	34	_	mS	$V_{DS} = 5 \text{ V}, V_{G2S} = 1 \text{ V},$ $I_{D} = 10 \text{ mA}, f = 1 \text{ kHz}$
Input capacitance	Ciss	_	0.58	1.0	pF	$V_{DS} = 5 \text{ V}, V_{G1S} = V_{G2S} = -3 \text{ V},$ f = 1 MHz
Output capacitance	Coss	_	0.36	0.6	pF	_
Reverse transfer capacitance	Crss		0.028	0.05	pF	
Power gain	PG	17	19.6	_	dB	$V_{DS} = 5 \text{ V}, V_{G2S} = 1 \text{ V},$ $I_D = 10 \text{ mA}, f = 900 \text{ MHz}$
Noise figure	NF	_	1.3	2.0	dB	_

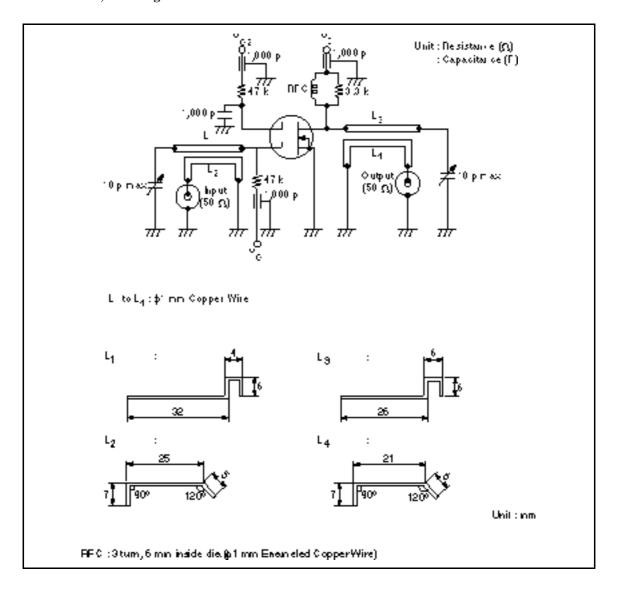
Note: Marking is "XR-".







Power Gain, Noise Figure Test Circuit



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HITACHI

Hitachi, Ltd.
Semiconductor & IC Div.
Neppon Bidg., 2-5-2, Ohte-medil, Chiyode-ku, Tokyo 100, Jepan Tat Tokyo (03, 3270-2111 Fax (03, 3270-5109

For further in formellon write to: Hitechi Americe, Ltd. Semiconductor & IC Div. 2000 Sierre Point Perkwey Briebene, CA. 94005-4885 U.S.A.

Tel: 415-589-8300 Fax: 415-583-4207 Hitechi Burope GmbH
Bedronic Componente Group
Continentel Burope
Dornecher Streife 3
D-85622 Feldkirchen
München
Tet 089-9 94 80-0
Fex: 089-9 29 30 00

Hitachi Burope Ltd.
Bedronic Componenta Div.
Northern Bur ope Headquartera
Whitebrook Ferk
Lower Cook hem Floed
Heidenhead
Barkshire SL68YA
Urited Kingdom
Tet 0628-858000
Fex: 0628-778322

Hitachi Asia Pta, Ltd 45 Collyer Quay \$20-00 Hitachi Tower Snappore 0404 Tet 535-2400 Fex: 535-4533

Hitachi Asia (Hong Kong) Ltd. Unit 705, North Towar, World Finance Centre, Harbour City, Centon Road Taim She Talui, Kowloon Hong Kong Tet 27350218 Fax: 27306074