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April 1st, 2010 Renesas Electronics Corporation

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DATA SHEET



MOS FIELD EFFECT TRANSISTOR

<u>2SK3479</u>

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

The 2SK3479 is N-channel MOS Field Effect Transistor designed for high current switching applications.

FEATURES

- Super low on-state resistance:
- $R_{DS(on)1} = 11 \text{ m}\Omega \text{ MAX.} (V_{GS} = 10 \text{ V}, \text{ ID} = 42 \text{ A})$
- $R_{DS(on)2}$ = 13 m Ω MAX. (Vgs = 4.5 V, ID = 42 A)
- Low Ciss: Ciss = 11000 pF TYP.
- Built-in gate protection diode

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

	•	•	
Drain to Source Voltage (Vcs = 0 V)	VDSS	100	V
Gate to Source Voltage (VDS = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	D(DC)	±83	А
Drain Current (pulse) ^{Note1}	D(pulse)	±332	А
Total Power Dissipation (Tc = 25°C)	P T1	125	W
Total Power Dissipation (T _A = 25°C)	Pt2	1.5	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	–55 to +150	°C
Single Avalanche Current Note2	las	65	А
Single Avalanche Energy ^{Note2}	Eas	422	mJ

Notes 1. PW \leq 10 μ s, Duty cycle \leq 1%

2. Starting T_{ch} = 25°C, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V

ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK3479	TO-220AB
2SK3479-S	TO-262
2SK3479-ZJ	TO-263
2SK3479-Z	TO-220SMD ^{Note}

Note TO-220SMD package is produced only in Japan.

(TO-220AB)



(TO-262)



(TO-263, TO-220SMD)



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ELECTRICAL CHARACTERISTICS (TA = 25°C)

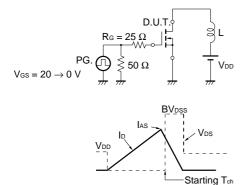
NEC

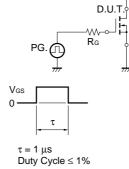
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 100 V, V _{GS} = 0 V			10	μA
Gate Leakage Current	lgss	$V_{GS} = \pm 20 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$			±10	μA
Gate Cut-off Voltage	VGS(off)	Vds = 10 V, Id = 1 mA	1.5		2.5	V
Forward Transfer Admittance	yfs	Vbs = 10 V, Ib = 42 A	37	74		S
Drain to Source On-state Resistance	RDS(on)1	$V_{GS} = 10 \text{ V}, \text{ ID} = 42 \text{ A}$		8.8	11	mΩ
	RDS(on)2	$V_{GS} = 4.5 V, I_D = 42 A$		10	13	mΩ
Input Capacitance	Ciss	V _{DS} = 10 V		11000		pF
Output Capacitance	Coss	V _{GS} = 0 V		1100		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		540		pF
Turn-on Delay Time	td(on)	$V_{DD} = 50 V, I_D = 42 A$		27		ns
Rise Time	tr	Vgs = 10 V		18		ns
Turn-off Delay Time	td(off)	$R_G = 0 \Omega$		140		ns
Fall Time	tr			13		ns
Total Gate Charge	QG	Vdd = 80 V		210		nC
Gate to Source Charge	QGS	Vgs = 10 V		26		nC
Gate to Drain Charge	Qgd	ID = 83 A		60		nC
Body Diode Forward Voltage	VF(S-D)	IF = 83 A, VGS = 0 V		1.0		V
Reverse Recovery Time	trr	IF = 83 A, VGS = 0 V		85		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/µs		280		nC

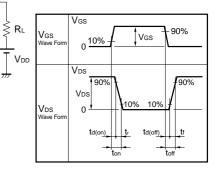
TEST CIRCUIT 1 AVALANCHE CAPABILITY

TEST CIRCUIT 2 SWITCHING TIME

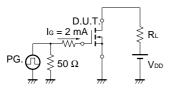
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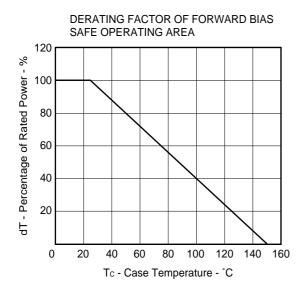




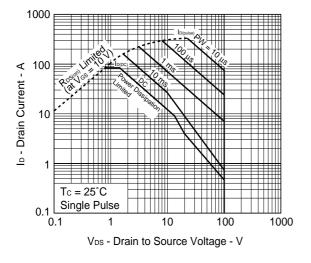
TEST CIRCUIT 3 GATE CHARGE



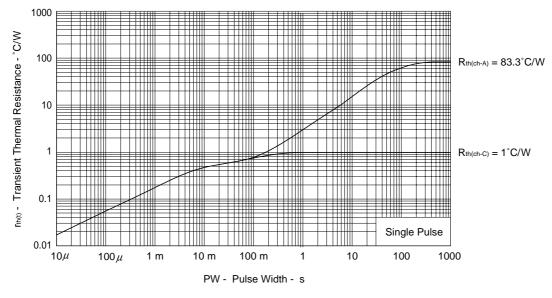
TYPICAL CHARACTERISTICS (TA = 25°C)



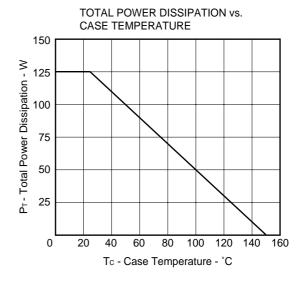
FORWARD BIAS SAFE OPERATING AREA

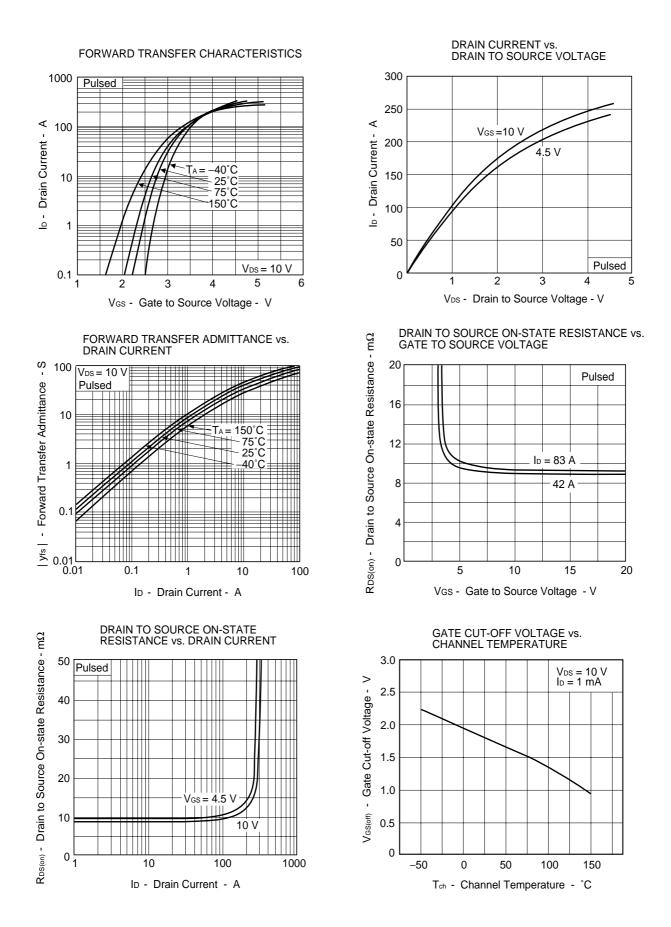


TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

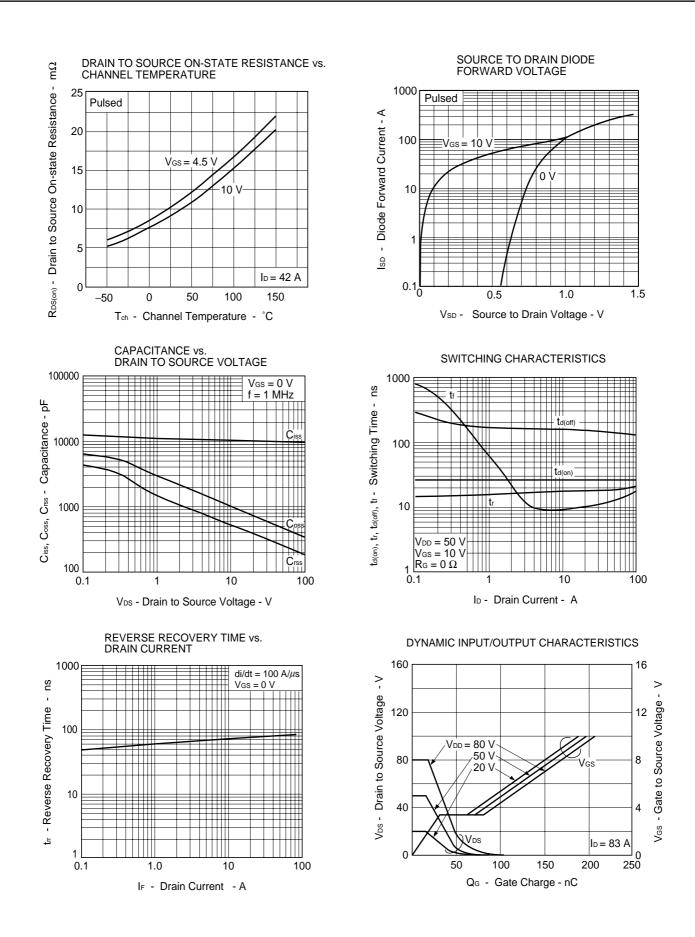


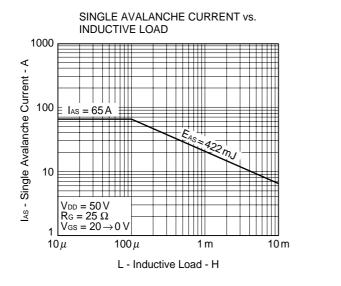
Data Sheet D15077EJ1V0DS

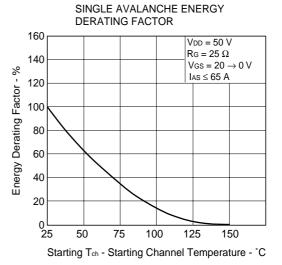




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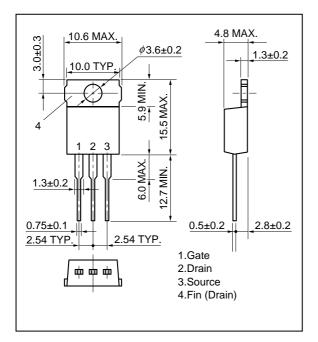


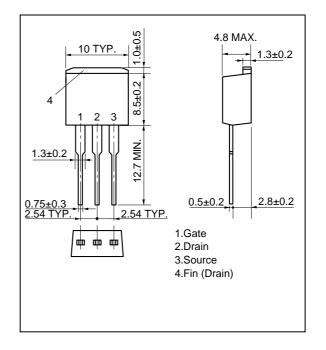




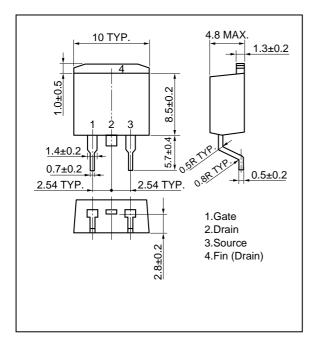
PACKAGE DRAWINGS (Unit: mm)

1) TO-220AB(MP-25)

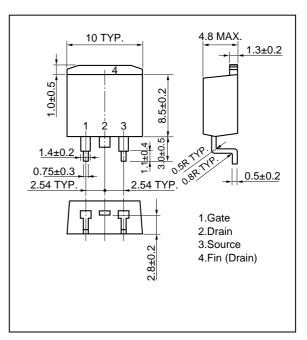




3) TO-263 (MP-25ZJ)



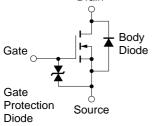
4) TO-220SMD(MP-25Z)^{Note}



Note This package is produced only in Japan.

Drain

EQUIVALENT CIRCUIT



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

Data Sheet D15077EJ1V0DS

2) TO-262(MP-25 Fin Cut)

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