TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

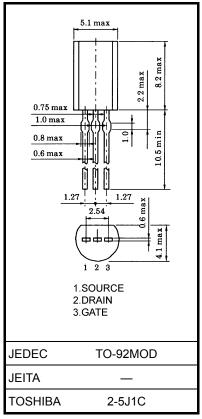
2SK3670

Chopper Regulator and DC-DC Converter Applications

- 2.5V-Gate Drive
- Low drain-source ON-resistance: R_{DS} (ON) = 1.0 Ω (typ.)
- High forward transfer admittance: |Y_{fs}| = 2.1 S (typ.)
- Low leakage current: I_{DSS} = 100 μA (max) (V_{DS} = 150 V)
- Enhancement mode: V_{th} = 0.5 to 1.3 V (V_{DS} = 10 V, I_D =200 μ A)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics			Symbol	Rating	Unit
Drain-source voltage			V _{DSS}	150	V
Drain-gate voltage (R _{GS} = 20 kΩ)			V _{DGR}	150	V
Gate-source voltage			V _{GSS}	±12	V
Drain current	DC	(Note 1)	ID	0.67	
	Pulse (t ≤ 5s) (Note 1)		I _{DP}	1	А
	Pulse	(Note 1)	I _{DP}	3	
Drain power dissipation			PD	0.9	W
Single pulse avalanche energy (Note 2)			E _{AS}	41	m J
Avalanche current			I _{AR}	0.67	А
Repetitive avalanche energy (Note 3)			E _{AR}	0.09	m J
Channel temperature			T _{ch}	150	°C
Storage temperature range			T _{stg}	−55 to 150	°C



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

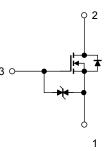
Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to ambient	R _{th (ch−a)}	138	°C / W	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_DS = 50V, T_ch = 25°C(initial), L = 135mH, I_{AR} = 0.67A, R_G = 25\Omega

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.



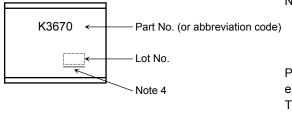
Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition		Тур.	Max	Unit	
Gate leakage cu	ite leakage current		V _{GS} = ±9.6 V, V _{DS} = 0 V			±10	μA	
Drain cut-off cu	rrent	I _{DSS}			100	μA		
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	150 — —			V	
Gate threshold v	voltage	V _{th}	V _{DS} = 10 V, I _D =200 μ A	0.5	— 1.3		V	
Drain-source ON-resistance		R _{DS (ON)}	V _{GS} = 2.5 V, I _D = 0.5 A	_	1.1	2	Ω	
			V _{GS} = 4 V, I _D = 0.5 A —		1.0	1.7	12	
Forward transfer admittance		Y _{fs}	V _{DS} = 10 V, I _D = 0.5 A		2.1	_	S	
Input capacitance		C _{iss}		_	230	_		
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	14	_	pF	
Output capacitance		C _{oss}			50	_		
Switching time	Rise time	tr	$\begin{array}{c} 5 \text{ V} \\ \text{V}_{\text{GS}} \\ 0 \text{ V} \\ \hline \\ 50 \Omega \\ \hline \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$	_	16	_		
	Turn-on time	t _{on}		_	40			
	Fall time	t _f		_	23	_	- ns	
	Turn-off time	^t off		_	95	_		
Total gate charge (gate-source plus gate-drain)		Qg		_	4.6	_		
Gate-source charge		Q _{gs}	V _{DD} ≈ 120 V, V _{GS} = 5 V, I _D = 1 A		2.9		nC	
Gate-drain ("miller") Charge		Q _{gd}			1.7			

Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	0.67	А
Pulse drain reverse current (t=5s) (Note 1)	I _{DRP}	_	_	_	1	А
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	3	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 0.5 A, V _{GS} = 0 V	_	_	-1.5	V
Reverse recovery time	t _{rr}	I _{DR} = 1A, V _{GS} = 0V	_	95	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} / dt = 50A / µs		110	_	nC

Marking



Note 4: A line under a Lot No. identifies the indication of product Labels.

[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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