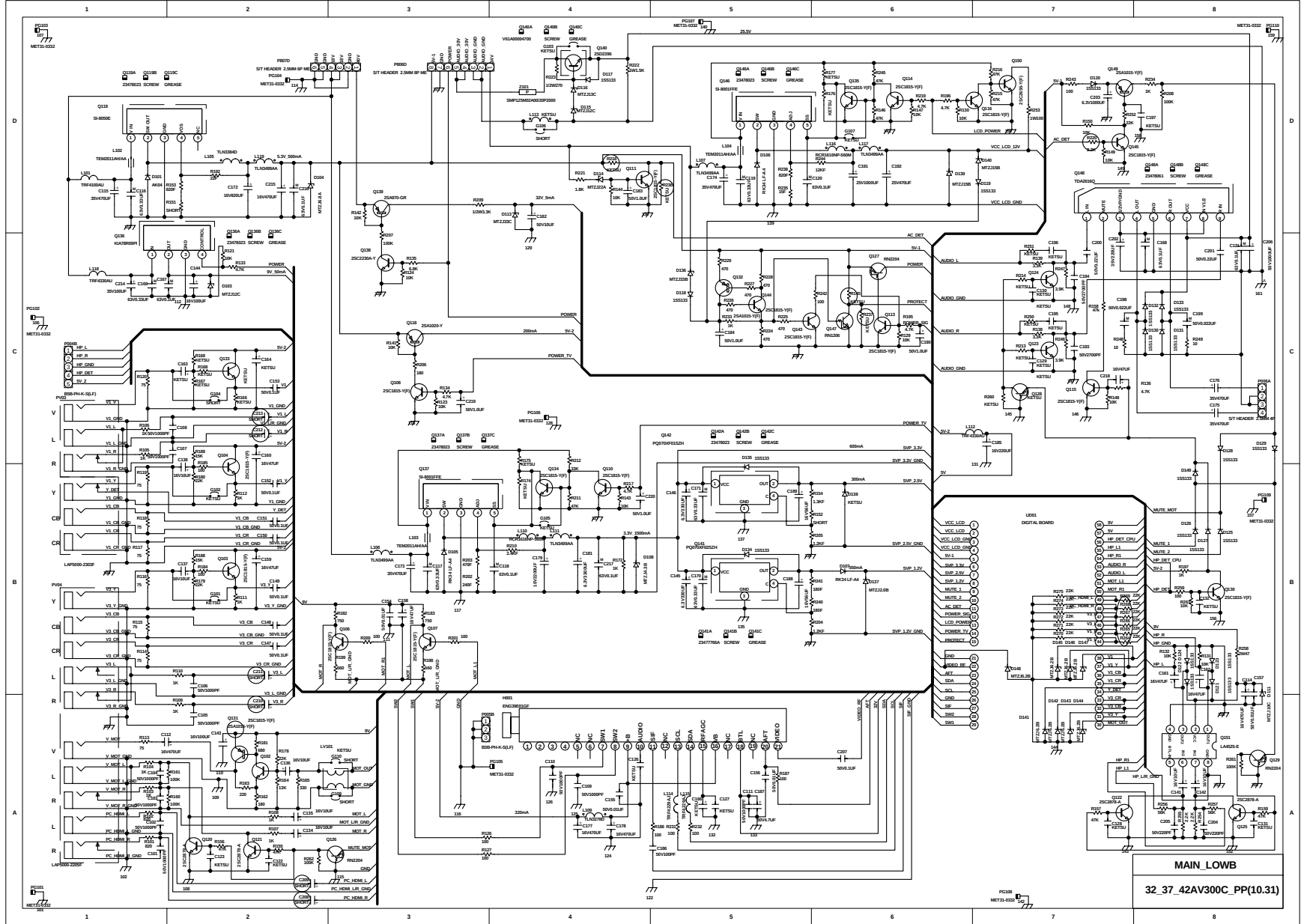
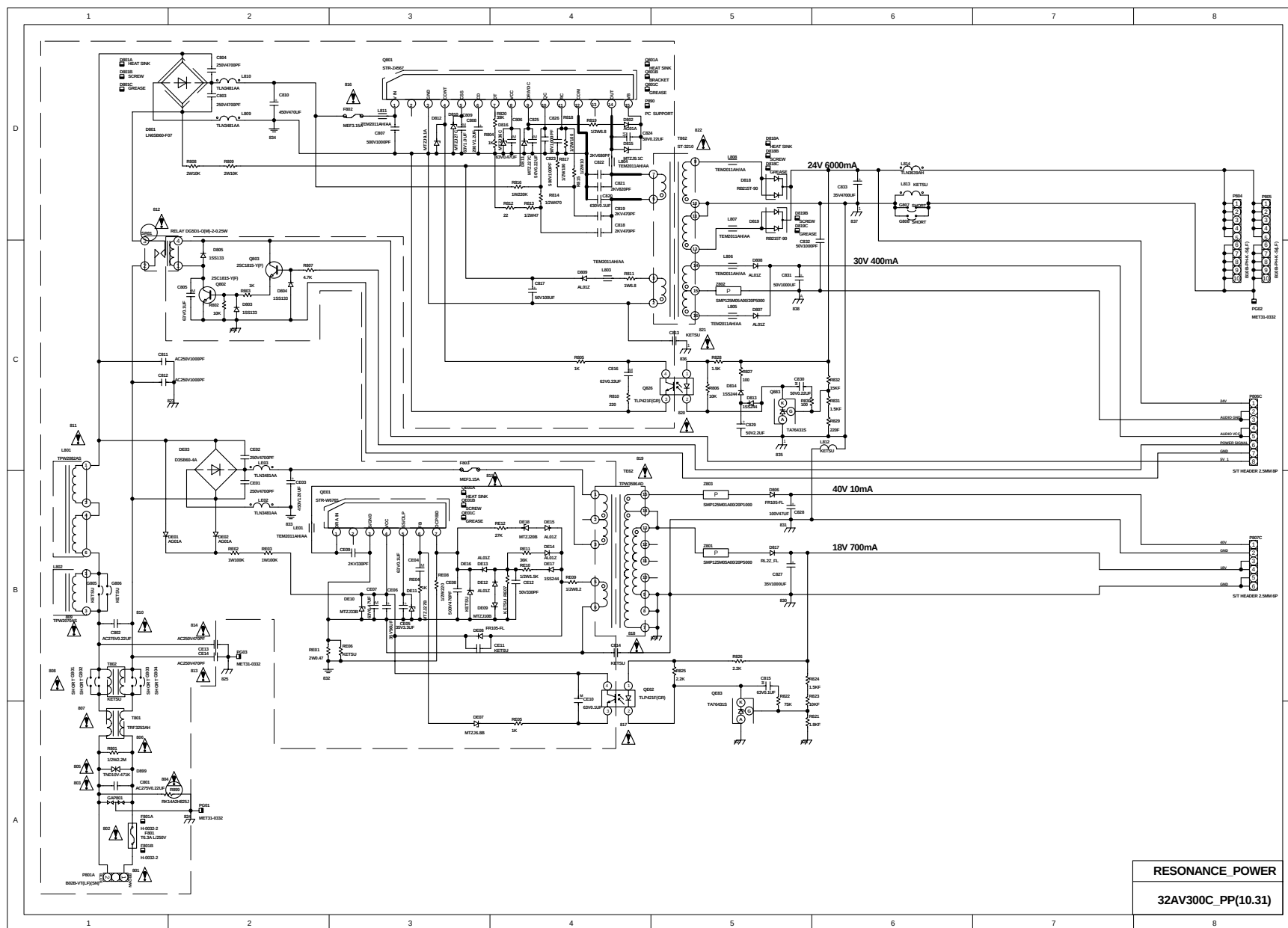
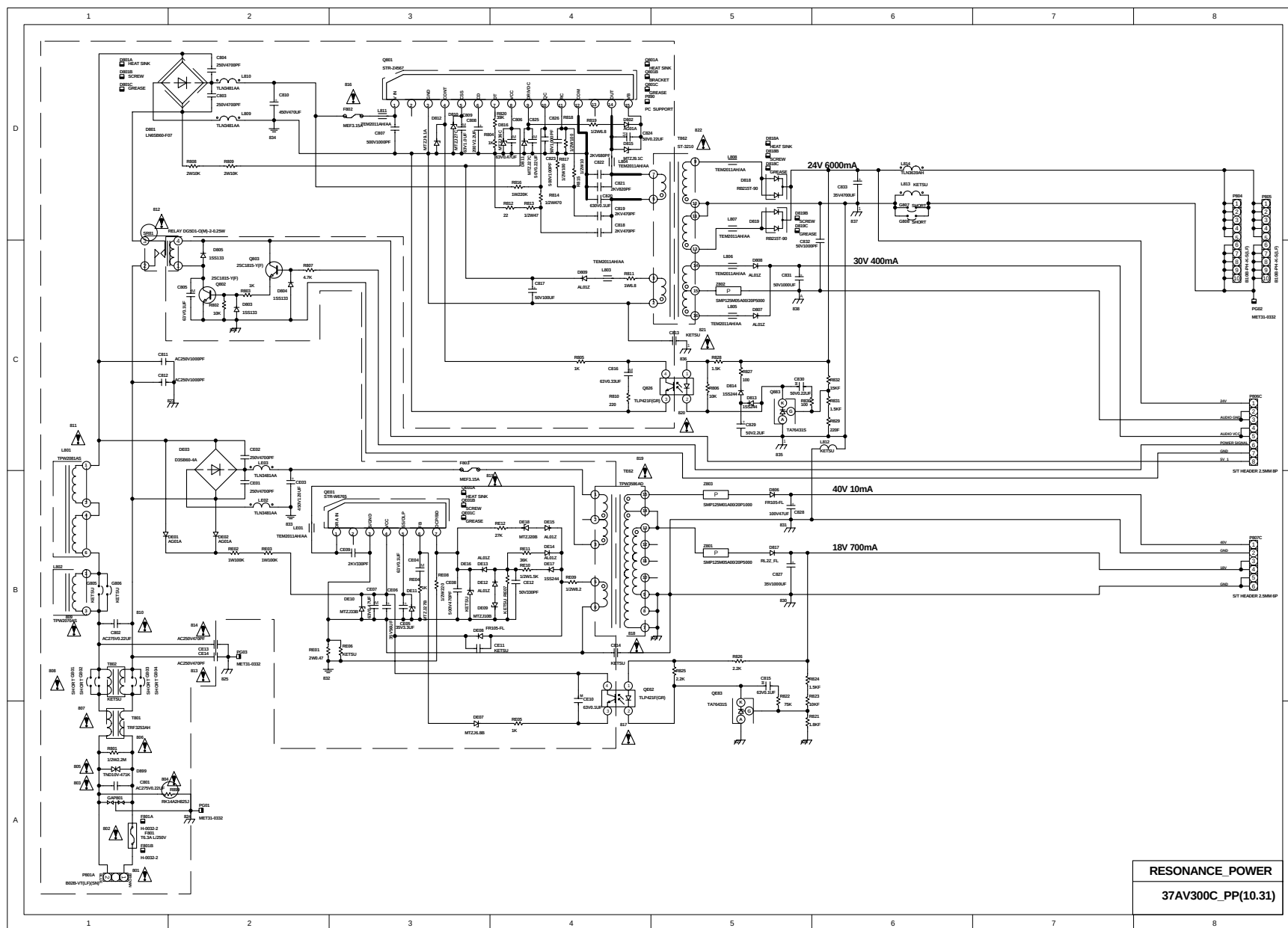
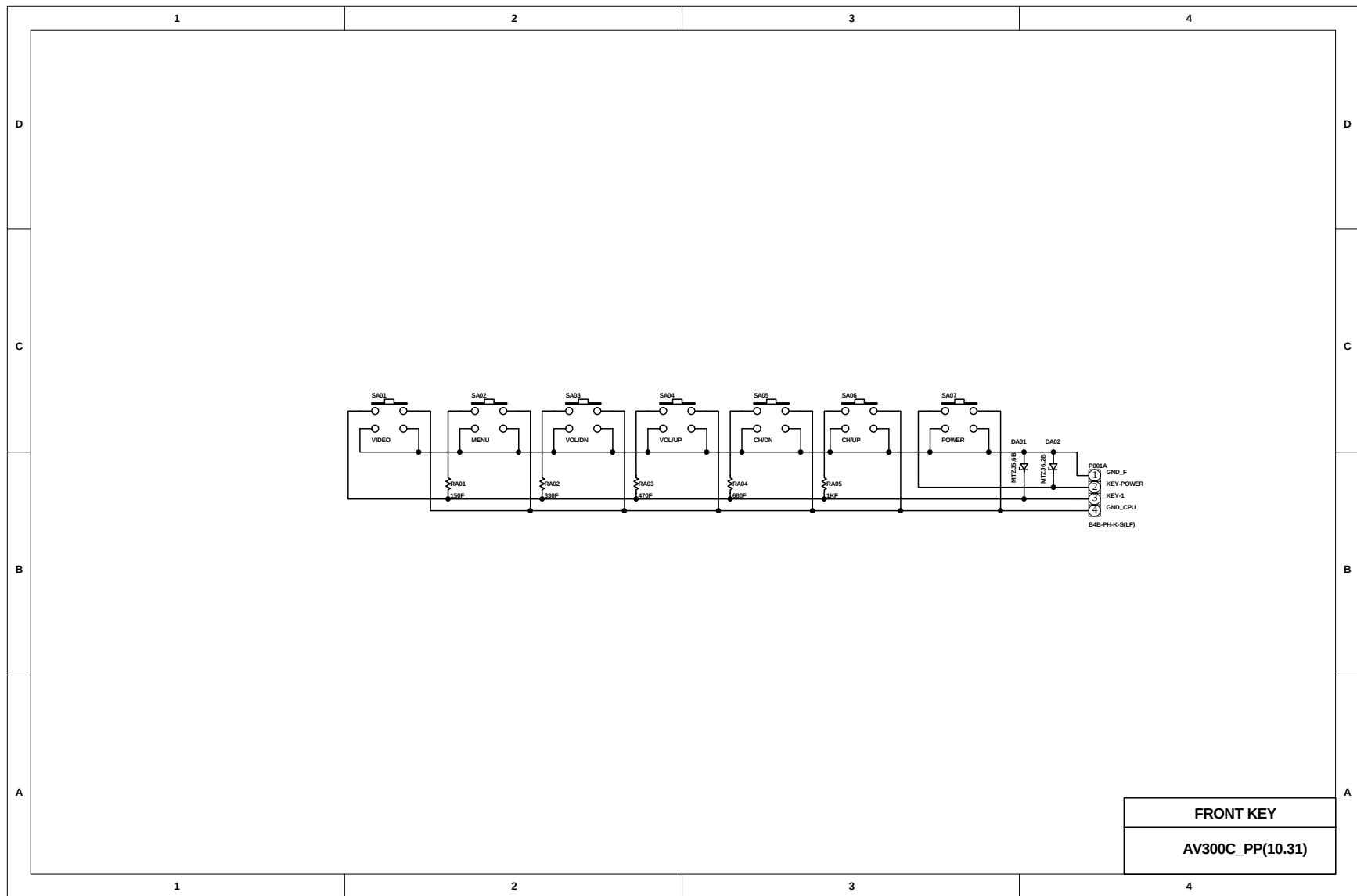
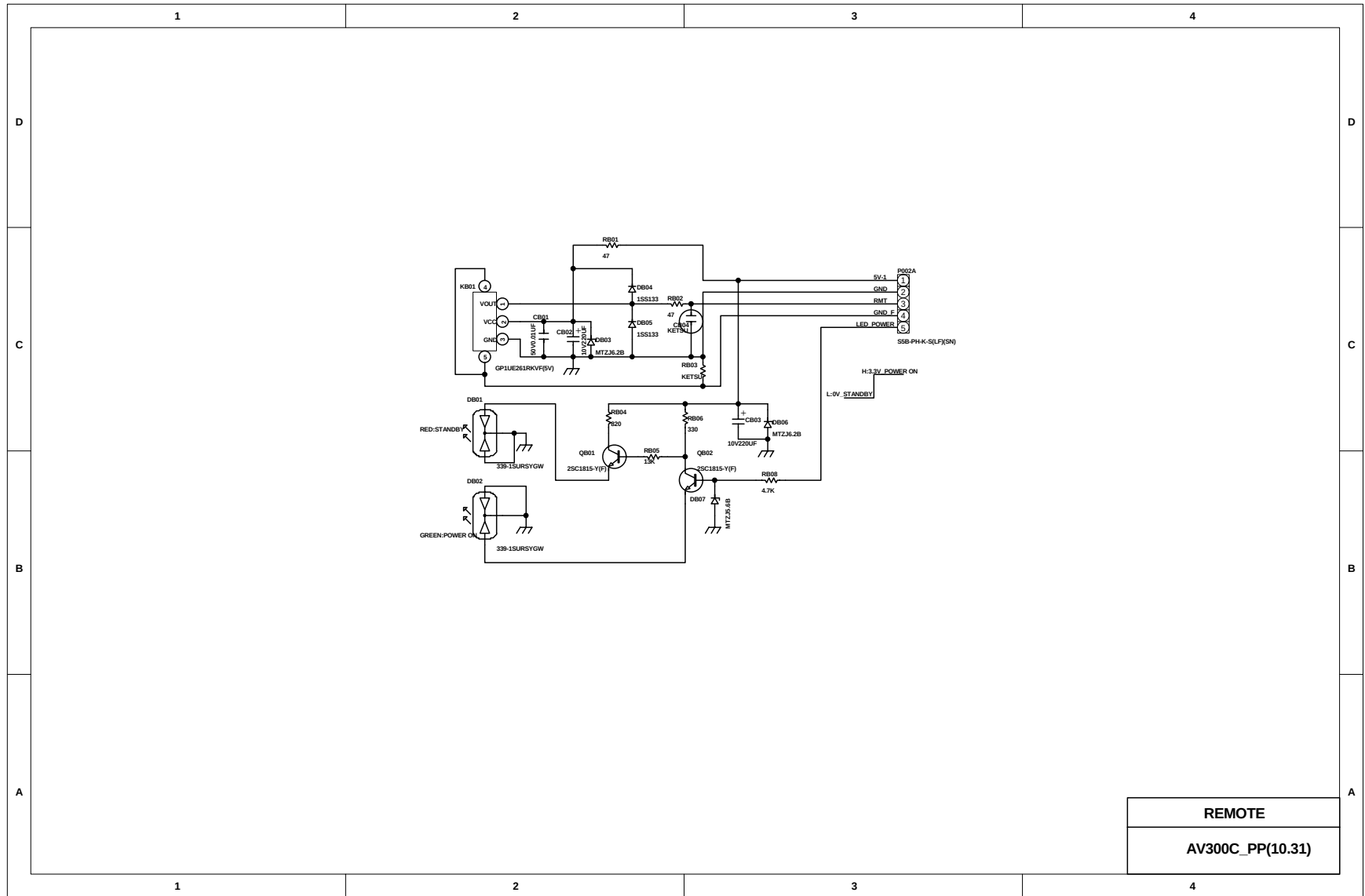


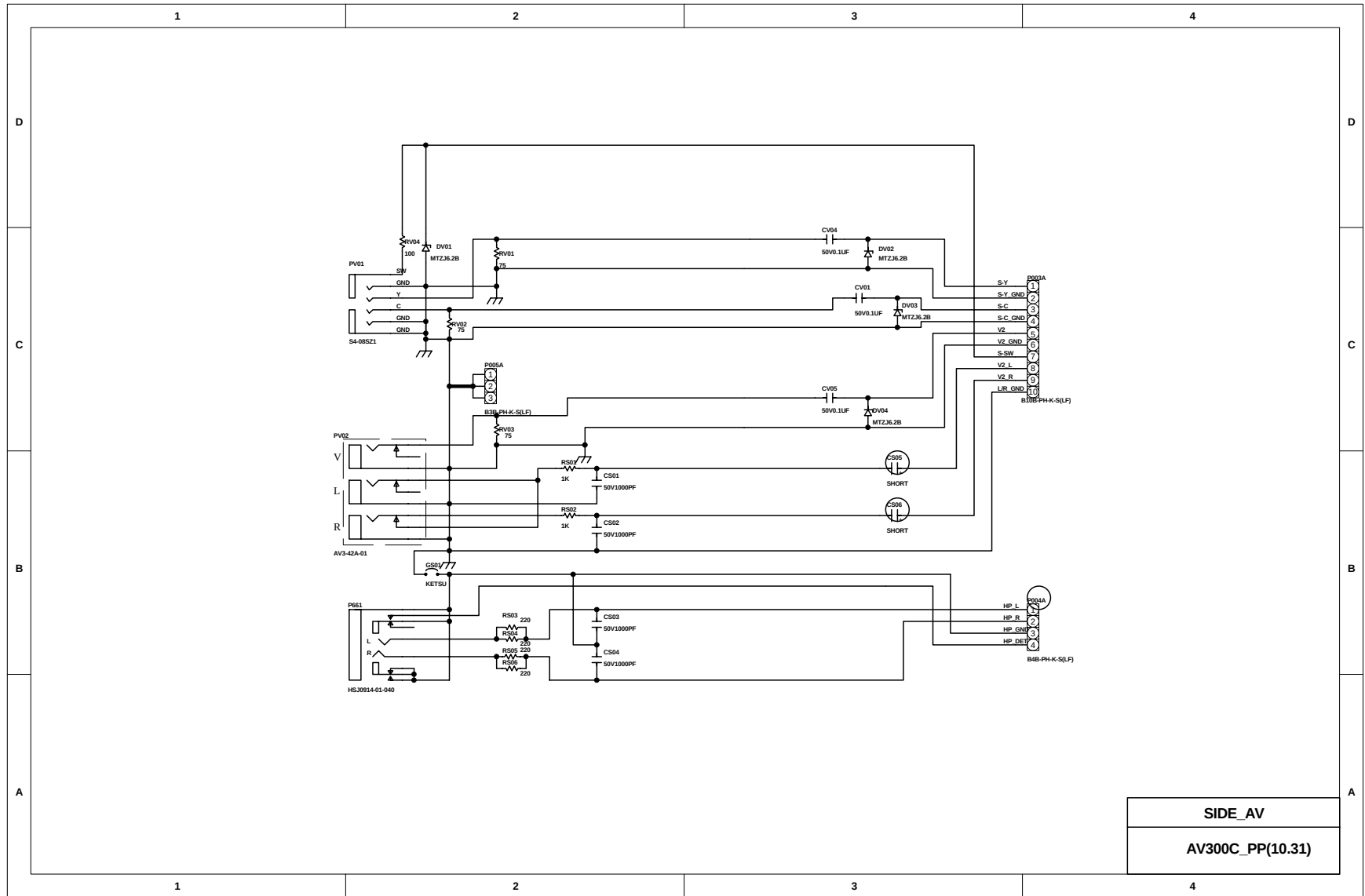




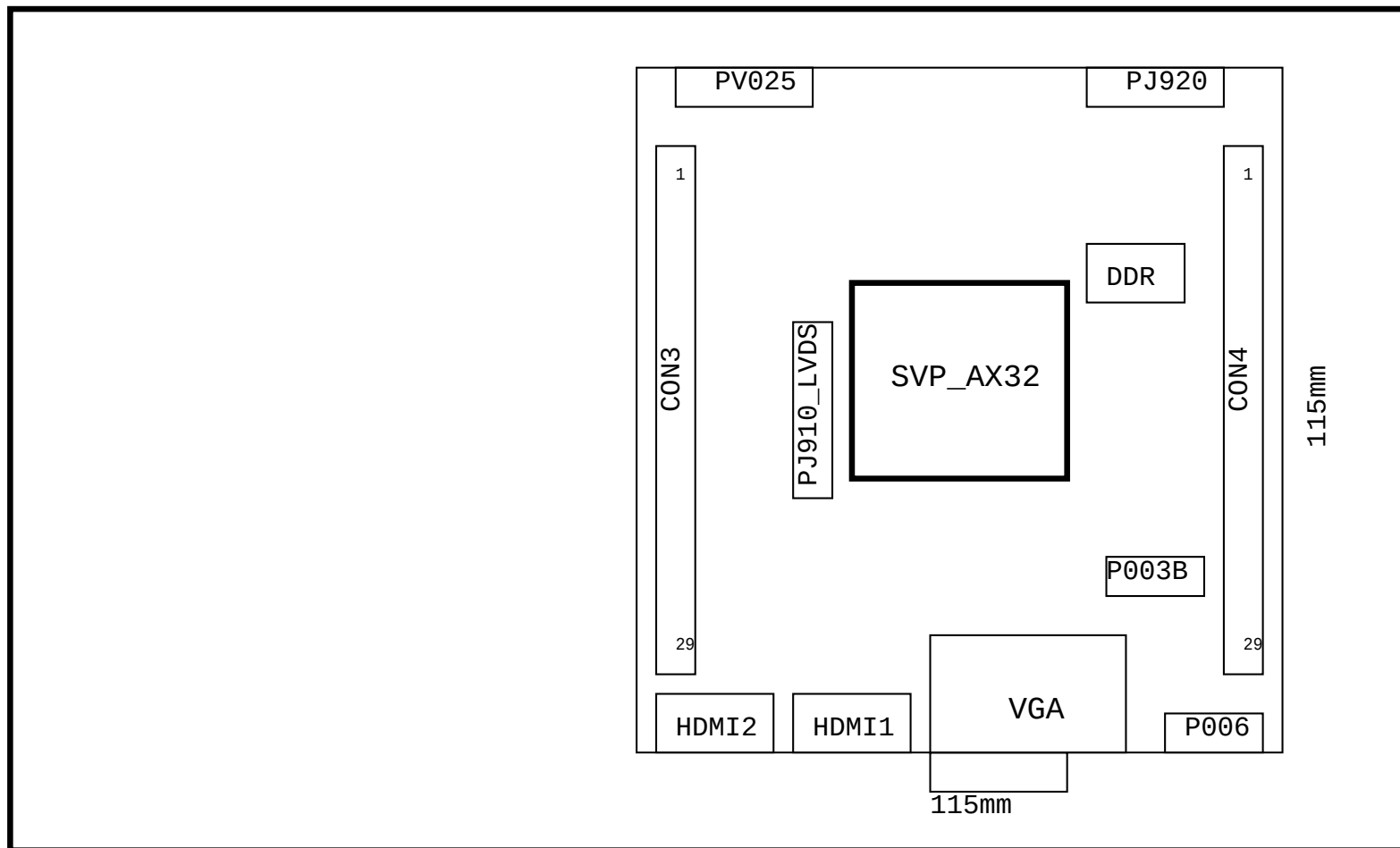




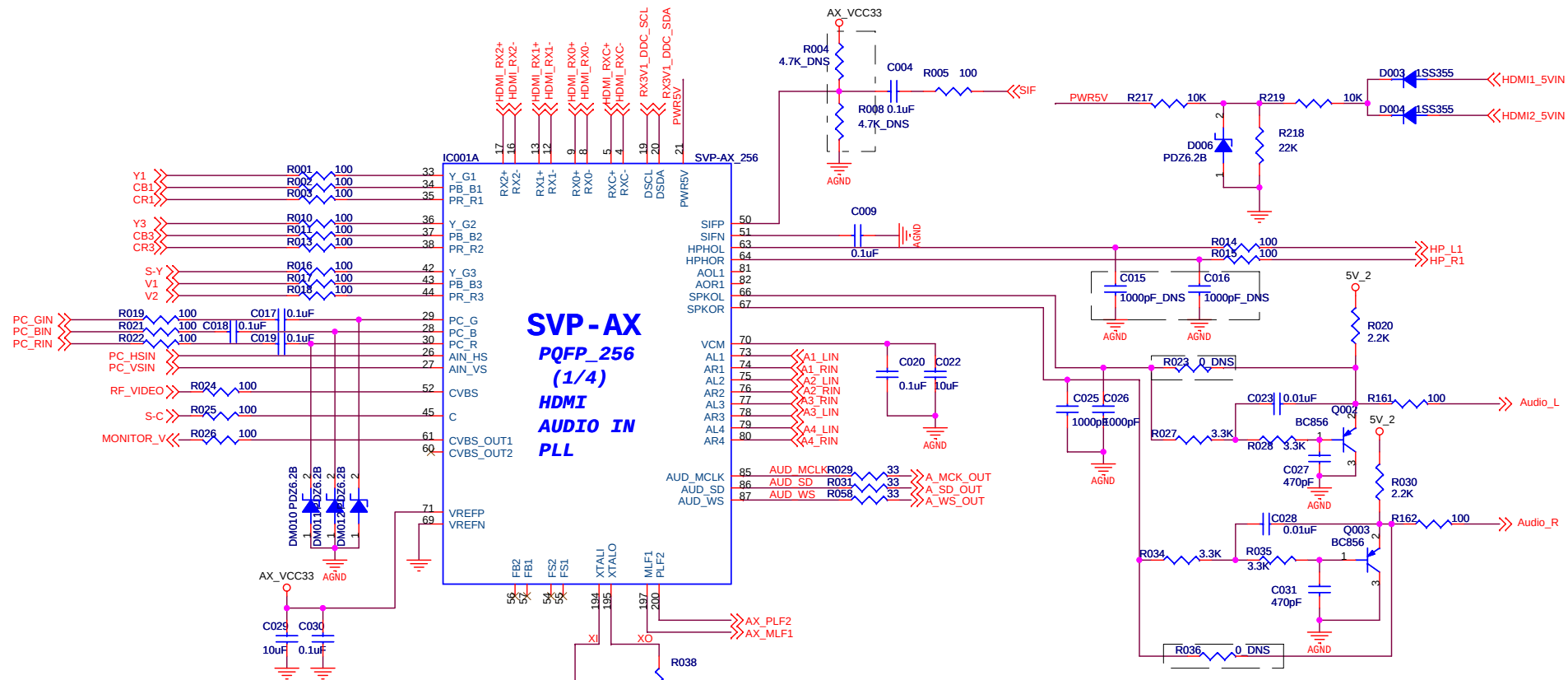




LCD312 project placement



		Trident Digital Media, Inc. 3408 Garrett Drive Santa Clara ,CA 95054-2803 http://www.tridentmicro.com	
		Title SVP-AX_ADC/HDMI/AUDIO/PLL	
Size A	Schematic Name LCD312		Rev C
Date: Tuesday, October 30, 2007		Sheet 1 of 13	



LAYOUT: Place xtal circuit as compact and close to chip as possible

Pin Name	Pin No.	DEFINITION IN LCD301
CVBS1	52	MAIN TUNER INPUT
Y_G3	42	SVIDEO Y INPUT
PB_B3	43	CVBS1 INPUT
PR_R3	44	CVBS2 INPUT

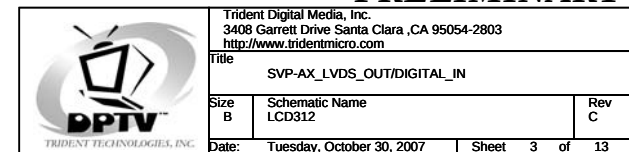
PRELIMINARY

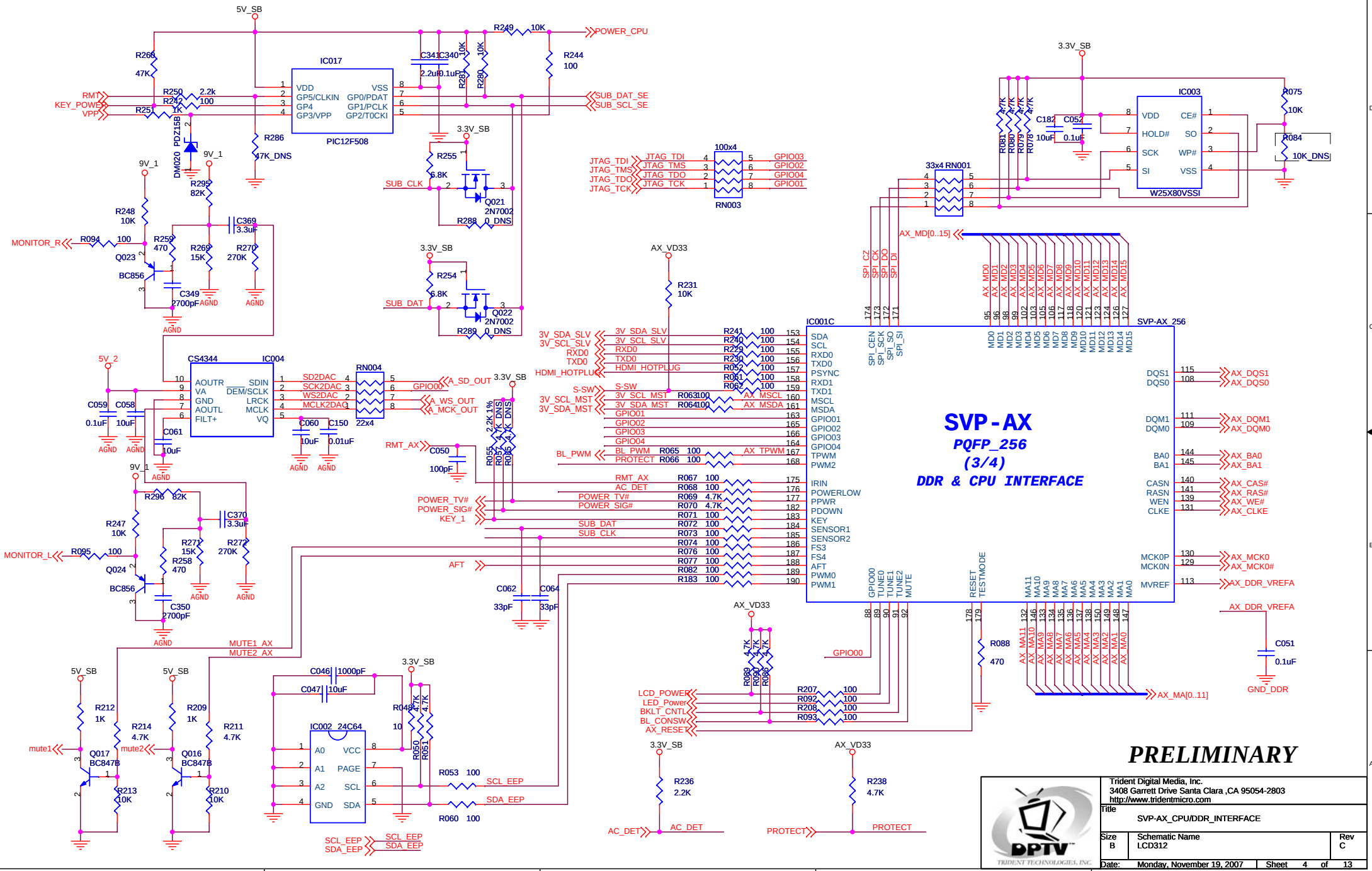
Trident Digital Media, Inc.
3408 Garrett Drive Santa Clara, CA 95054-2803
<http://www.tridentmicro.com>

Title: SVP-AX_ADC/HDMI/AUDIO/PLL

Size B: Schematic Name LCD312 Rev C

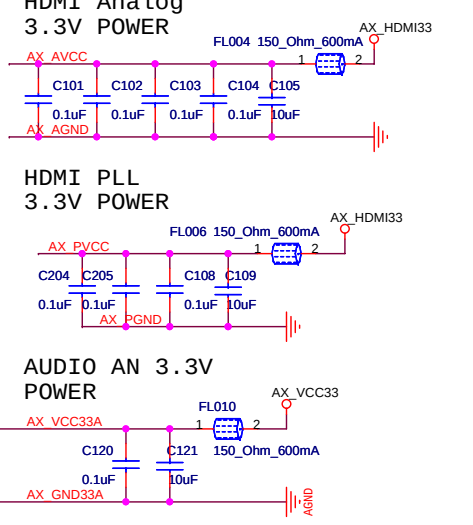
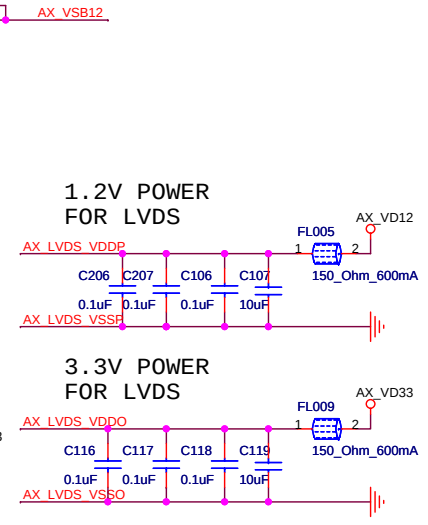
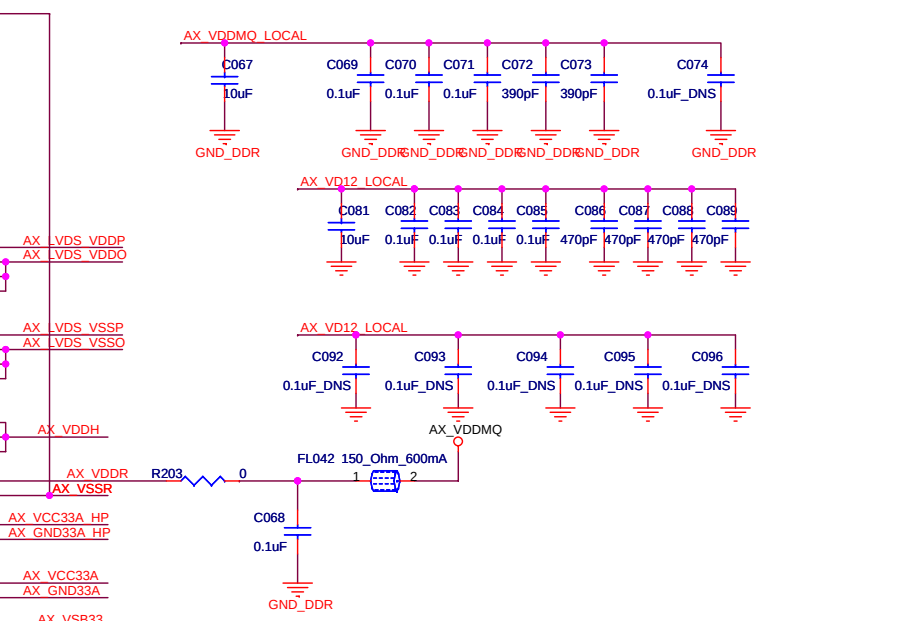
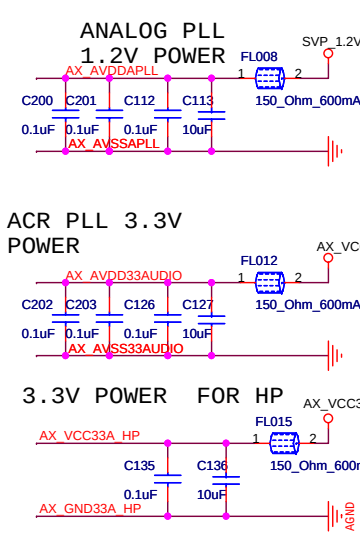
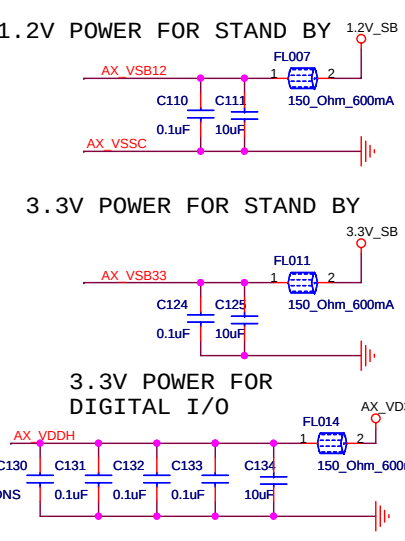
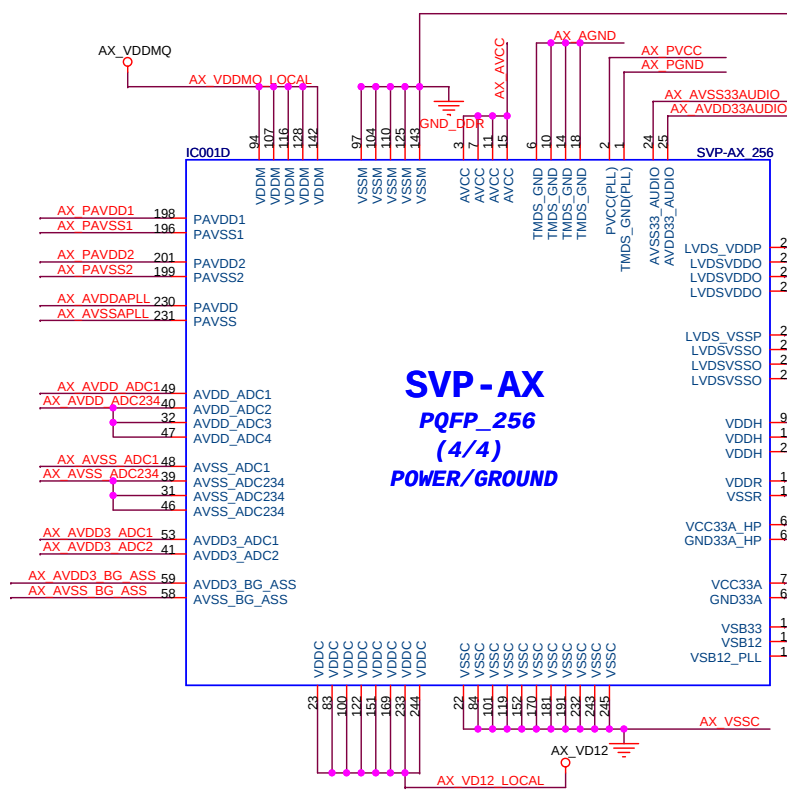
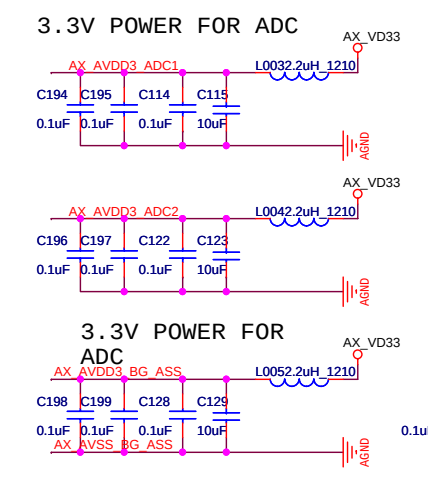
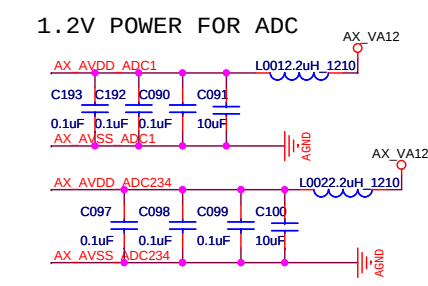
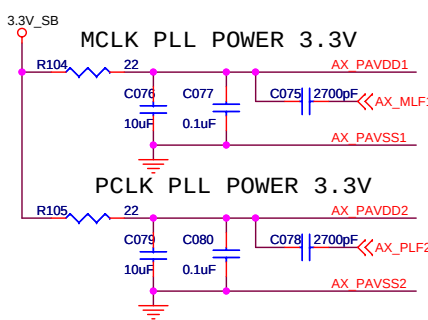
Date: Tuesday, October 30, 2007 Sheet 2 of 13





PRELIMINARY

 TRIDENT TECHNOLOGIES, INC.		Trident Digital Media, Inc. 3408 Garrett Drive Santa Clara, CA 95054-2803 http://www.tridentmicro.com			
		Title SVP-AX_CPU/DDR_INTERFACE			
Size B	Schematic Name LCD312			Rev C	
	Date: Monday, November 19, 2007				
		Sheet	4	of	13



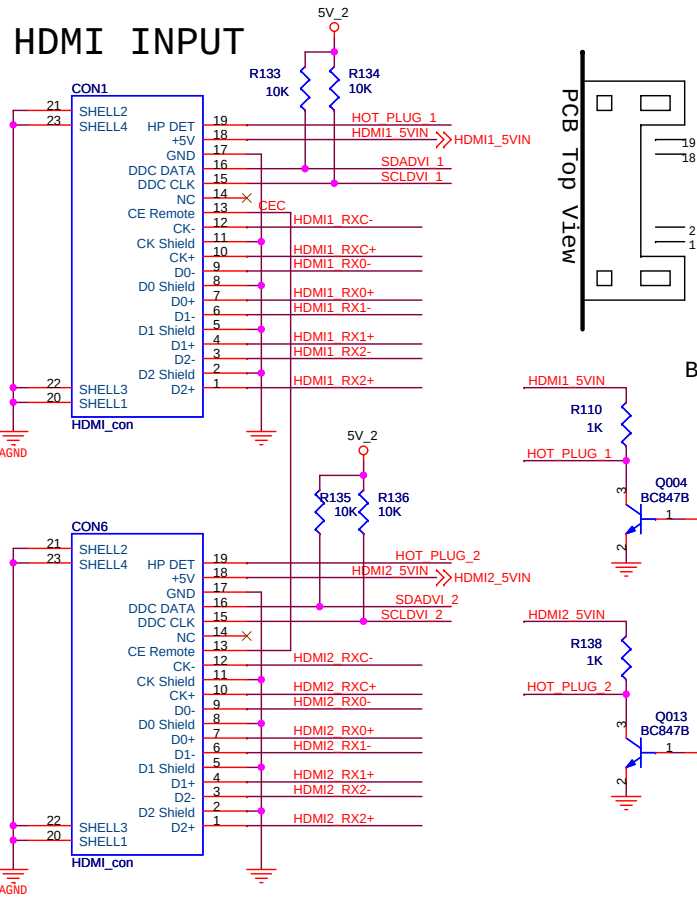
TRIDENT DIGITAL MEDIA, INC.

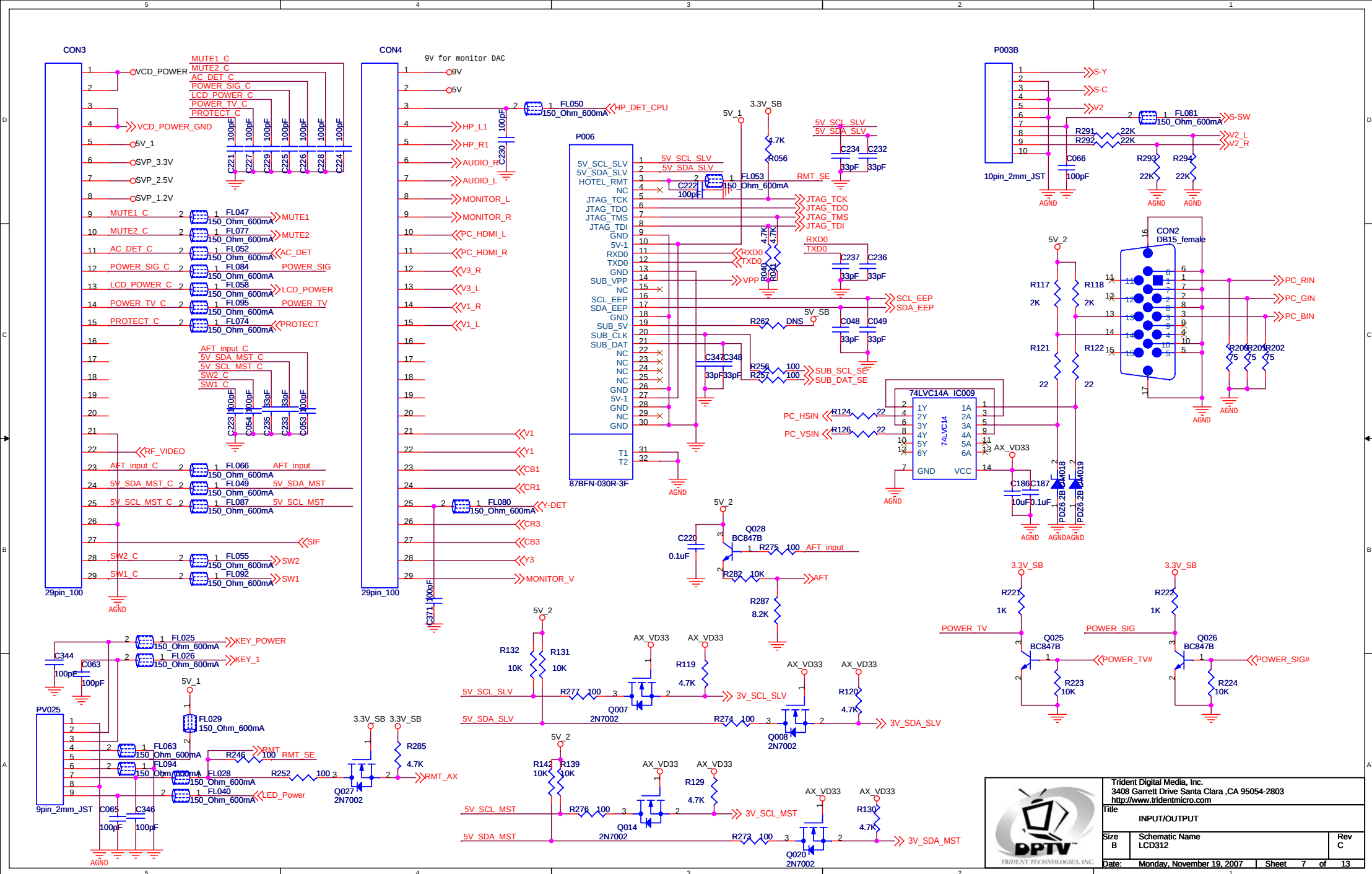
3408 Garrett Drive Santa Clara, CA 95054-2803
http://www.tridentmicro.com

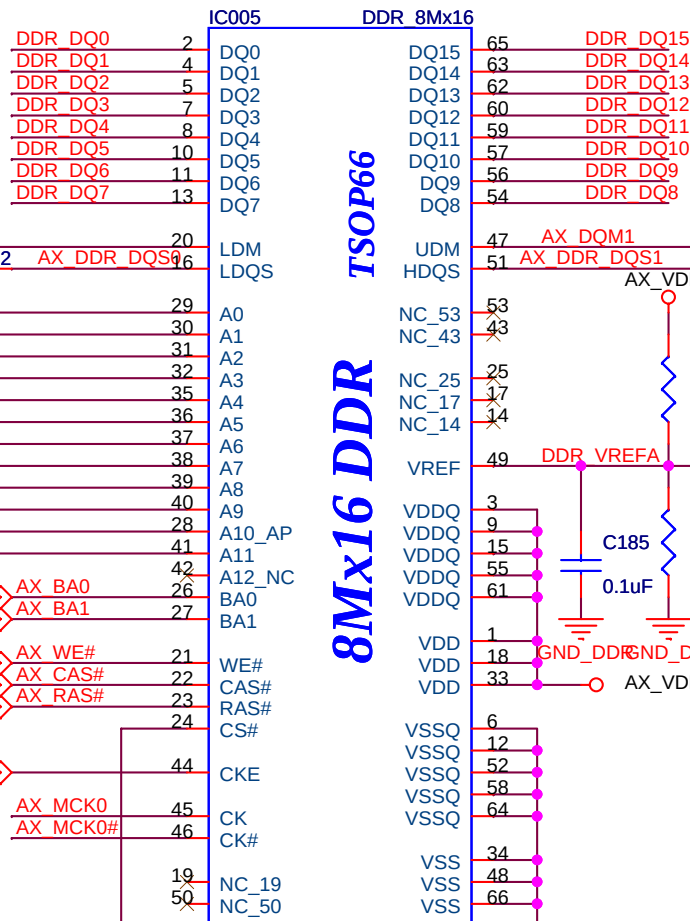
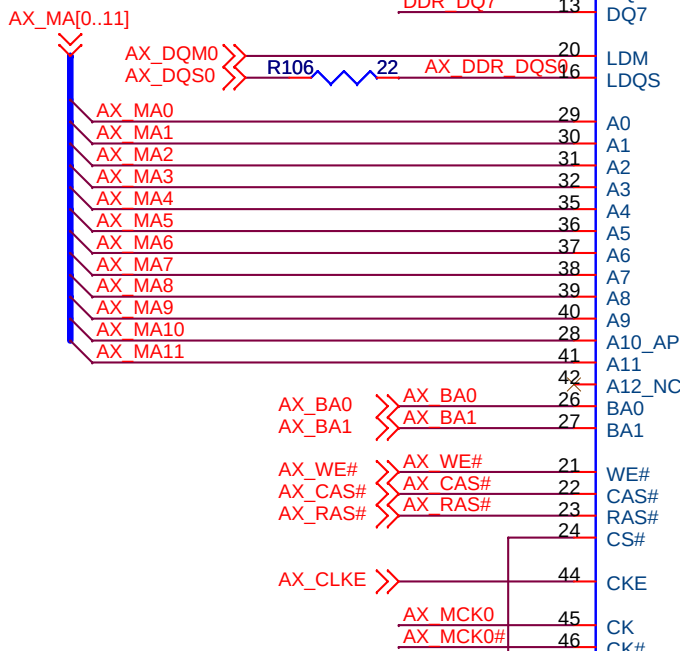
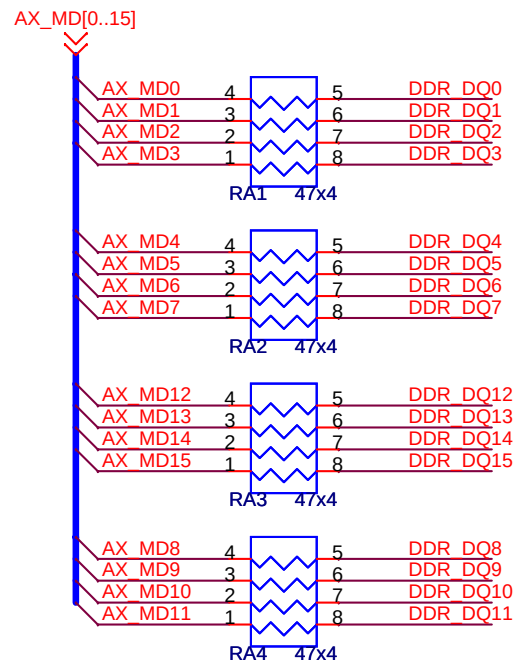
TRIDENT TECHNOLOGIES, INC.

Title: SVP-AX POWER/GROUND		
Size: B	Schematic Name: LCD312	Rev: C
Date: Tuesday, October 30, 2007	Sheet: 5	of: 13

HDMI INPUT

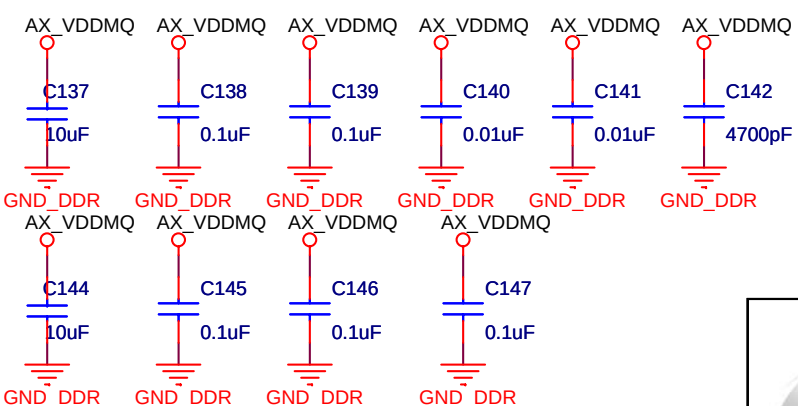
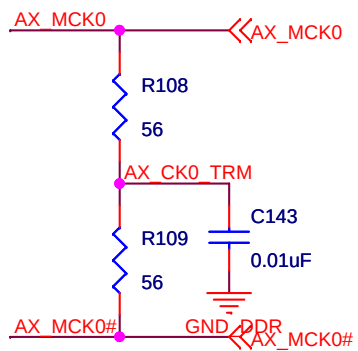




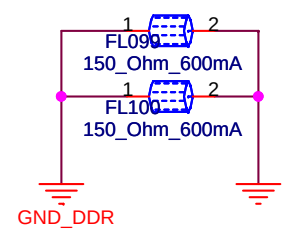


8Mx16 DDR

TSOP66



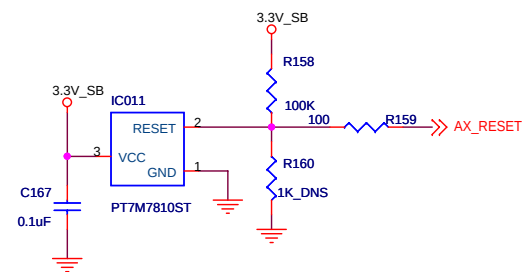
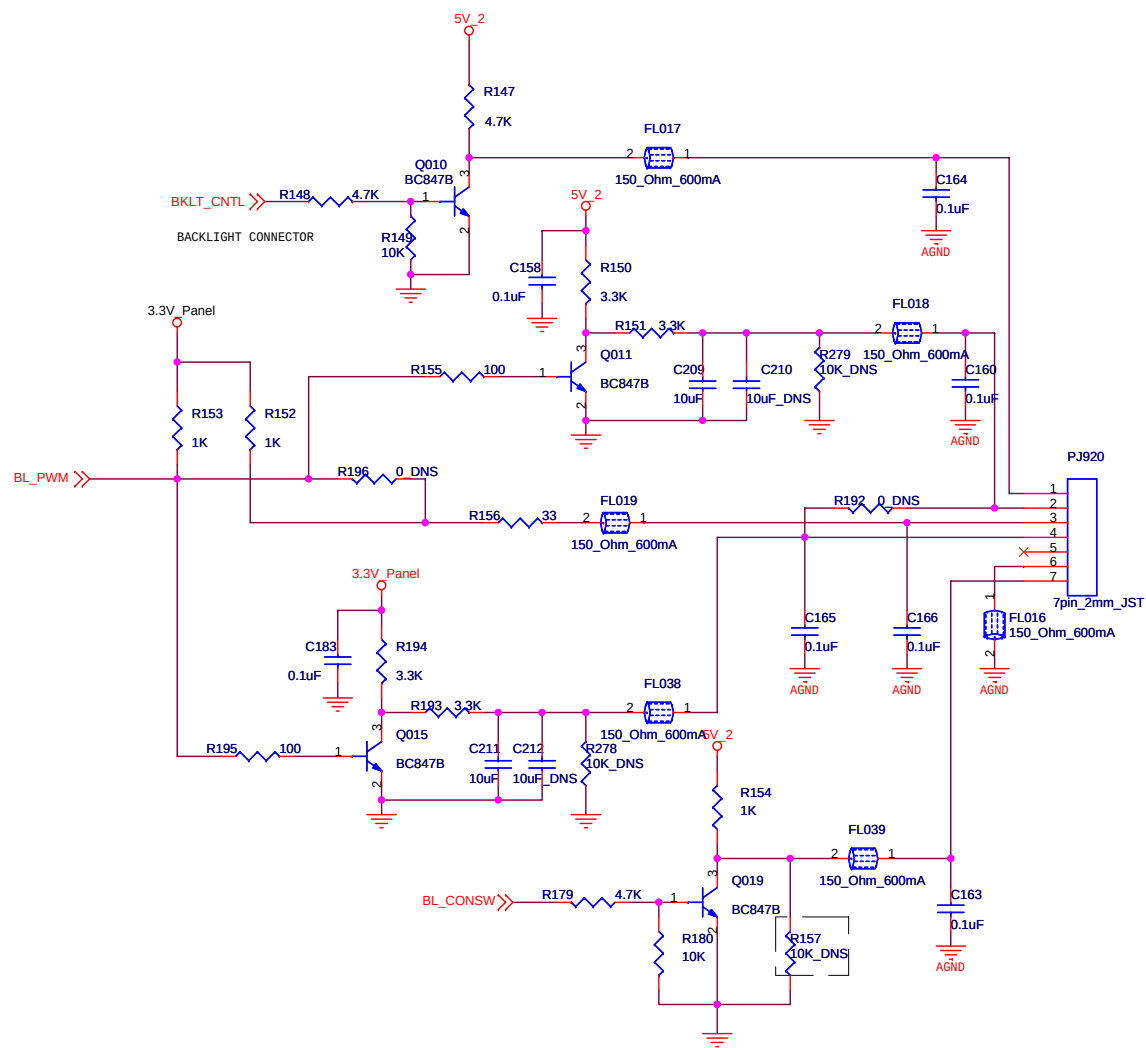
DDR DECOUPLING CAPACITORS



PRELIMINARY

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Title 8Mx16_DDR		
Size A	Schematic Name LCD312	Rev C
Date:	Tuesday, October 30, 2007	Sheet 8 of 13



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Title
BUTTON, RESET, BRT/PWR_CONTROL, I2C

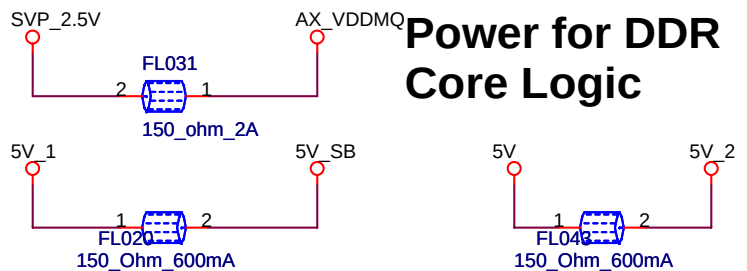
Size
B

Schematic Name
LCD312

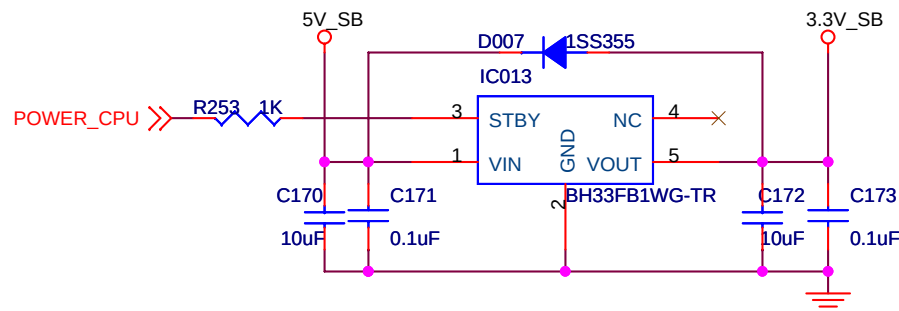
Rev
C

Date: Tuesday, October 30, 2007

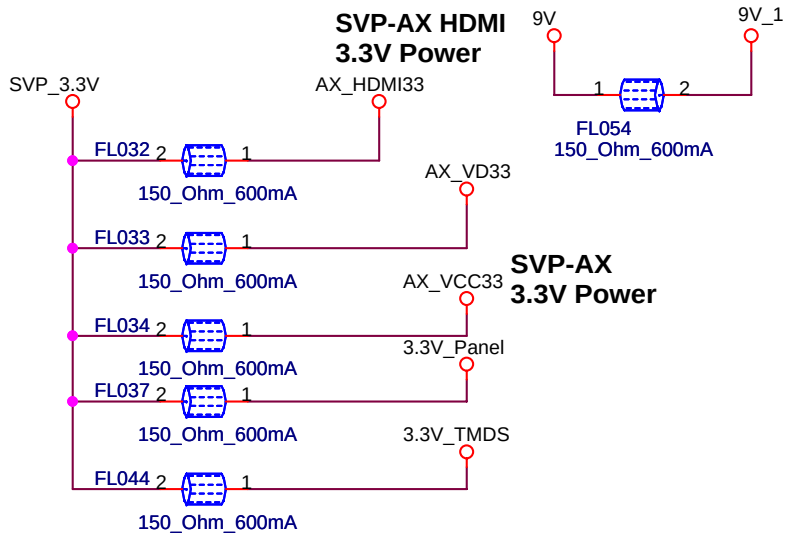
Sheet 9 of 13



Power for DDR Core Logic

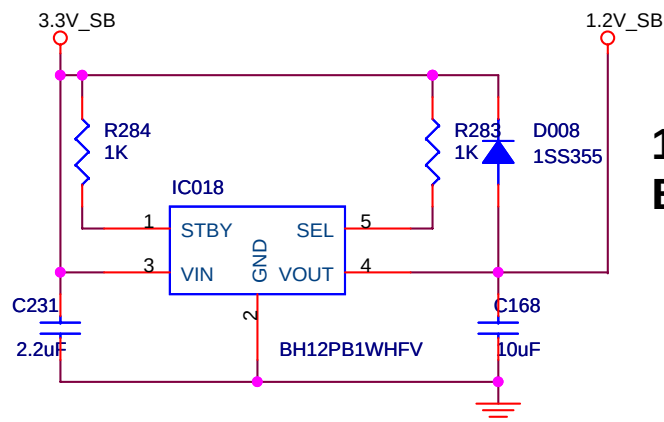


3.3V Stand By Power

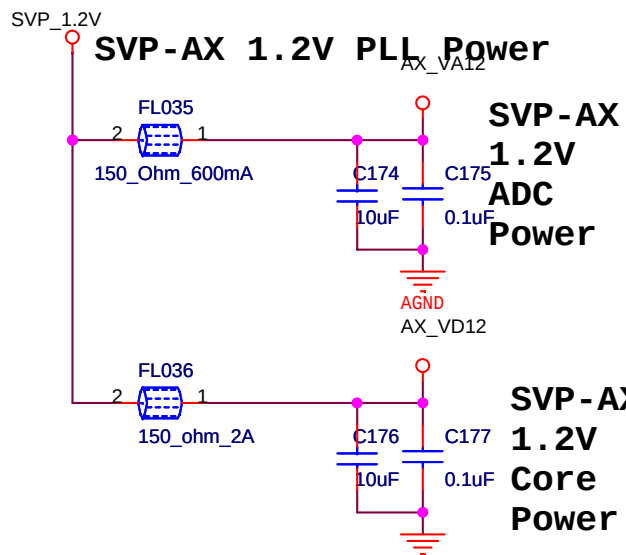


SVP-AX HDMI 3.3V Power

SVP-AX 3.3V Power



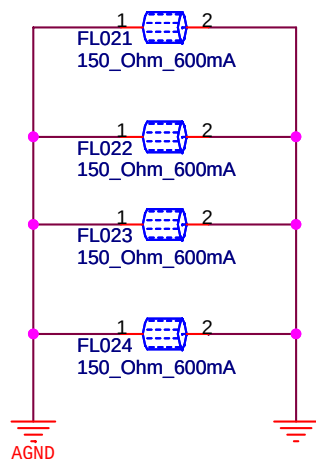
1.2V Stand By Power



SVP-AX 1.2V PLL Power

SVP-AX 1.2V ADC Power

SVP-AX 1.2V Core Power



Trident Digital Media, Inc. 3408 Garrett Drive Santa Clara ,CA 95054-2803 http://www.tridentmicro.com		
Title POWER : LDO REGULATORS		
Size A	Schematic Name LCD312	Rev C
Date: Tuesday, October 30, 2007	Sheet 10	of 13

PRELIMINARY

REVISION HISTORY OF LCD312 REFERENCE BOARD

REV	DATE	DESCRIPTION	SIGNATURE
A-00	03-16-2007	1.page 5,Separate AX VDDR pin from VDDM power and add a 33ohm resistor between VDDR and VDDM to changed VDDR pin voltage to about 2.2V. modify C68 to 1000pF,C188 modify to 1uF. Modify FB54 to R203 33ohm 2, page 10 Separate the Power GND to DGND and AGND of AX 3, page 8, modify PC in connector GND to AGND all.	TaoJie
	03-21-2007	1, page 8, add I2C debug connector P004. 2, page 8, add I2C in VGA put in order to soft upgrade, perhaps RX and TX are can't.add R204,R205	TaoJie
	03-22-2007	1, because the PCB size is too small,modify connector 2.54mm to 2.0mm. 2, change the VGA connector footprint to thinnest 3, change the PCB board to large to 90X100mm 4, swap some pin from AX and DDR about RA3 and RA4 5, change the LVDS connector to 1mm span as customer require.	TaoJie
	03-23-2007	1,page 4, move BKL_T_CNTL net from U1 pin167-->pin 189 2,page 3, changed the U1 pin167 net name to BL_PWM(backlight PWM)to control LCD brightness 3,page 11, changed the PWM net name to BL_PWM to control BRT_CNTL	TaoJie
	03-26-2007	1,page 8, modify CON3 pin24,25 I2C_SLV to I2C_MST to control Tuner, add Q9,Q12 2, P001 connector add to 4pin and modify its footprint 3, modify P003A to P003B 4, page 8,add 10K pull up with 5V_1 to I2C_SLV debug connector P004 5, page 4, move prectect net from AX pin164 to 157	TaoJie
	03-27-2007	1, page 2, modify C15,C16 to option,R12 to 压敏电阻。 2, page 4, del PWM1 net 3, page 5, modify AV_VDDMQ net, move R203 to pin114 directly,del C188, modify C68 to 0.1uF, add FB42 4, page 5, modify all AGND to DGND in order to layout good. 5, page 8, modify C156 to 100pF 6, page 10, modify U12 footprint to small SOT_89.	TaoJie
	03-28-2007	1, page 4, swap RN1 2, modify CON2 footprint	TaoJie
	03-29-2007	1, page4, modify CPU part port based on LCD311.	TaoJie
	03-30-2007	1, page 2, modify R12 to 10K, add DM21 6.2V。 2, page 6, modify DDR part components to small 0402: RA1~RA4, R106~R109,C139~C143,C145~C147 3, page 8, modify VGA 11/4 pin I2C net to 3.3V I2C. add DM21,22 to protect the IC. 4, modify DGND and AGND to old sch. 5, modify all bottom R/C/FB components footprint as customer's require. 6, page 6, RA3,RA4 pin swap.	TaoJie

B-00	04-13-2007	1,page 10,modify U11 value to PT7M7810ST	
	06-13-2007	1.Page2, del R33 2.Page2, del R215 Q18, net MONITOR_V connect to R26 3.del net S2, modify R125 form 100 to 4.7k ,connect R125 pin1 to net 3.3V_TMDS 4.Page2 Page3 disconnect R206 pin2 and AX pin54 then connect to AX pin248 5.Page2 Page3 swap AX pin158/249/250/251 6.Page4, modify net name 5V_SCL_EEP to SCL_EEP modify net name 5V_SDA_EEP to SDA_EEP 7.Page4, modify R56/R40/R41 from 1k to 4.7k 8.Page7, del R103 modify R102 from 8.2k 1% to 3.9k 1% modify R115 from 20 1% to 220 1% 9.Page7, del D2 connetc FB16 pin1 to net 5V_1 10.Page7, del P002, add R116 (0_DNS) to U6 pin7 and GND	LiuKe
	06-13-2007	1, page 3, Add R233 10K ohm to AX_VD33; R215 100 ohm, R216 100 ohm 2, page 4, modify net name JTAG_TDO/E_PAGE to JTAG_TDO;modify R49 4.7K to 4.7K_DNS; Add R54 0 ohm. modify CON5 HEARER_5*2 to P003 5pin_2mm_JST. Add R90 4.7K ohm to AX_VD33; 3, page 7, Del R123 K_DNS; modify Q4\ Q13 MMBT3904 to BC847B. modify C149 to 10UF cap_np Add C151 10UF cap_np modify C149 to 10UF cap_np , modify R116 0_DNS to 0 ohm. Add pull up resistors R133/R134/R135/R136 4.7K ohm	



Trident Digital Media, Inc.
1090 East Arques Ave. Sunnyvale, CA 94085
<http://www.tridentmicro.com>

Title
REVISION HISTORY (1)

Size
B

Schematic Name
LCD312

Rev
C

Date: Tuesday, October 30, 2007Sheet 11 of 13

PRELIMINARY

REV	DATE	DESCRIPTION	SIGNATURE																																																																																																																																																																								
	06-15-2007	GPIO pin name Port Pin No.Toshiba KEYP1_0 183KEY 1. AX 8051Pin Assignment AX Pin Assignment Change List <table><tr><td>GPIO pin name</td><td>Port</td><td>Pin No</td><td>Toshiba</td></tr><tr><td>KEY P1_0</td><td>183</td><td>KEY</td><td></td></tr><tr><td>SENSOR1 P1_1</td><td>184</td><td>AC_DET</td><td></td></tr><tr><td>SENSOR2 P1_2</td><td>185</td><td>LED_TIME</td><td></td></tr><tr><td>FS3 P1_3</td><td>186</td><td>MUTE1</td><td></td></tr><tr><td>FS4 P1_4</td><td>187</td><td>MUTE2</td><td></td></tr><tr><td>AFT P1_5</td><td>188</td><td>AFT</td><td></td></tr><tr><td>PWM0 P1_6</td><td>189</td><td>EED_SCL</td><td></td></tr><tr><td>PWM1 P1_7</td><td>190</td><td>EED_SDA</td><td></td></tr><tr><td>IRIN P3_2</td><td>175</td><td>RMT</td><td></td></tr><tr><td>POWERLOW P3_3</td><td>176</td><td>(Interrupt)</td><td></td></tr><tr><td>PPWR P3_6</td><td>177</td><td>Power_TV</td><td></td></tr><tr><td>PDOWN P3_7</td><td>182</td><td>Power_CPU</td><td></td></tr><tr><td>JTAG_TCK P0_1</td><td>163</td><td>JTAG_TCK/DTV_SW</td><td></td></tr><tr><td>JTAG_TMS P0_2</td><td>165</td><td>JTAG_TMS</td><td></td></tr><tr><td>JTAG_TDI P0_3</td><td>166</td><td>JTAG_TDI</td><td></td></tr><tr><td>JTAG_TDO P0_4</td><td>164</td><td>JTAG_TDO/E_PAGE</td><td></td></tr><tr><td>RXD1 P2_0</td><td>158</td><td>S1</td><td></td></tr><tr><td>TXD1 P2_1</td><td>159</td><td>S_SW</td><td></td></tr><tr><td>MSCL P2_2</td><td>160</td><td>SCL_MST</td><td></td></tr><tr><td>MSDA P2_3</td><td>161</td><td>SDA_MST</td><td></td></tr><tr><td>LED P2_4</td><td>89</td><td>LCD_POWER</td><td></td></tr><tr><td>E_PAGE P2_5</td><td>90</td><td>LED_POWER</td><td></td></tr><tr><td>TUNE2 P2_6</td><td>91</td><td>BKLT_CNTL</td><td></td></tr><tr><td>MUTE P2_7</td><td>92</td><td>BL_CONSW</td><td></td></tr><tr><td>RXD0 P3_0</td><td>155</td><td>RXD0</td><td></td></tr><tr><td>TXD0 P3_1</td><td>156</td><td>TXD0</td><td></td></tr><tr><td>PSYNC P3_4</td><td>157</td><td>HDMI_HOTPLUG_RET</td><td></td></tr><tr><td>PWM2 P3_5</td><td>168</td><td>PROTECT</td><td></td></tr><tr><td>GPIO00 P0_0</td><td>88</td><td>For DAC</td><td></td></tr><tr><td>AUD_WS/DP4 GPIO1</td><td>87</td><td></td><td></td></tr><tr><td>AUD_SD/DP5 GPIO2</td><td>86</td><td></td><td></td></tr><tr><td>AUD_MCLK/DP6 GPIO3</td><td>85</td><td></td><td></td></tr><tr><td>DP3 SVP_GPIO4</td><td>251</td><td>HP_DET_CPU</td><td></td></tr><tr><td>DP4 SVP_GPIO1</td><td>250</td><td>SW1</td><td></td></tr><tr><td>DP5 SVP_GPIO2</td><td>249</td><td>SW2</td><td></td></tr><tr><td>DP6 SVP_GPIO3</td><td>248</td><td>Y_DET</td><td></td></tr><tr><td>TPWM 167</td><td>BL_PWM</td><td></td><td></td></tr><tr><td>SPI_SI 171</td><td>SPI_SI</td><td></td><td></td></tr><tr><td>SPI_SO 172</td><td>SPI_SO</td><td></td><td></td></tr><tr><td>SPI_SCK 173</td><td>SPI_SCK</td><td></td><td></td></tr><tr><td>SPI_CEN 174</td><td>SPI_CEN</td><td></td><td></td></tr></table> 2. add P002 FB48 FB49 3. connetc HDMI port CEC pins, add R123 to GND	GPIO pin name	Port	Pin No	Toshiba	KEY P1_0	183	KEY		SENSOR1 P1_1	184	AC_DET		SENSOR2 P1_2	185	LED_TIME		FS3 P1_3	186	MUTE1		FS4 P1_4	187	MUTE2		AFT P1_5	188	AFT		PWM0 P1_6	189	EED_SCL		PWM1 P1_7	190	EED_SDA		IRIN P3_2	175	RMT		POWERLOW P3_3	176	(Interrupt)		PPWR P3_6	177	Power_TV		PDOWN P3_7	182	Power_CPU		JTAG_TCK P0_1	163	JTAG_TCK/DTV_SW		JTAG_TMS P0_2	165	JTAG_TMS		JTAG_TDI P0_3	166	JTAG_TDI		JTAG_TDO P0_4	164	JTAG_TDO/E_PAGE		RXD1 P2_0	158	S1		TXD1 P2_1	159	S_SW		MSCL P2_2	160	SCL_MST		MSDA P2_3	161	SDA_MST		LED P2_4	89	LCD_POWER		E_PAGE P2_5	90	LED_POWER		TUNE2 P2_6	91	BKLT_CNTL		MUTE P2_7	92	BL_CONSW		RXD0 P3_0	155	RXD0		TXD0 P3_1	156	TXD0		PSYNC P3_4	157	HDMI_HOTPLUG_RET		PWM2 P3_5	168	PROTECT		GPIO00 P0_0	88	For DAC		AUD_WS/DP4 GPIO1	87			AUD_SD/DP5 GPIO2	86			AUD_MCLK/DP6 GPIO3	85			DP3 SVP_GPIO4	251	HP_DET_CPU		DP4 SVP_GPIO1	250	SW1		DP5 SVP_GPIO2	249	SW2		DP6 SVP_GPIO3	248	Y_DET		TPWM 167	BL_PWM			SPI_SI 171	SPI_SI			SPI_SO 172	SPI_SO			SPI_SCK 173	SPI_SCK			SPI_CEN 174	SPI_CEN			LiuKe
GPIO pin name	Port	Pin No	Toshiba																																																																																																																																																																								
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PWM0 P1_6	189	EED_SCL																																																																																																																																																																									
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IRIN P3_2	175	RMT																																																																																																																																																																									
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JTAG_TCK P0_1	163	JTAG_TCK/DTV_SW																																																																																																																																																																									
JTAG_TMS P0_2	165	JTAG_TMS																																																																																																																																																																									
JTAG_TDI P0_3	166	JTAG_TDI																																																																																																																																																																									
JTAG_TDO P0_4	164	JTAG_TDO/E_PAGE																																																																																																																																																																									
RXD1 P2_0	158	S1																																																																																																																																																																									
TXD1 P2_1	159	S_SW																																																																																																																																																																									
MSCL P2_2	160	SCL_MST																																																																																																																																																																									
MSDA P2_3	161	SDA_MST																																																																																																																																																																									
LED P2_4	89	LCD_POWER																																																																																																																																																																									
E_PAGE P2_5	90	LED_POWER																																																																																																																																																																									
TUNE2 P2_6	91	BKLT_CNTL																																																																																																																																																																									
MUTE P2_7	92	BL_CONSW																																																																																																																																																																									
RXD0 P3_0	155	RXD0																																																																																																																																																																									
TXD0 P3_1	156	TXD0																																																																																																																																																																									
PSYNC P3_4	157	HDMI_HOTPLUG_RET																																																																																																																																																																									
PWM2 P3_5	168	PROTECT																																																																																																																																																																									
GPIO00 P0_0	88	For DAC																																																																																																																																																																									
AUD_WS/DP4 GPIO1	87																																																																																																																																																																										
AUD_SD/DP5 GPIO2	86																																																																																																																																																																										
AUD_MCLK/DP6 GPIO3	85																																																																																																																																																																										
DP3 SVP_GPIO4	251	HP_DET_CPU																																																																																																																																																																									
DP4 SVP_GPIO1	250	SW1																																																																																																																																																																									
DP5 SVP_GPIO2	249	SW2																																																																																																																																																																									
DP6 SVP_GPIO3	248	Y_DET																																																																																																																																																																									
TPWM 167	BL_PWM																																																																																																																																																																										
SPI_SI 171	SPI_SI																																																																																																																																																																										
SPI_SO 172	SPI_SO																																																																																																																																																																										
SPI_SCK 173	SPI_SCK																																																																																																																																																																										
SPI_CEN 174	SPI_CEN																																																																																																																																																																										

	06-21-2007	1.PAGE2, add pull-up resistor R233 to net HP_DET_CPU 2.PAGE2, add R215 R216 in SW1 SW2 3.PAGE3, modify R49 to 4.7K_DNS 4.PAGE3, disconnect U2 pin 7 and net JTAG_TDO/E_PAGE and connect to GND via R54 5.PAGE3, modify net name JTAG_TDO/E_PAG to JTAG_TDO 6.PAGE3, modify CON5 to P005 7.PAGE3, modify P005 to single row 5PIN 2mm connector 8.PAGE3, add R90 to net LCD_POWER and net AX_VD33 9.PAGE7, del R123 10.PAGE7, modify Q4 Q5 from MMBT3904 to BC847 11.PAGE7, modify C149 PCB footprint to 0805b_toshiba 12.PAGE7, add C151 to U6 pin8 and GND13 13.PAGE7, modify R116 from 0_DNS to 0 14.PAGE7, add pull-up resistors R133 R134 R135 R136 to net SDADVI_1 SDADVI_2	LiuKe
	07-06-2007	1.PAGE7: modify U9 footprint from SOIC14_155_50 to ssop14_208_26 2.PAGE6: modify part refence U14_dns to U14 3.PAGE6: modify U7 footprint from tqfp100_20 to tqfp80_20 4.PAGE3: modify P001 footprint from TJC3_4_R_TOSHIBA to con_sip4_100_jst	LiuKe
	07-09-2007	1.modify RA1,RA2,RA3,RA4,RN1,RN3,RN4 pcb footprint from ra_exb28v_toshiba to ra_exbv8v	LiuKe
	07-13-2007	1.PAGE3:swap P005 from Pin1 to Pin 4 2.PAGE3:add C63 C64 C65 C66to GND and net KEY_LED_Timer LED_Power S-SW	LiuKe
	07-16-2007	1.PAGE1:modify D6 part type from MTZJ6.2B to UDZS6.2B	LiuKe
	07-19-2007	1.PAGE1:modify D6 part type from UDZS6.2B to PDZ6.2B 2.PAGE3:modify U3 part type from W25X80VSN1 to W25X80VSSI	LiuKe



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Title
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REV	DATE	DESCRIPTION	SIGNATURE
C-00	09-13-2007	1.modify part references U->IC FB->FL 2or1 digit numbers to 3 digit 2.PAGE2: Modify R026 value from 75ohm to 100ohm 3.PAGE4: Delet P001 P005.Remove net interrupt from P002 Pin4.Remove net JTAG_TCK/DTV_SW from P002 Pin6 4.PAGE7: Add P006 87BFN-030R-3F 5.PAGE4:Modify net name JTAG_TCK/DTV_SW to JTAG_TCK 6.PAGE4:Add IC PIC12F508 and the peripheries 7.PAGE4:Add 100ohm R? R? to 3V_SDA_SLV and 3V_SCL_SLV 8.PAGE4 PAGE6:Delet net S1 9.PAGE6:Modify IC007 from PS301 to Ps 321 10.PAGE6:Modify R125 from 4.7k to 100 11.PAGE6:Connetc 3V_SDA_MST to IC007 pin 21,3V_SCL_MST to pin 22 12.PAGE4:Rename net KEY to KEY-1 13.PAGE4:Move net AC_DET from Pin 184 to Pin176(R068) 14.PAGE4:Delet net LED_timer 15.PAGE4:Add net SUB_CLK to Pin 16.PAGE4:Remove net POWER_CPU from AX Pin182 (R070) to PIC12F508 Pin6 17.PAGE4:Add net POWER_SIG to AX Pin182 (R070) 18.PAGE4:Modify C63/C64/C65/C66 from 2200pF to 100pF 19.PAGE6:Delet IC006 and the peripheries 20.PAGE7:Add P006 21.PAGE7:Add ferrite beads to CON3 CON4 P003 pins 22.PAGE7:Reassign PV025 pins 23.PAGE7:Add IC018 and the peripheries 24.PAGE10:Modify IC013 from AP1117E33 to BH33FB1WG-TR 25.PAGE4:Add 100pF capacitors to MCU I/O pins	LiuKe
	09-14-2007	1.PAGE4:Delet C054 C055 R087 R091 2.PAGE4:Add equalization network (C369 C370 R269 R270 R271 R272) to CS4334 audio out	LiuKe
	09-19-2007	1.PAGE1:Modify placement scheme 2.PAGE2:Delet R009,Add 100ohm R005 to net SIF 3.PAGE3:Add inverter ciruit(Q005 Q006) to net SW1 SW2 4.PAGE3:Modify R177 from 4.7K_DNS to 4.7K.Modify R178 from 0 to 0_DNS to 0ohm 5.PAGE3:Add R189 0ohm and R190 4.7K to PJ910 pin 22 6.PAGE4:Delet C345/R243/R245 7.PAGE4:Modigy R256/257 from 47ohm to 100ohm 8.PAGE4:Modigy R250 from 1K to 100ohm 9.PAGE4:Delet FI046 C351-368 10.PAGE4:Modify 100pF capacitors(C063/C063/C048/C049 to 33pF 11.PAGE5:Replace FL013 with 22uH L003 12.PAGE6:Delet R099/R125/R127 13.PAGE6:Modify R102/R115 to1K parallel connect to GND 14.PAGE7:Delet FLFL086 FL070 FL002 FL003 FL048 FL054 FL056 FL058 FL059 FL061 FL057 FL062 FL064 FL065 FL067-083 FL085 FL086 FL088 FL090 FL091 FL093 FL096 15.PAGE7:Add serial resistor R273 R274 R276 R277 16.PAGE7:Replace IC018 with Q027 17.PAGE9:Add serial resistor R278 R279 18.PAGE10:Add FL054 to 9V net 19.PAGE10:Replace IC012 AP1122YL with BH12PBWHFV	LiuKe
	09-18-2007	1.PAGE4:Connect IC017 pin8 to GND 2.PAGE4:Modify parttype from W25X80VSNi to W25X80SSNi	LiuKe

REV	DATE	DESCRIPTION	SIGNATURE
		3.PAGE7:Add R291 R292 R293 R294 to net V2_L and V2_R 4.Change sheet number 8->6 6->7 7->8 5.Update PCB footprints	
	09-25-2007	1.Update PCB footprints	LiuKe
	09-26-2007	1.PAGE9: Add FL016 to PJ920 pin6 and AGND	LiuKe
	09-28-2007	1.PAGE4: Add 20k R295 and R296 pull-up to C369 pin1 and C370 pin1 2.PAGE4:Modify R269 and R271 value from 10k to 20k 3.Update PCB footprints	LiuKe
	10-08-2007	1.Update PCB footprints	LiuKe
	10-22-2007	1.Merge net +5V_2 to 5V_2	LiuKe
	10-23-2007	1.PAGE4:Modify R295/R296 part value from 20k to 47k 2.PAGE4:Modify R269/R271 part value from 20k to 18k	LiuKe
	10-24-2007	1.PAGE4:Modify R250 part value from 100 to 2.2k	LiuKe
	10-25-2007	1.PAGE5:Modify R203 part value from 33 to 0	LiuKe
	10-30-2007	1.PAGE2:Add 1uF coupling capacitors C240/C241/C243/C244/C245/C246/C247 2.PAGE7:Modify R291/R292 part value from 4.7k to 22k	LiuKe
	10-30-2007	1.PAGE2:Modify footprint C240/C241/C242/C243/C244/C245/C246/C247 from 0805 to 0603	LiuKe
	10-30-2007	1.PAGE2:Modify footprint C240/C242/C246/C247 from 0603a_toshiba to 0603b_toshiba	LiuKe
	11-01-2007	1.PAGE4:Add inverter circuit (Q029,R234,R236,R237) to net AC_DET 2.PAGE4:Add inverter circuit (Q030,R235,R238,R239) to net PROTECT	LiuKe
	11-01-2007	1.PAGE4:Delet Q029,Q030,R234,R237,R235,R239 2.PAGE4:Modify part value R236 from 10k to 2.2k 3.PAGE4:Modify part value R238 from 10k to 4.7k	LiuKe
	11-19-2007	1.PAGE4:Modify R295/R296 part value from 47k to 82k 2.PAGE4:Modify R269/R271 part value from 18k to 15k 3.PAGE7:Modify R221/R222 part value from 4.7k to 1k	LiuKe



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