

1.5V Drive Pch MOSFET

RAF040P01

Structure

Silicon P-channel MOSFET

Features

- 1) Low on-resistance.
- 2) Low voltage drive(1.5V drive).
- 3) Small surface mount package(TUMT3).

Application

Switching

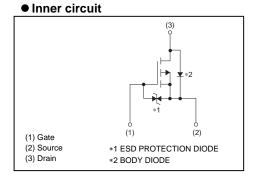
Packaging specifications

	Package	Taping
Type	Code	TCL
	Basic ordering unit (pieces)	3000
RAF040P0	0	

SOT-323T

• Dimensions (Unit : mm)

0.65 0.65



Abbreviated symbol : SF

● Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit
Drain-source voltage		V_{DSS}	-12	V
Gate-source voltage		V_{GSS}	0 to -8	V
Drain current	Continuous	I _D	±4	Α
	Pulsed	I _{DP} *1	±16	Α
Source current	Continuous	Is	-0.6	Α
(Body Diode)	Pulsed	I _{SP} *1	-16	Α
Power dissipation		P _D *2	0.8	W
Channel temperature		Tch	150	°C
Range of storage temperature		Tstg	-55 to +150	°C

^{*1} Pw \leq 10 μ s, Duty cycle \leq 1%

• Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to Ambient	Rth (ch-a)*	156	°C / W

^{*}Mounted on a ceramic board.

^{*2} Mounted on a ceramic board.

● Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	I_{GSS}	-	-	-10	μA	$V_{GS}=-8V, V_{DS}=0V$
Drain-source breakdown voltage	$V_{(BR)DSS}$	-12	-	-	٧	$I_D=-1$ mA, $V_{GS}=0$ V
Zero gate voltage drain current	I _{DSS}	1	-	-10	μA	V_{DS} =-12V, V_{GS} =0V
Gate threshold voltage	V _{GS (th)}	-0.3	-	-1.0	٧	$V_{DS}=-6V$, $I_{D}=-1mA$
		•	22	30	mΩ	I _D =-4A, V _{GS} =-4.5V
Static drain-source on-state	P	•	27	38		I _D =-2A, V _{GS} =-2.5V
resistance	R _{DS (on)}	•	35	53	11152	I _D =-2A, V _{GS} =-1.8V
		•	40	68		$I_D = -0.8A, V_{GS} = -1.5V$
Forward transfer admittance	IY _{fs} I*	5.5	-	-	S	V_{DS} =-6V, I_{D} =-4A
Input capacitance	C _{iss}	•	4000	-	pF	V _{DS} =-6V
Output capacitance	C _{oss}	1	410	-	pF	V _{GS} =0V
Reverse transfer capacitance	C_{rss}	-	400	-	pF	f=1MHz
Turn-on delay time	t _{d(on)} *	-	15	-	ns	$V_{DD} = -6V, I_D = -2A$
Rise time	t _r *	-	43	-	ns	V _{GS} =-4.5V
Turn-off delay time	t _{d(off)} *	-	240	-	ns	$R_L=3\Omega$
Fall time	t _f *	-	120	-	ns	$R_G=10\Omega$
Total gate charge	Q _g *	-	37	-	nC	V _{DD} ≒-6V, I _D =-4A
Gate-source charge	Q _{gs} *	-	6.0	-	nC	V _{GS} =-4.5V
Gate-drain charge	Q _{gd} *	-	5.5	-	nC	

^{*}Pulsed

●Body diode characteristics (Source-Drain)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward Voltage	V _{SD} *	-	-	-1.2	V	$I_s=-4A$, $V_{GS}=0V$

^{*}Pulsed

●Electrical characteristic curves (Ta=25°C)

Fig.1 Typical Output Characteristics (I)

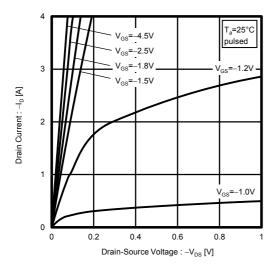


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

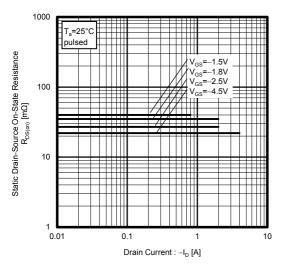


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

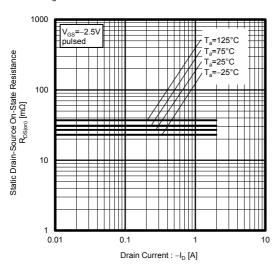


Fig.2 Typical Output Characteristics (II)

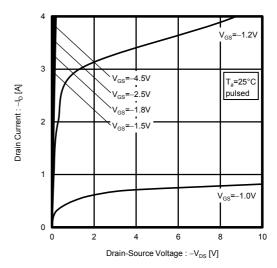


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current

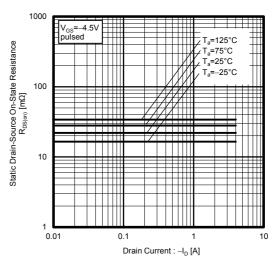


Fig.6 Static Drain-Source On-State Resistance vs. Drain Current

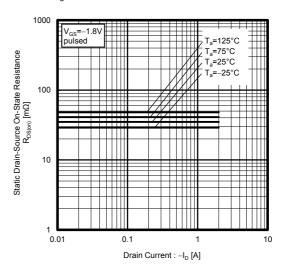


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current

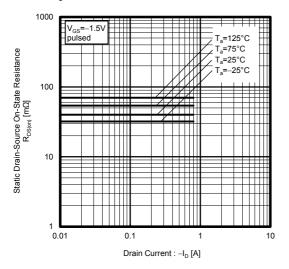


Fig.9 Typical Transfer Characteristics

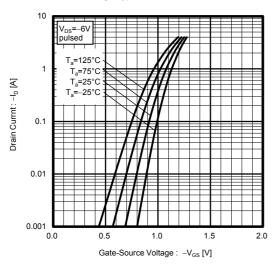


Fig.11 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

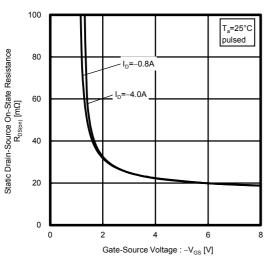


Fig.8 Forward Transfer Admittance vs. Drain Current

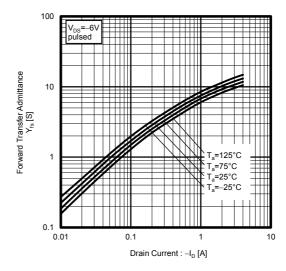


Fig.10 Source Current vs. Source-Drain Voltage

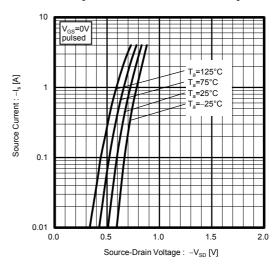
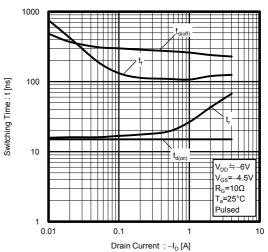


Fig.12 Switching Characteristics



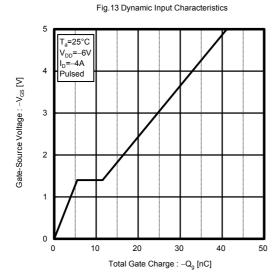
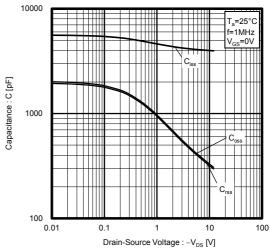


Fig.14 Typical Capacitance vs. Drain-Source Voltage



Measurement circuits

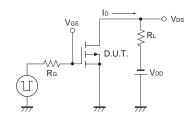


Fig.1-1 Switching Time Measurement Circuit

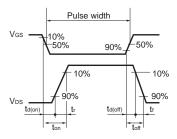


Fig.1-2 Switching Waveforms

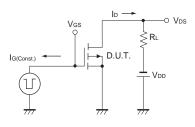


Fig.2-1 Gate Charge Measurement Circuit

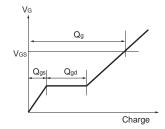


Fig.2-2 Gate Charge Waveform

Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

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